

4	1.1.1	Beamline Conceptual Design
5	1.1.2	Beamline Review
6	1.1.3	Frascati's Refurbishment
7	1.1.4	Frascati beampipe 1
8	1.1.5	Beam Pipe to SVT
9	1.1.6	SVT Vacuum Box
10	1.1.7	Electron Target
11	1.1.8	Ecal Vacuum Chamber Welding
12	1.1.9	Restore controls and diagnostics
13	1.1.10	SVT Collimator Protection
14	1.1.11	Vacuum beam line downstream of chicane
15	1.1.12	BOM and link to FS
16	1.1.13	Complete single piece vacuum chamber
17	1.1.14	Frascati Beampipe 2
18	1.1.15	Beam Offset Monitor
19	1.1.16	Beam Profile Monitor
20	1.1.17	Cavity BPM
21	1.1.18	Photon Dump
22	1.1.19	Shielding
23	1.1.20	Beamline Installation
24	1.1.21	Beamline Installed
25	1.2	SVT
26	1.2.1	SVT returns from Jlab
27	1.2.2	SVT Engineering Design Review
28	1.2.3	Layers 1-3
29	1.2.4	Layers 4-6
30	1.2.5	C-Support
31	1.2.6	DAQ Support/Cooling
32	1.2.7	Baseplate
33	1.2.8	Motion Levers
34	1.2.9	Vacuum Flanges
35	1.2.10	Shipping Crates
36	1.2.11	Integration and testing at SLAC
37	1.2.12	SVT Shipped at JLAB
38	1.2.13	Integration at Jlab
39	1.2.14	SVT Ready For Installation
40	1.3	SVT DAQ
41	1.3.1	FE Boards
42	1.3.2	Flange
43	1.3.3	DAQ
44	1.3.4	Hybrid
45	1.3.5	Flex Cable
46	1.4	ECAL
47	1.4.1	Ecal Enclosure
48	1.4.2	Fix 2-shorted HV groups on top-left
49	1.4.3	Order new bottom motherboard
50	1.4.4	Install New motherboards
51	1.4.5	Replacement pre-amplifier boards
52	1.4.6	Low voltage PS
53	1.4.7	Delay Cables
54	1.4.8	Light Monitoring System
55	1.4.9	Assemble Ecal
56	1.4.10	Test and Calibrate Ecal
57	1.4.11	New mounting system
58	1.4.12	ECAL Ready for the installation
59	1.5	Muon
60	1.5.1	Muon Engineering design review
61	1.5.2	PMT
62	1.5.3	Muon Vacuum Chamber
63	1.5.4	Stand
64	1.5.5	Absorbers
65	1.5.6	Hodoscope Support
66	1.5.7	Cable HV
67	1.5.8	Cable Signal
68	1.5.9	Assembly
69	1.5.10	Testing
70	1.5.11	Muon Ready for installation
71	1.6	TDAQ
72	1.6.1	EPICS information into data stream
73	1.6.2	Event header with appropriate inf
74	1.6.3	Readout of fADC scalers in EPICS
75	1.6.4	Rate monitoring with discriminator scalrs
76	1.6.5	Event display
77	1.6.6	Monitoring/histogramin
78	1.6.7	ECAL+Muon FDAC
79	1.6.8	Trigger Interface
80	1.6.9	Signal distribution
81	1.6.10	Crate Trigger Processor
82	1.6.11	Sub-System Processor
83	1.6.12	Global Trigger Processor
84	1.6.13	Trigger Distribution
85	1.6.14	Trigger Supervisor
86	1.6.15	Update firmware on FADC250
87	1.6.16	New firmware for cluster finding
88	1.6.17	New firmware for SSP
89	1.6.18	Trigger monitoring tools
90	1.7	Slow Control
91	1.7.1	Chiller control programming
92	1.7.2	I/O modules for control
93	1.7.3	Sensor readout programming
94	1.7.4	SVT voltage control
95	1.7.5	ECAL voltage control
96	1.7.6	Muon detector voltage control
97	1.7.7	SVT motor controls
98	1.7.8	Target motor control
99	1.7.9	Software interlock
100	1.7.10	Hardware interlocks
101	1.7.11	Beamline control screens
102	1.7.12	EPICS Scalers programing
103	1.7.13	Alarms configuration
104	1.7.14	Archiving of controls
105	1.8	Installation & Commissioning
106	1.8.1	Installation review
107	1.8.2	SVT I&C
108	1.8.3	ECAL I&C
109	1.8.4	Muon I&C
110	1.8.5	TDAQ I&C
111	1.8.6	Slow Control I&C
112	1.8.7	HPS ready for the beam
113	1.9	Electron Running
114	1.9.1	Beamline Commissioning (2014)
115	1.9.2	Commissioning Run (2014)
116	1.9.5	Run 2.2GeV (2014)
117	1.9.6	Run 1.1 GeV (2014)
118	1.9.3	Beamline recommissioning (2015)
119	1.9.4	Data Run (2015)
120	1.10	SLAC Travel
121	1.10.2	Collaboration Meeting May 2013
122	1.10.3	Consultation 1 at JLAB
123	1.10.4	Collaboration Meeting November 2013
124	1.10.5	Collaboration Meeting May 2014
125	1.10.9	Consultation 2 at JLAB
126	1.10.7	Collaboration Meeting November 2014
127	1.10.8	Installation&Commissioning
128	1.11	SLAC Travel for Commissioning& Run Shifts
129	1.11.1	Commissioning (2014)
130	1.11.2	Run 2.2GeV (2014)

