MEET DOSHI

Software Development Engineer

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EDUCATION

B.Tech in Electronics

K.J Somaiya College of Engineering CGPA - 8.15 upto Sem V

2018 - 2022

EXPERIENCE

Internship

Image Classification

♥ KJSCE

• Implemented various Machine Learning Algorithms for a classification of images problem.

Internship

Audio Recognition

August 2019

♀ KJSCE

 Designed an algorithm to identify songs by just a few seconds of lyrics.

Internship

Arduino Robot Design

♥ KJSCE

• Built a functioning fence following robot with automatic speed and distance control.

PROJECTS

Face Mask Detection

♥ KJSCE

 Designed a CNN using Tensorflow to be deployed on an Raspberry Pi server which gave live classification on face mask detection. A static website was also created using HTML, CSS, JavaScript, Flask which gave live feed over the camera detection.

Music Notes Extraction

♥ KJSCE

• Wrote an algorithm on MATLAB to parse an audio file and determine its music notes which can help musicians to determine which notes are being played.

Zap iOS App Game

Movember 2020

♥ KJSCE

• Implemented and iOS based app on XCode which used Firebase authentication and Firestore database. App also stored user profiles, scores and friends section.

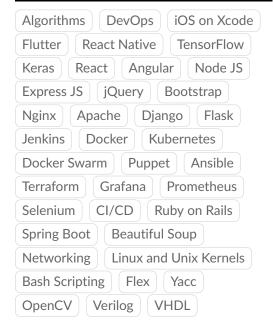
Quiz iOS App

Cotober 2020

♥ KJSCE

• iOS app written in Swift and built on XCode which had prebuilt questionnaire and evaluated user results and stored it on Firestore database.

SKILLS



BACKEND

MySQL	SQLite PostgreSQL			
NoSQL	MongoDB MariaDB			
influxdb	GraphQL Git		Firestore	
Heroku	AWS Azure IBM Cloud			
Oracle Cloud Digital Ocean				

LANGUAGES

C/C++	Java	Python	Swift		
Dart	Ruby	MATLAB	HTML		
CSS	JavaScrip	ot SASS	PHPR		
GoLang Typescript					

ACHIEVEMENTS

- Secured GATE Computer Science 2021 AIR 113 in Third year of Undergrad
- Department topper for Semester V with 9.95 SGPA

PROJECTS

Microprocessor Design

♀ KJSCE

• Designed an 8 bit Microprocessor which can run x86 commands with dual edge triggering to increase CPI and throughput. It was written in Verilog and implemented on FPGA for testing.