Design Rules Verification Report

Filename : C:\Users\mmeagher\Documents\Projects\RomeoZeroGenII\7404337_RZ_G2\7404

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=10mil) (InNamedPolygon('GND_L02_P002')),(All)	0
Clearance Constraint (Gap=10mil) (All),(All)	0
Clearance Constraint (Gap=10mil) (InNamedPolygon('P3V_L03_P005')),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Width Constraint (Min=6mil) (Max=10mil) (Preferred=6mil) (All)	0
Width Constraint (Min=8mil) (Max=10mil) (Preferred=10mil) (InNet('P3V'))	0
Width Constraint (Min=8mil) (Max=30mil) (Preferred=10mil) (InNet('GND'))	0
Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=3mil) (HasFootprint('VQFN-20 (3MMX3MM)')),(All)	0
Silk to Silk (Clearance=10mil) (All),(All)	0
Component Clearance Constraint (Horizontal Gap = 10mil, Vertical Gap = 10mil) (Disabled)(All),(All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Prefered=500mil) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
Warning	7404337_RZ_G2.SchDoc	Off grid Pin J2-3 at 1849.537mil,5103.987mil
Warning	7404337_RZ_G2.SchDoc	Off grid Pin J2-4 at 1853.661mil,5002.953mil

No differences were detected.