# Meghana Moorthy Bhat

https://www.linkedin.com/in/meghana-moorthy-bhat-7b23b337 mbhat2@wisc.edu | 949.750.7299

## **EDUCATION**

# UNIVERSITY OF WISCONSIN MADISON

MATERS IN COMPUTER SCIENCE Expected May 2019 | Madison, WI Cum. GPA: 3.5

#### **B.ENG IN COMPUTER SCIENCE**

June 2012 | Mysore, India Conc. in Computer Science and Engineering Sri Jayachamarajendra College of Engineering Cum. GPA: 9.21 / 10.0

## LINKS

LinkedIn://meghanamoorthy GitHub://meghu2791

## **COURSEWORK**

#### **GRADUATE**

Fall 2017: • Machine Learning models for Data Management • Data Science • Artificial Intelligence • Algorithms Fall 2018: • Machine Learning • Data Visualization • Stochastic Programming • Big Data • Cognitive Science

# **OBJECTIVE**

Seeking full-time opportunities for 2019 in software development and data analysis.

## **SKILLS**

#### **PROGRAMMING**

Java • C • C++ • Python • SQL • Android Tools and Technologies: Visual Studio • Eclipse • LaTex • MATLAB • GDB • MySQL • Git • Jira • Agile methodology

# **PUBLICATIONS**

- Meghana Moorthy B, Melwyn Scudder, Dr Josef Eckmueller, "Performance Optimization for Virtual Platforms" in the Intel world-wide annual conference DTTC, Portland, Oregon, USA, August 2015.
- Meghana Moorthy B, Melwyn Scudder, Kartik Shah, "Virtual Prototype Quality Improvement Methodologies" in DvCon India, Bangalore, India, September 2015

#### **EXPERIENCE**

#### QUALCOMM RESEARCH | Applied Deep Learning Intern

June 2018 - Aug 2018 | San Diego, CA

- Languages: C++, Python
- Worked on Fixed Point math enablement in Snapdragon Neural Processor Engine used by Snapdragon powered mobile applications.
- Implemented few layer operations like ChannelShuffle, CMRN, Elementwise Operations in Deep Learning Frameworks.
- All code was reviewed, perfected, and pushed to production.

#### INTEL | SOFTWARE ENGINEER I/II

April 2012 - July 2017 | Bangalore, India

- Languages: C++, Python
- Worked as lead on a project titled Virtual Prototyping (VP) Performance Optimization for detecting bottlenecks in platform design.
- First-hand support for customers (Apple, inhouse) for debugging issues at component level during bring-up.
- Automated the entire generation of projects along with testbench framework at various levels using Python.

# RESEARCH AND PROJECTS

#### **REAKTIVE LAB** | GRADUATE RESEARCH ASSISTANT

October 2017 - Present | Madison, WI

Working with Prof. Theodoros Rekatsinas for exploring various machine learning techniques for accurate and efficient data cleaning methods, learning Functional dependencies and constraints through datasets. Publication under submission.

# ERROR DETECTION AND CORRECTION USING DEEP LEARNING Mar 2018 - May 2018 | Madison, WI

Explored deep learning models for strucutred data with respect to error detection and correction. Used LSTMs, GRU, Facebook FastText, word2vec for word embeddings. Github link Word Embeddings.

# ENTITY MATCHING USING MACHINE LEARNING AND DEEP LEARNING April 2018 – May 2018 | Madison, WI

Performed entity macthing based on paper Magellan using Machine Learning techniques. Also evaluated the next paper on entity matching using Deep Learning and benchmarked the results based on performance and accuracy. Github link Entity Matching.

# **AWARDS**

- 2018 top 10/260 in Qualcomm Hackathon
- 2016 Project Secureware secured 3/72 in Intel Innovation Group Chetana
- 2014 Intel Department Recognition award
- 2013 Intel Employee Recognition award

# OTHER INTERESTS

- 2014 National Recepient of Sangeetha Kalamani title
- 2010 Winner of All India Radio Singing Competition
- 2001 Been performing live music concerts globally (300+ concerts)