DHAKA UNIVERSITY OF ENGINEERING & TECHNOLOGY, GAZIPUR-1707 DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING



Course No: CSE 3812

Course Title: Microprocessor and Interfacing Sessional

<u>Lab 5</u>

Report Name: Logic, Shift and Rotate Instructions & Multiplication and Division Instructions in EMU8086.

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Year/Semester: 3rd Year/1st Semester

Section: B

Objectives:

To understanding Logic, Shift and Rotate Instructions & Multiplication and Division Instructions in EMU8086.

Problem Statements:

1. Write a program to multiply AX by 27 using only Shift and Add instructions. You should not use the MUL instruction.

Recall that shifting left n bits multiplies the operand by 2^n .

If the multiplier is not an absolute power of 2,

then express the multiplier as a sum of terms which are absolute powers of 2.

For example, multiply AX by 7. $(7 = 4 + 2 + 1 = 2^2 + 2^1 + 1)$

Answer = AX shifted left by 2 + AX shifted left by 1 + AX.

Note: Only the original value of AX is used in each operation above.

2. Write a program to divide AX by 8 using Shift instructions. You should not use the DIV instruction. Assume AX is a multiple of 8.

Recall that shifting right n bits divides the operand by 2^n .

3. Write a program to check if a byte is a Palindrome. [Hint: Use Rotate instructions]. If the byte is a Palindrome, then move AAh into BL. Otherwise move 00h in BL.

A Palindrome looks the same when seen from the left or the right.

For example, 11011011 is a Palindrome but 11010011 is not a Palindrome

4. Write a program to display the bits of a register or memory location. Use the INT 21H interrupts to display data on the display monitor. [Hint: Use logical shift instruction to move data bit into the carry flag]

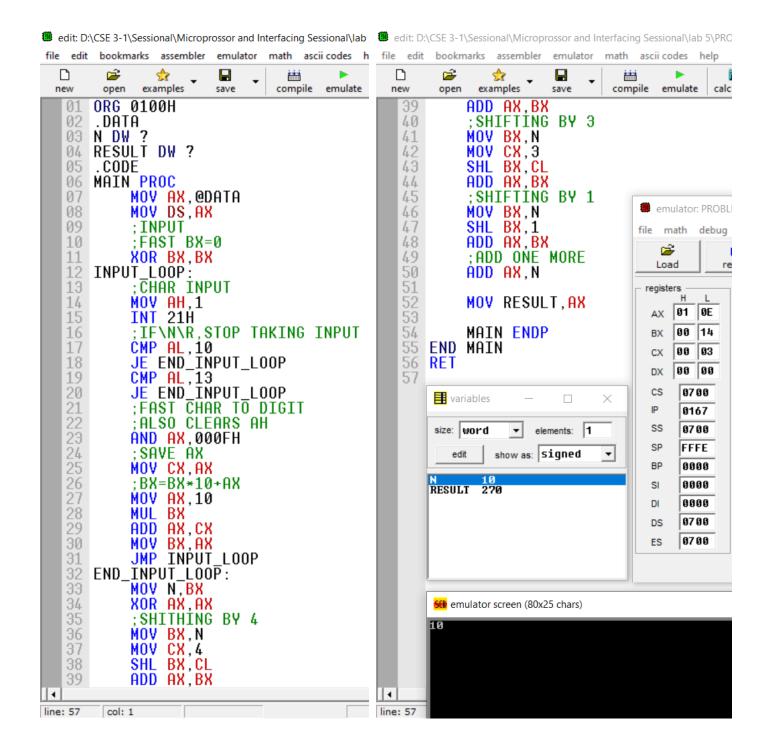
For example, if AL = 55H, then your program must display:

$$AL = 01010101$$

- 5. Write assembly code for each of the following high-level language assignment statements. Suppose that A, B, and C are word variables and all products will fit in 16 bits. Use IMUL for multiplication. It's not necessary to preserve the contents of variables A, B, and C.
 - a. $A = 5 \times A 7$
 - b. $B = (A-B) \times (B-10)$

Problem 1: ORG 0100H .DATA NDW? **RESULT DW?** .CODE MAIN PROC MOV AX,@DATA MOV DS,AX ;INPUT ;FAST BX=0 XOR BX,BX INPUT_LOOP: ;CHAR INPUT MOV AH,1 INT 21H ;IF\N\R,STOP TAKING INPUT CMP AL,10 JE END_INPUT_LOOP CMP AL,13 JE END_INPUT_LOOP ;FAST CHAR TO DIGIT ;ALSO CLEARS AH AND AX,000FH ;SAVE AX MOV CX,AX ;BX=BX*10+AX MOV AX,10 **MUL BX** ADD AX,CX MOV BX,AX JMP INPUT_LOOP

END_INPUT_LOOP:
MOV N,BX
XOR AX,AX
;SHITHING BY 4
MOV BX,N
MOV CX,4
SHL BX,CL
ADD AX,BX
;SHIFTING BY 3
MOV BX,N
MOV CX,3
SHL BX,CL
ADD AX,BX
;SHIFTING BY 1
MOV BX,N
SHL BX,1
ADD AX,BX
;ADD ONE MORE
ADD AX,N
MOV RESULT,AX
MAIN ENDP
END MAIN
RET



Problem 2:

ORG 0100H

.DATA

NDW?

RESULT DW?

.CODE

MAIN PROC

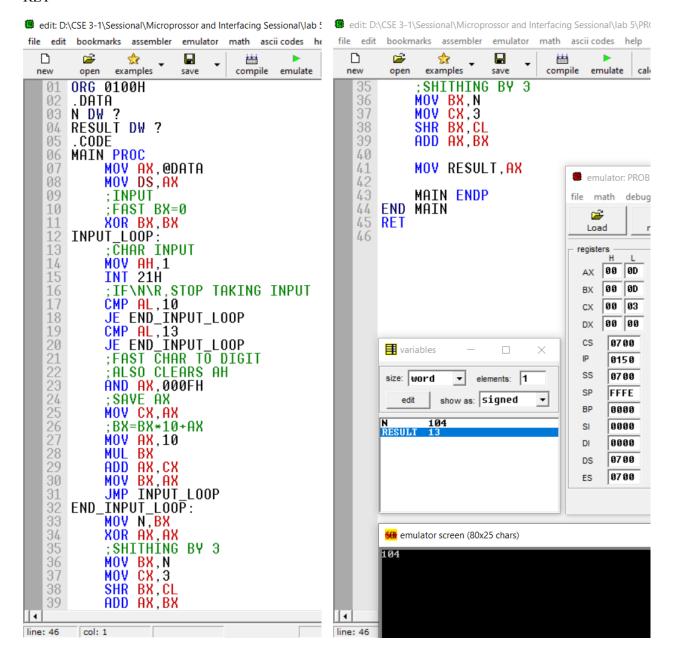
MOV AX,@DATA

```
MOV DS,AX
  ;INPUT
  ;FAST BX=0
  XOR BX,BX
INPUT LOOP:
  ;CHAR INPUT
 MOV AH,1
 INT 21H
 ;IF\N\R,STOP TAKING INPUT
 CMP AL,10
 JE END INPUT LOOP
 CMP AL,13
  JE END INPUT LOOP
  ;FAST CHAR TO DIGIT
  ;ALSO CLEARS AH
 AND AX,000FH
  ;SAVE AX
 MOV CX,AX
  ;BX=BX*10+AX
 MOV AX,10
 MUL BX
 ADD AX,CX
 MOV BX,AX
 JMP INPUT_LOOP
END_INPUT_LOOP:
 MOV N,BX
 XOR AX,AX
  ;SHITHING BY 3
 MOV BX,N
  MOV CX,3
  SHR BX,CL
 ADD AX,BX
```

MAIN ENDP

END MAIN

RET



Problem 3:

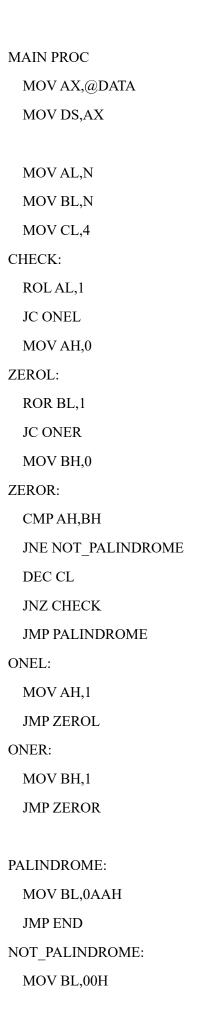
ORG 0100H

.DATA

N DB 11011011B

RESULT DB?

.CODE



END:

MAIN ENDP

END MAIN

RET

```
edit: D:\CSE 3-1\Sessional\Microprossor and Interfacing Sessional\lab 5\PROE
file edit bookmarks assembler emulator math ascii codes help
  🚅
                                            •
                 ☆
                                     examples
                                          emulate
  new
         open
                                   compile
                                                  calcu
       ORG 0100H
        . DATA
   03 N DB 11011011B
       RESULT DB ?
   04
   05
        . CODE
       MAIN PROC
                                        emulator: PROBL
   07
             MOV AX, @DATA
                                           math debug
   08
             MOV DS, AX
                                           <u>a</u>
   09
             MOV AL, N
   10
             MOV BL,N
                                          Load
                                                    re
             MOV CL, 4
                                         registers
        CHECK:
   13
             ROL AL,1
                                            01 BD
                                          AX
             JC ONEL
   14
                                         ВХ
                                             01
                                                AA
             MOV AH,0
   15
                                             00
                                                00
                                         CX
   16
       ZEROL:
   17
             ROR BL,1
                                         DX
                                             00 00
   18
             JC ONEŘ
                                         CS
                                              0700
             MOV BH, 0
   19
                                         IP
                                              0149
   20
21
22
23
24
       ZEROR:
                                          SS
                                              0700
             CMP AH.BH
             JNE NOT_PALINDROME
                                         SP
                                              FFFE
             DEC CL
                                              0000
                                         BP
             JNZ CHECK
                                              0000
                                         SI
             JMP PALINDROME
   26
27
28
29
                                              0000
                                         DI
       ONEL:
             MOV AH,1
                                              0700
                                         DS
             JMP ZEROL
                                              0700
                                         ES
       ONER:
             MOV BH,1
JMP ZEROR
   30
   32
33
       PALINDROME:
             MOV BL, OAAH
JMP END
       NOT_PALINDROME:
             MOV BL,00H
   36
   37
       END:
             MAIN ENDP
   39 END MAIN
line: 41 col: 1
```

Problem 4:

ORG 0100H

.DATA

MDW'AL =\$'

.CODE

MAIN PROC

MOV AX,@DATA

MOV DS,AX

;MSG

MOV AH,9

LEA DX,M

INT 21H

MOV CL,8

MOV AL,55H

MOV BL,AL

AGAIN:

ROL BL,1

JC ONE

MOV DL,'0'

JMP DISPLAY

ONE:

MOV DL,'1'

DISPLAY:

MOV AH,2

INT 21H

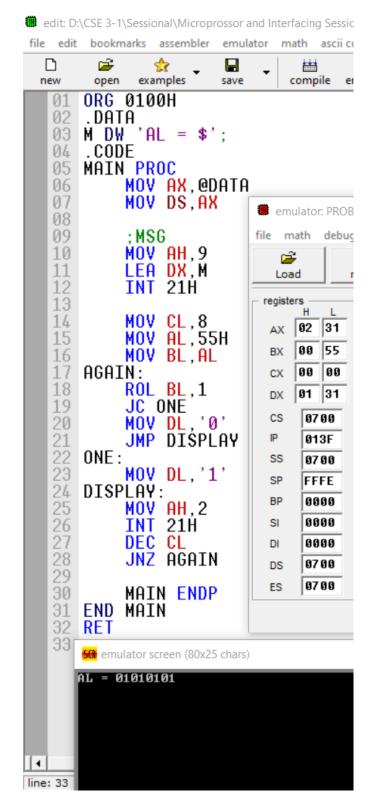
DEC CL

JNZ AGAIN

MAIN ENDP

END MAIN

RET



Problem 5:

ORG 0100H

.DATA

ADW?

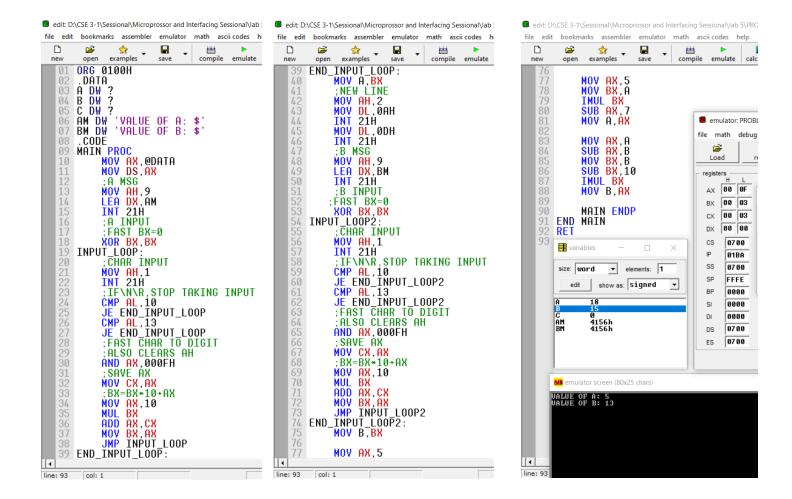
BDW?

CDW?

AM DW 'VALUE OF A: \$' BM DW 'VALUE OF B: \$' .CODE MAIN PROC MOV AX,@DATA MOV DS,AX ;A MSG MOV AH,9 LEA DX,AM INT 21H ;A INPUT ;FAST BX=0 XOR BX,BX INPUT_LOOP: ;CHAR INPUT MOV AH,1 INT 21H ;IF\N\R,STOP TAKING INPUT CMP AL,10 JE END_INPUT_LOOP CMP AL,13 JE END_INPUT_LOOP ;FAST CHAR TO DIGIT ;ALSO CLEARS AH AND AX,000FH ;SAVE AX MOV CX,AX ;BX=BX*10+AXMOV AX,10 MUL BX ADD AX,CX MOV BX,AX JMP INPUT_LOOP

```
END_INPUT_LOOP:
 MOV A,BX
 ;NEW LINE
 MOV AH,2
 MOV DL,0AH
 INT 21H
 MOV DL,0DH
 INT 21H
 ;B MSG
 MOV AH,9
 LEA DX,BM
 INT 21H
 ;B INPUT
 ;FAST BX=0
 XOR BX,BX
INPUT_LOOP2:
 ;CHAR INPUT
 MOV AH,1
 INT 21H
 ;IF\N\R,STOP TAKING INPUT
 CMP AL,10
 JE END_INPUT_LOOP2
 CMP AL,13
 JE END_INPUT_LOOP2
 ;FAST CHAR TO DIGIT
 ;ALSO CLEARS AH
 AND AX,000FH
 ;SAVE AX
 MOV CX,AX
 ;BX=BX*10+AX
 MOV AX,10
 MUL BX
 ADD AX,CX
```

MOV BX,AX JMP INPUT_LOOP2 END_INPUT_LOOP2: MOV B,BX MOV AX,5 MOV BX,A IMUL BX SUB AX,7 MOV A,AX MOV AX,A SUB AX,B MOV BX,B SUB BX,10 IMUL BX MOV B,AX MAIN ENDP END MAIN RET



Discussion:

Finally it can be said that, we learned about so many new instruction called Logic, Shift and Rotate Instructions & Multiplication and Division Instructions. By using those instruction, we are able solve new logical problem like Palindrome Check or Byte Display etc.