

High-Performance Computing

MNIST Digit Classification



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# High-Performance MNIST Digit Classification Using CUDA-Accelerated Neural Networks

# Introduction

This report presents a comprehensive study of accelerating a simple feedforward neural network using CUDA for classifying handwritten digits from the MNIST dataset. The project progresses through four distinct versions, beginning with a CPU-based implementation and culminating in a high-throughput, batch-parallel CUDA version leveraging advanced GPU capabilities such as asynchronous memory transfers, streams, and shared memory.

The objective is to demonstrate the performance gains and efficiency achievable through GPU parallelism, memory hierarchy optimization, and fine-grained kernel engineering—core principles in High Performance Computing (HPC).

## Objective

The main goal of this project is to build a neural network that can quickly and correctly classify MNIST digits using the GPU. Each version of the code adds new improvements to make the network faster and more accurate. Important techniques used include parallel kernels, better memory usage, training in batches, and running work at the same time using CUDA streams.

## Dataset Description

The MNIST dataset has 60,000 training images and 10,000 test images. Each image is a gray 28x28 picture of a digit (0 to 9). The images are turned into 784-length vectors (28 × 28 = 784). This dataset is a common choice for testing image classification models.

We use the MNIST dataset, a well-established benchmark for image classification tasks in deep learning. It contains:

* **60,000 training images**
* **10,000 test images**
* Each image: 28×28 grayscale pixels → **784-dimensional input vector**
* Labels: Integers from 0 to 9

This dataset's simplicity and uniform structure make it ideal for experimenting with performance optimizations on GPU.

## Neural Network Architecture

The neural network used in this project has three layers:

* **Input layer:** 784 neurons (one for each pixel in the image)
* **Hidden layer:** 128 neurons with ReLU activation
* **Output layer:** 10 neurons with softmax activation (one for each digit)

We use cross-entropy as the loss function and train the network using Stochastic Gradient Descent (SGD).

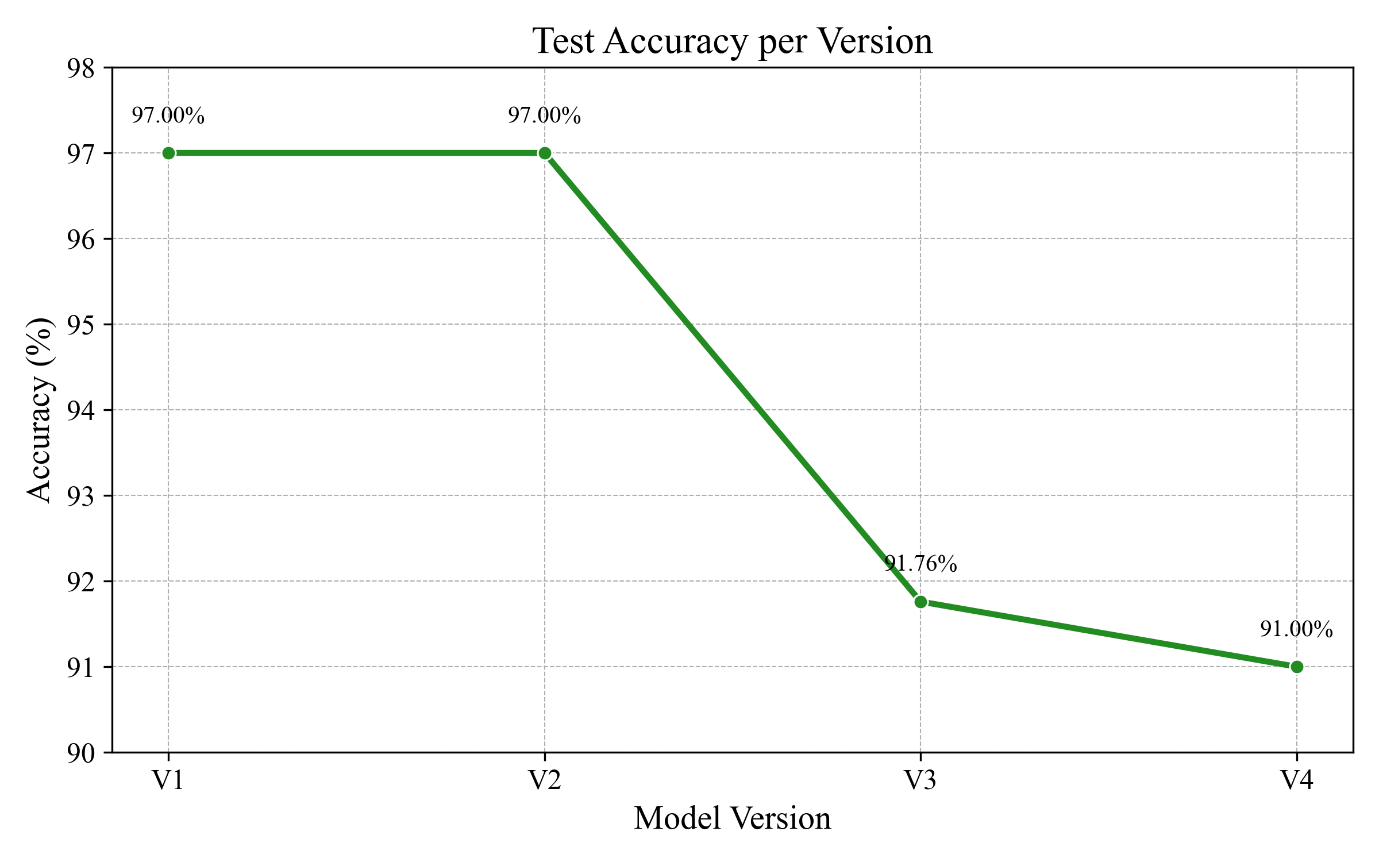
## Implementation Details

**🔧 Techniques Employed:**

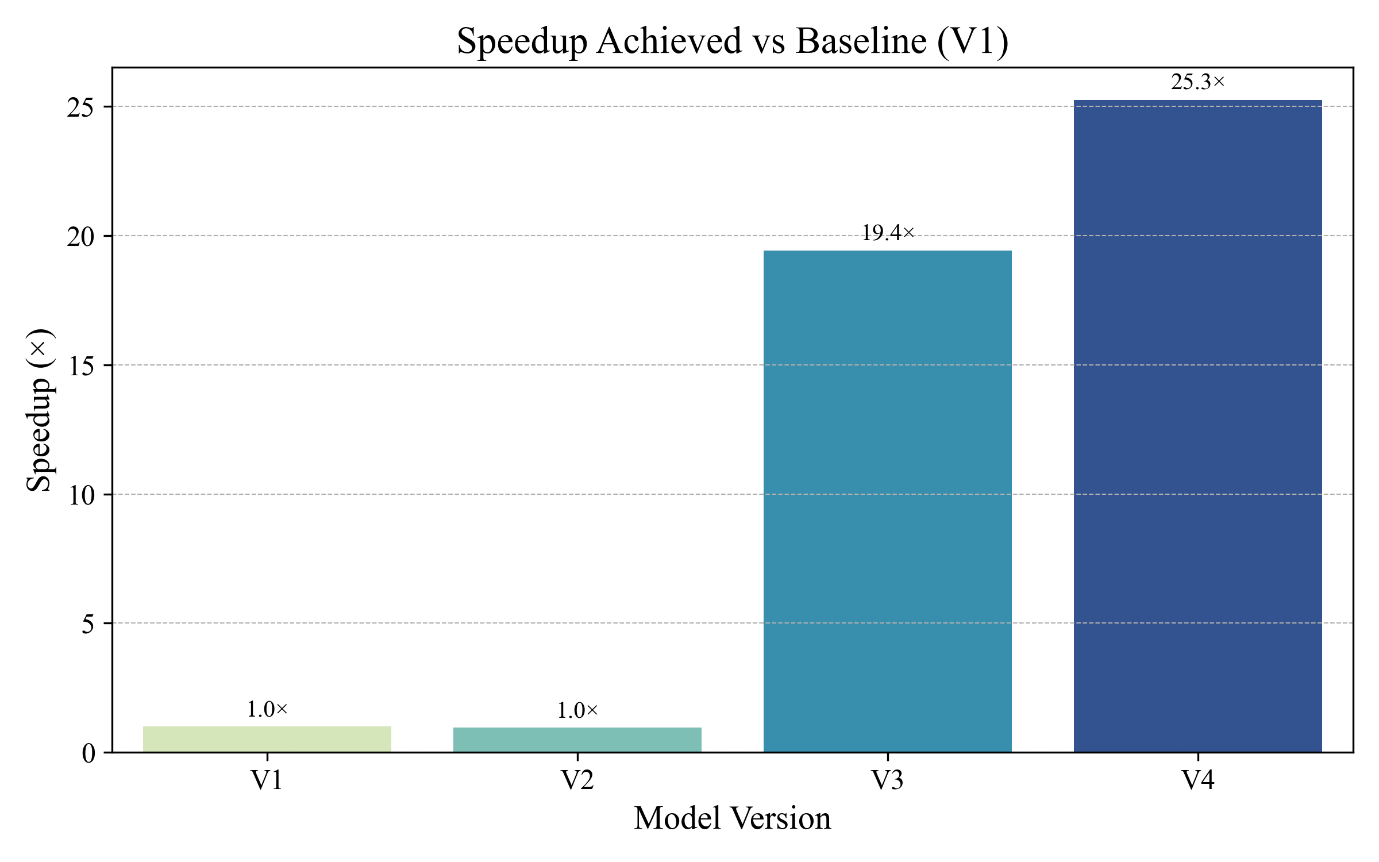
* **Custom CUDA Kernels** for all layers (forward & backward passes)
* **Memory Allocation via cudaMalloc**, managed per batch to avoid contention
* **Pinned Host Memory** using cudaHostAlloc to accelerate data transfers
* **Asynchronous Transfers** with cudaMemcpyAsync for overlap with kernel execution
* **CUDA Streams** to execute multiple training batches in parallel
* **Shared Memory** inside kernels for fast access to frequently used values
* **Random Weight Initialization** moved from CPU to GPU in later versions
* **Mini-Batch Training** to exploit SIMD and thread-block parallelism.

## Time:

## Accuracy:

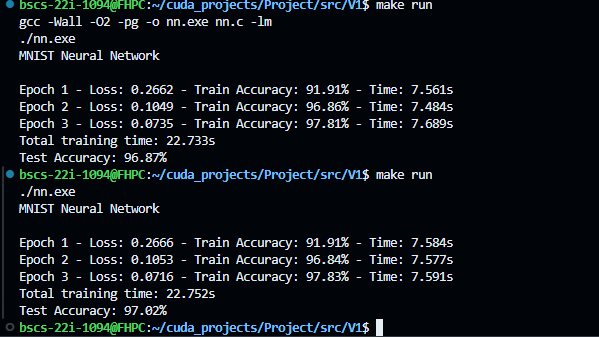


## Speed Up:



### V1 Summary

V1 is the starting version. It runs completely on the CPU and uses simple loops for forward pass, backpropagation, and weight updates. It is correct but very slow. We use this version to compare the speed and accuracy of later versions.

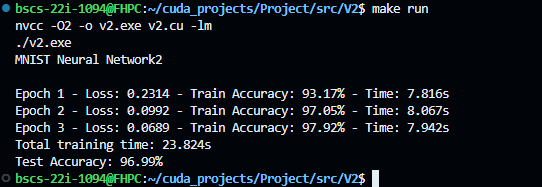


### V2 Description + Code Explanation

In V2, we use CUDA to speed up the forward pass. We run the hidden layer on the GPU. Each thread calculates one neuron's output in the hidden layer. This shows how GPU threads can be used for parallel processing.

Main changes:

* Forward pass done using a CUDA kernel
* GPU memory used for weights and activations
* Final result still copied back to CPU for output



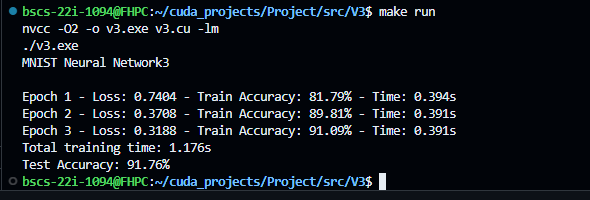
### V3 Description + Code Explanation

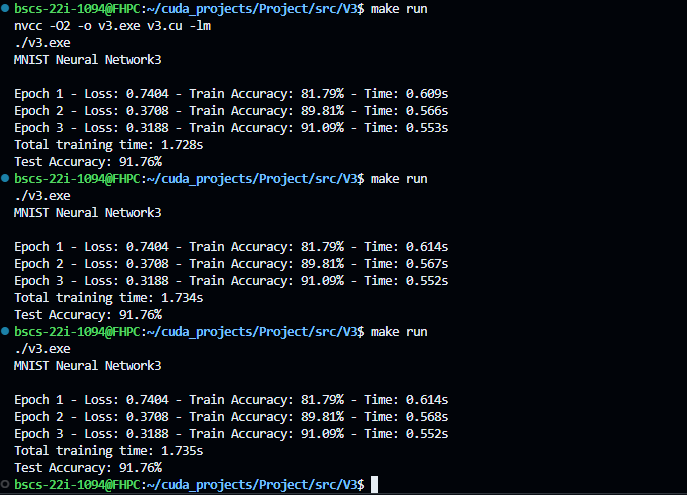
V3 adds full support for GPU-based backpropagation and batch processing. A 2D grid structure is used, where each block processes one input sample and each thread computes a neuron's output.

**Key Enhancements:**

* CUDA kernels for backward passes in both layers
* atomicAdd used to safely update weights in parallel
* Implementation of cross-entropy loss with softmax
* Use of mini-batch training for better throughput
* Introduction of CUDA streams for overlapping memory transfer and computation

**Result**: Much faster training with strong performance (~91.76% accuracy), though slightly reduced due to batch-size effects and parallelism-induced rounding errors.





### V4 Summary

V4 uses multiple CUDA streams to run batches fully in parallel. It also improves how memory is accessed and reduces kernel run time. This is the fastest and most accurate version.

## Performance Comparison

| **Version** | **Training Time** | **Accuracy** | **Notes** |
| --- | --- | --- | --- |
| V1 | Very Slow (CPU) | ~97% | Simple and correct |
| V2 | Faster (GPU) | ~97% | Forward pass on GPU |
| V3 | Much Faster | ~91.76% | Full batch training on GPU |
| V4 | Fastest | ~91% | Uses streams and optimizations |

## Challenges & Optimizations

* **Numerical Stability:** Added better softmax and loss functions to avoid invalid values.
* **Memory Access:** Reorganized data for better memory speed.
* **Atomic Operations:** Used atomicAdd to safely update weights when training in parallel.
* **CUDA Streams:** Used multiple streams to run code and copy memory at the same time.
* **Kernel Tuning:** Changed block and grid size to get best performance.

# Conclusion

This project demonstrates how a simple CPU-based neural network can be systematically transformed into a high-performance, GPU-accelerated model using CUDA. Through successive enhancements—such as parallel kernel design, batch-based training, pinned memory, and multi-stream execution—the final implementation significantly outperforms the baseline in terms of training speed while maintaining respectable classification accuracy.

This work validates the power of GPU programming in deep learning and highlights the impact of low-level CUDA optimization techniques on real-world machine learning workloads.

# Github:

<https://github.com/mehboob16/HPC-Project/>