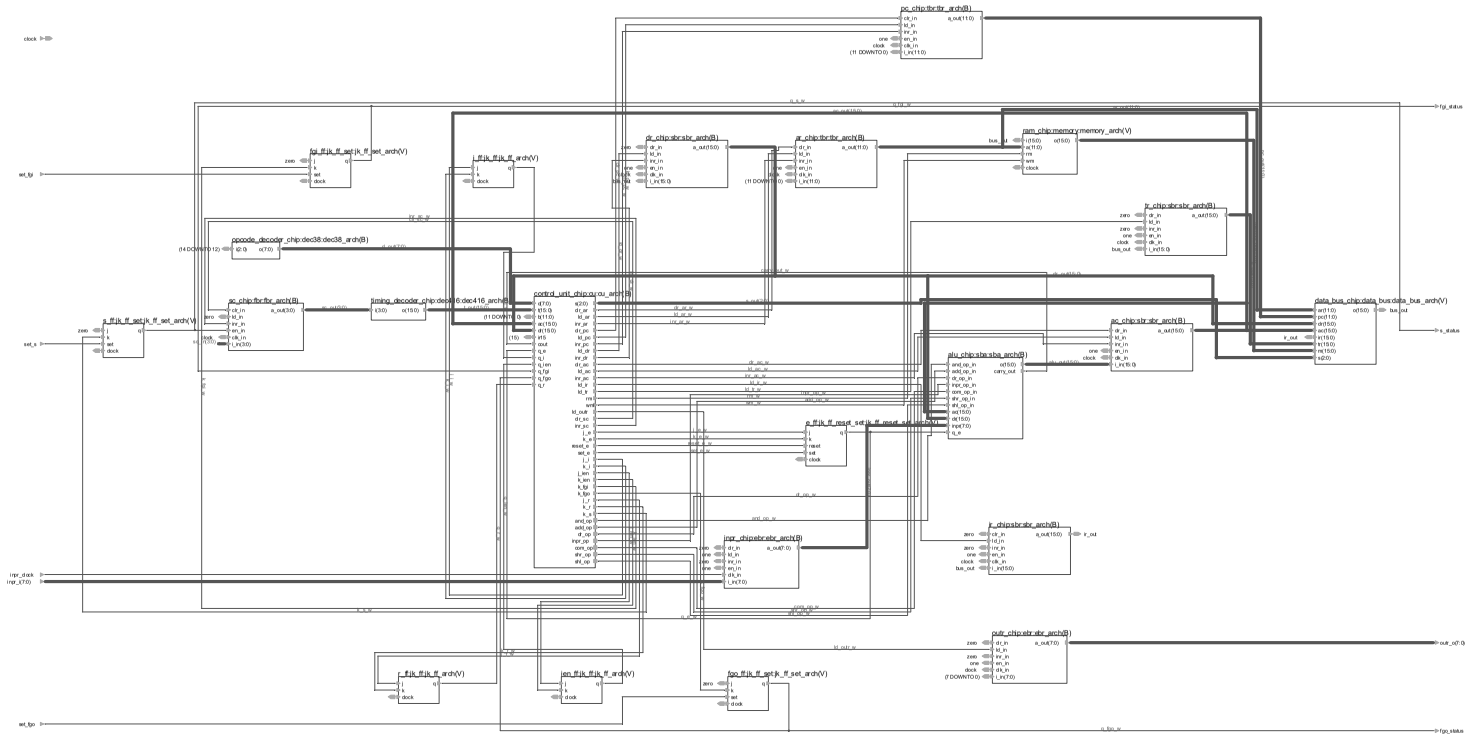
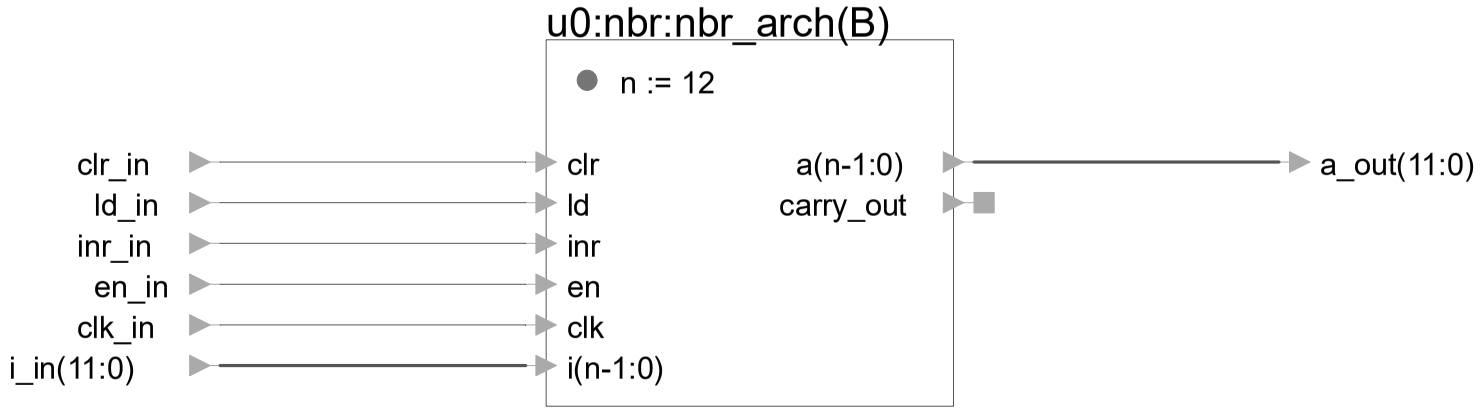
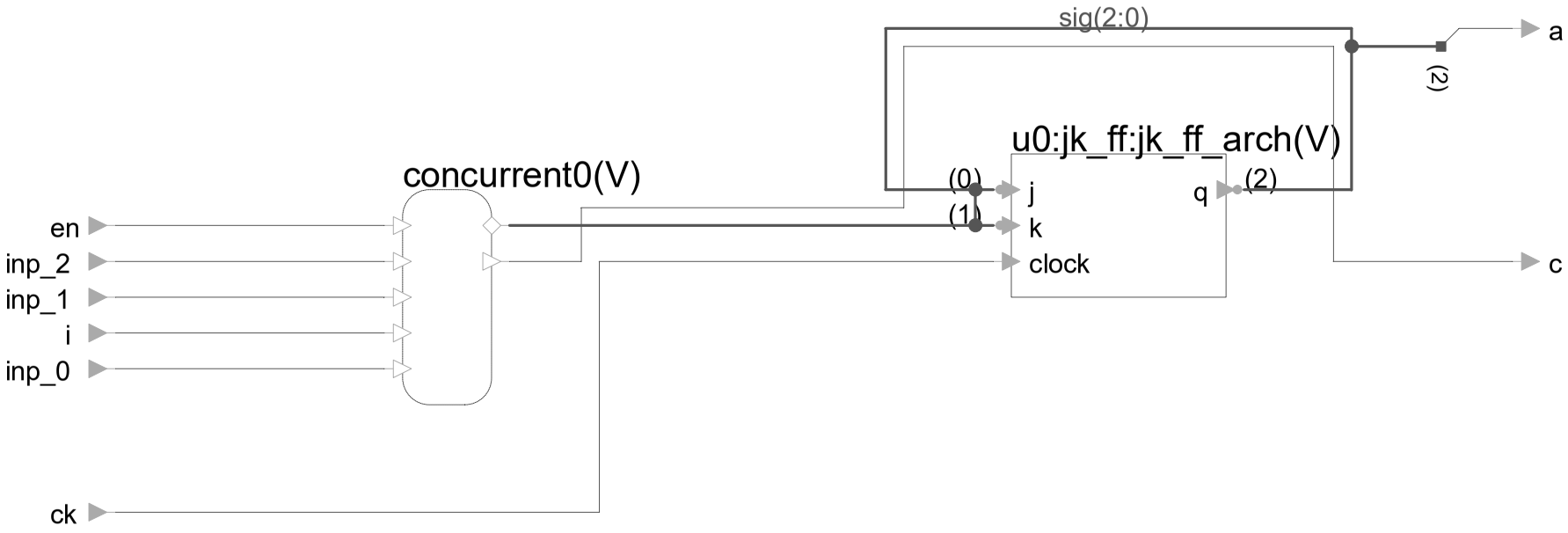

pc_chip:tbr:tbr_arch(B)
ram_chip:memory:memory_arch(V)
ir_chip:abr:abr_arch(B)
opcode_decoder_chip:dec38:dec38_arch(B)
control_unit_chip:cu_arch(B)
data_bus_chip:data_bus:data_bus_arch(V)
alu_chip:alu:alu_arch(B)
timing_decoder_chip:dec4x16:dec416_arch(B)
clock
set_fgi
set_s
set_fgo
fgi_status
s_status
fgo_status

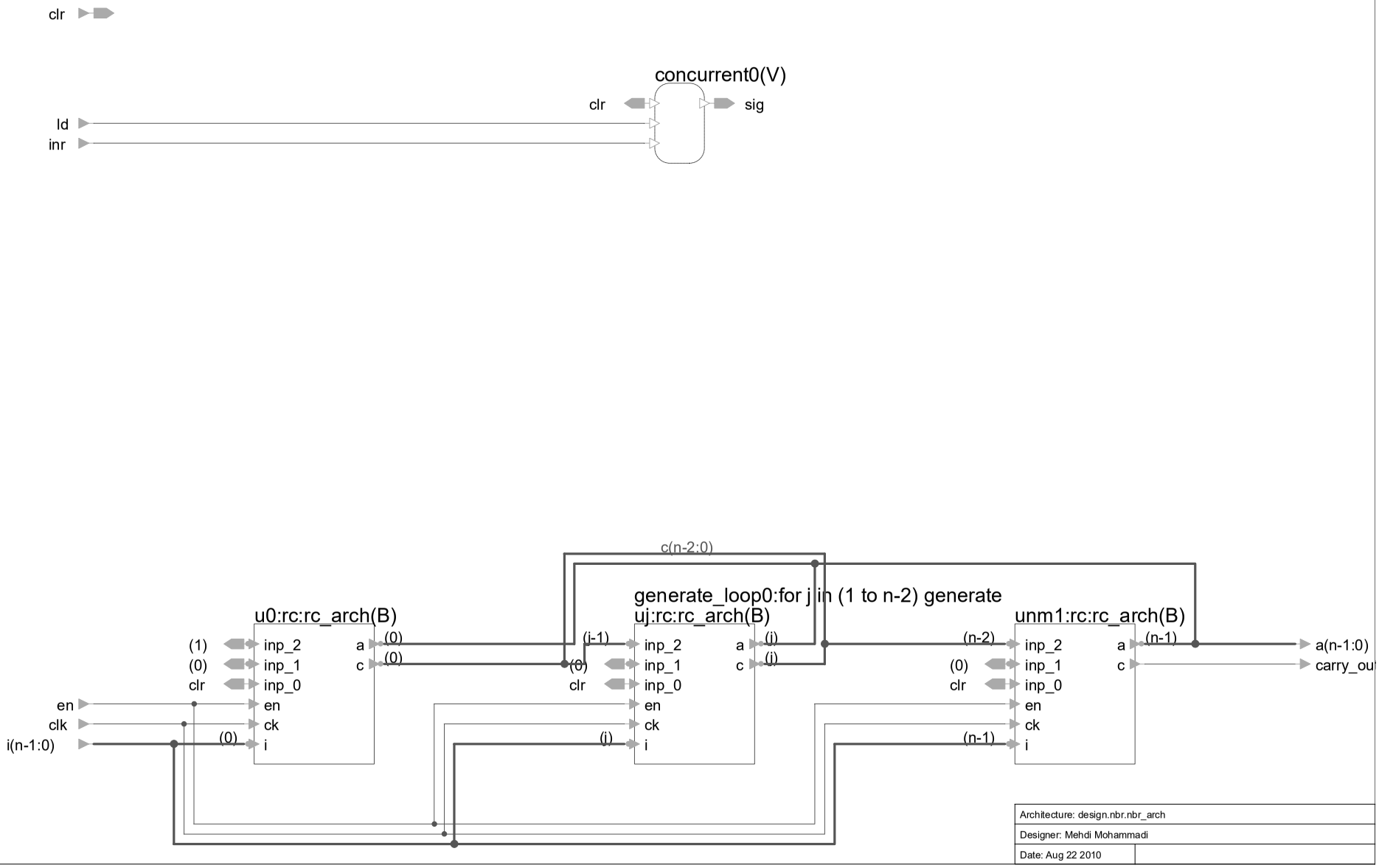


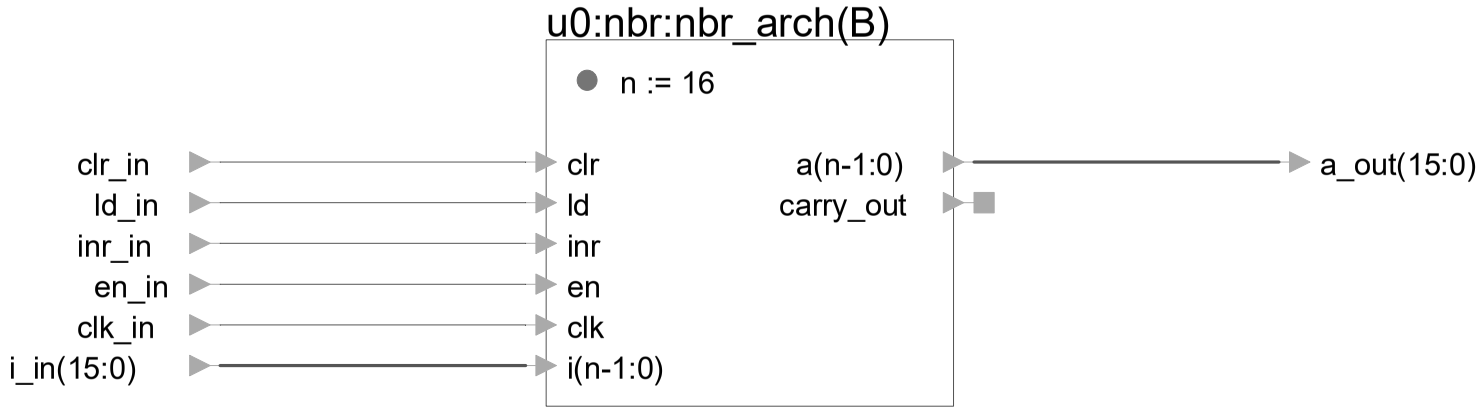
Architecture: design.tbr.tbr_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	



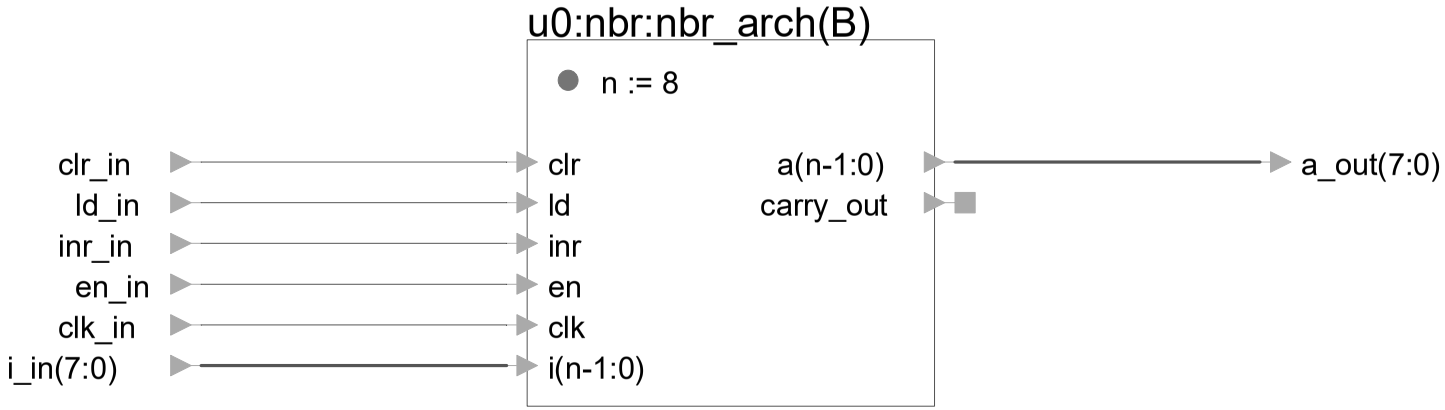
Architecture: design.rc.rc_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	

n :=

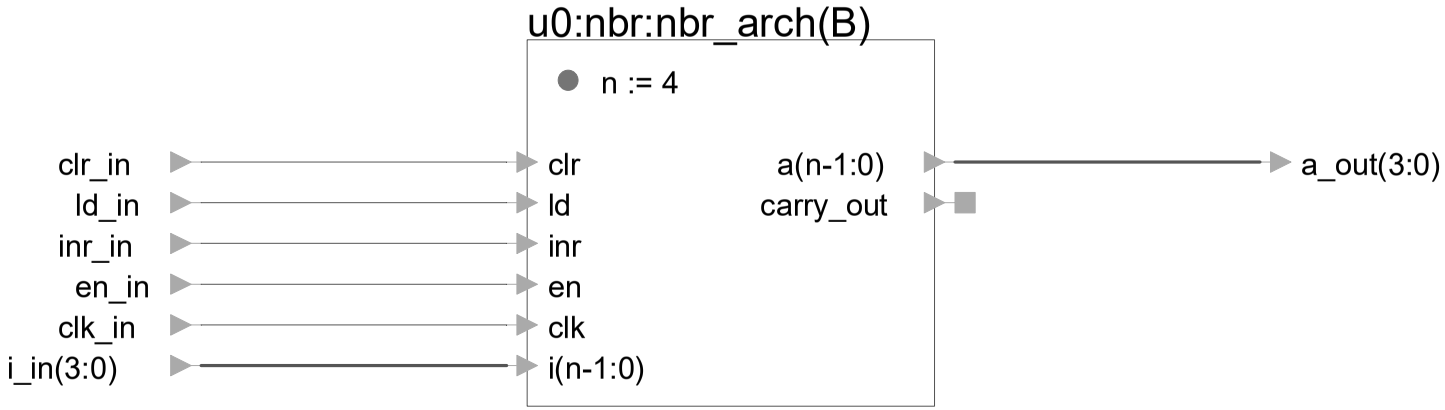

clr
concurrent0(V)
clr
sig
ld
inr
c(n-2:0)
generate_loop0:for j in (1 to n-2) generate
u0:rc:rc_arch(B)
uj:rc:rc_arch(B)
unm1:rc:rc_arch(B)
(1)
(0)
clr
inp_2
inp_1
inp_0
a
c
(0)
(0)
(j-1)
(0)
(j)
(j)
(n-2)
(0)
(n-1)
(n-1)
a(n-1:0)
carry_out
en
clk
i(n-1:0)
en
ck
i
(0)
(j)
(n-1)
Architecture: design.nbr.nbr_arch
Designer: Mehdi Mohammadi
Date: Aug 22 2010



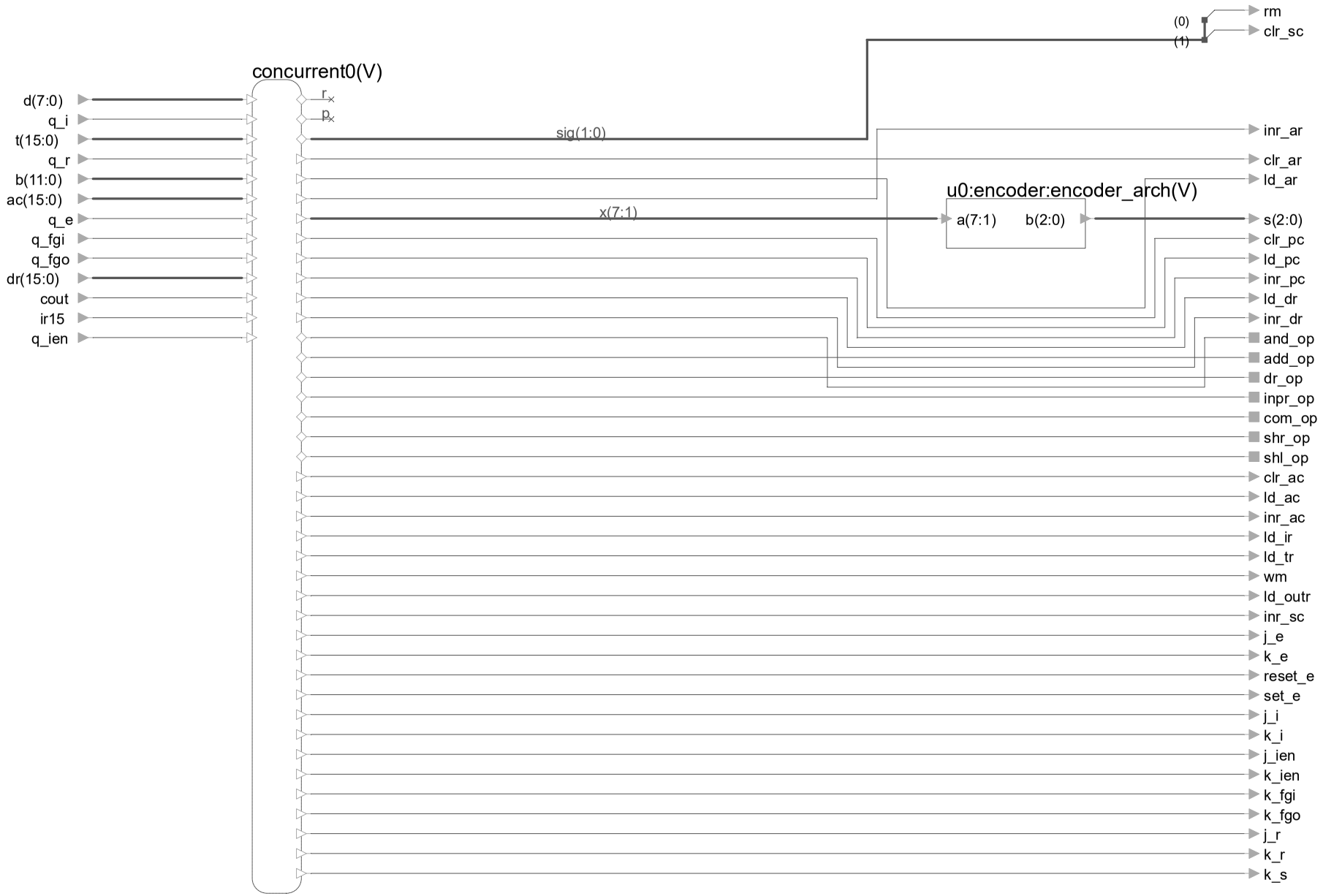
Architecture: design.sbr.sbr_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	



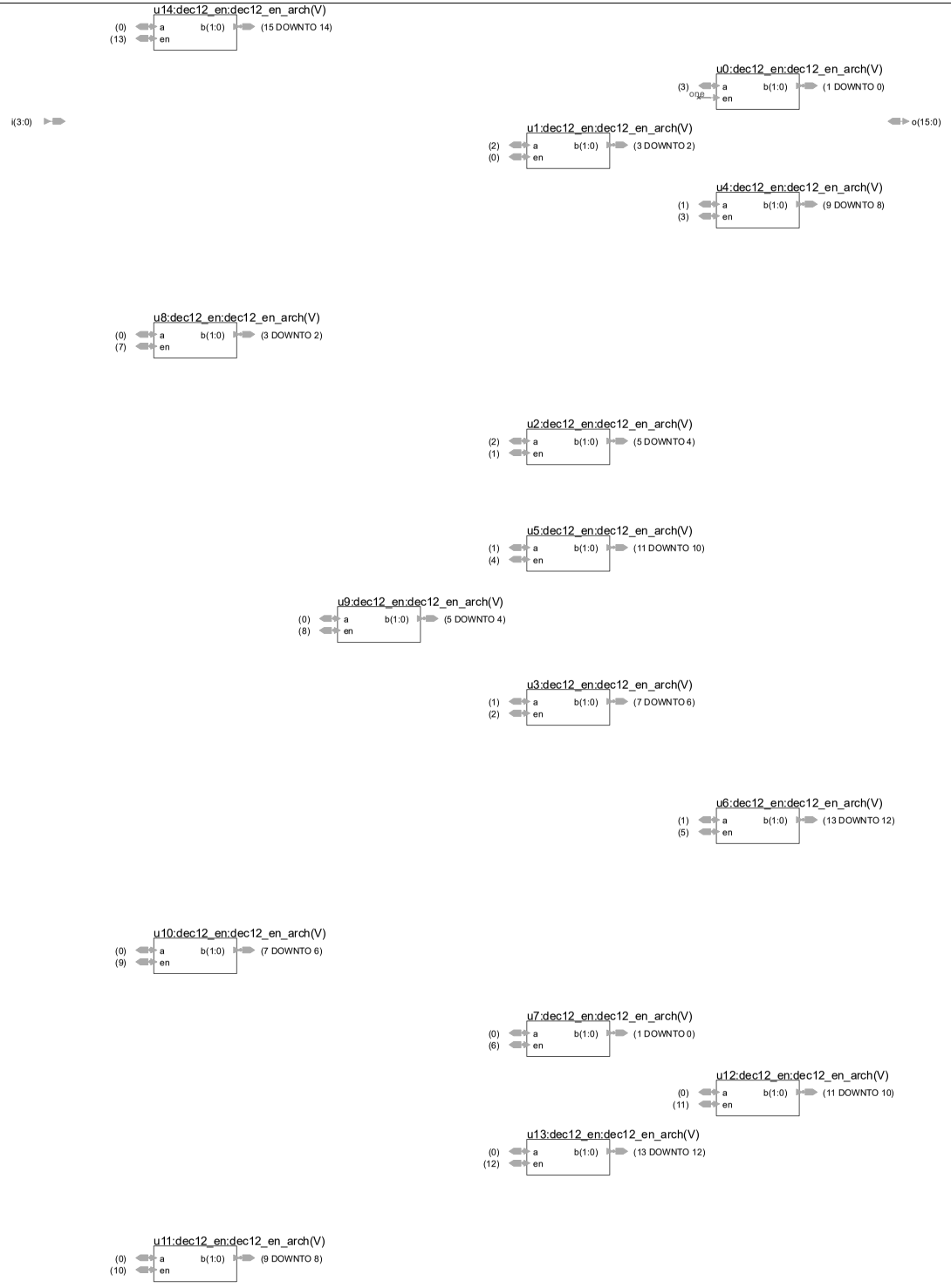
Architecture: design.ebr.ebr_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	

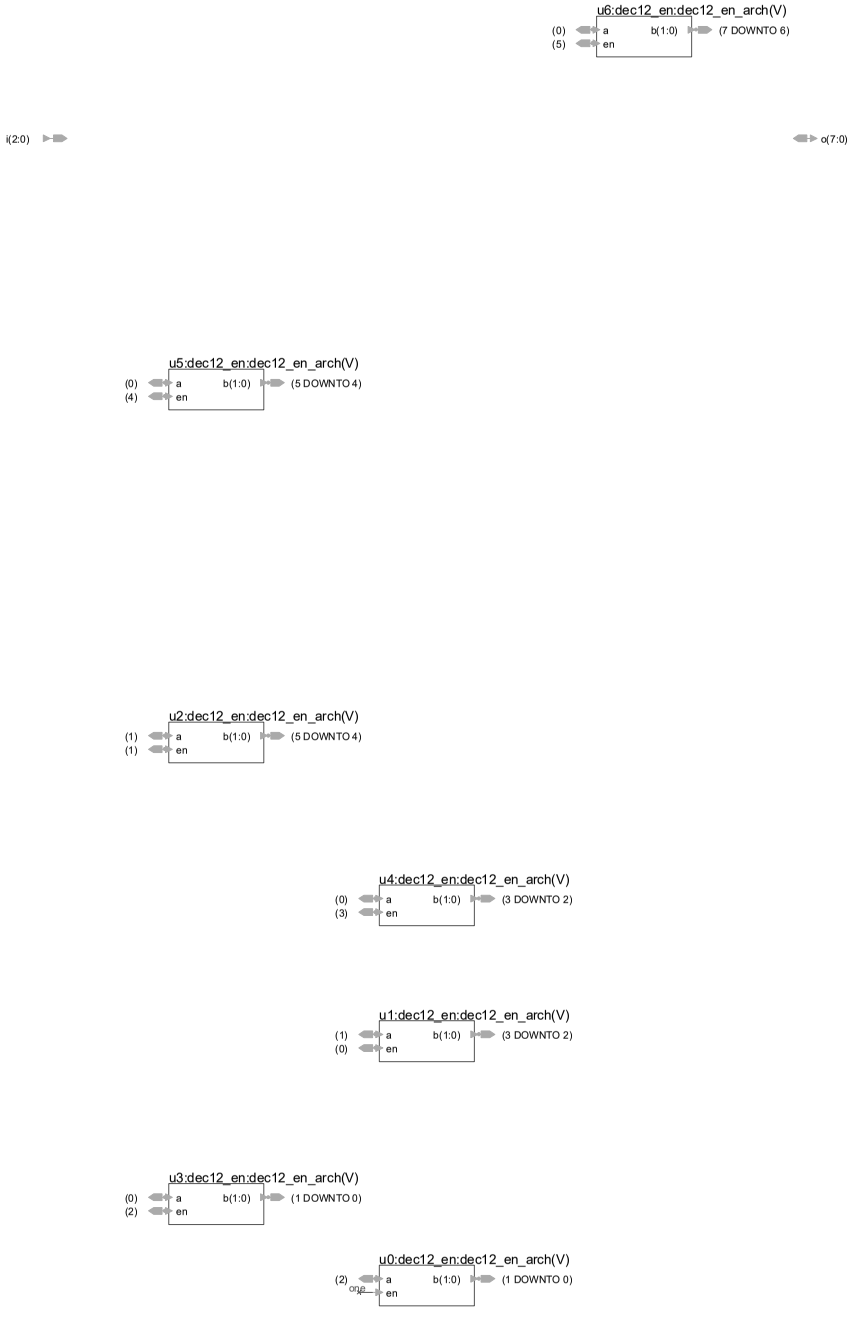


Architecture: design.fbr.fbr_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	

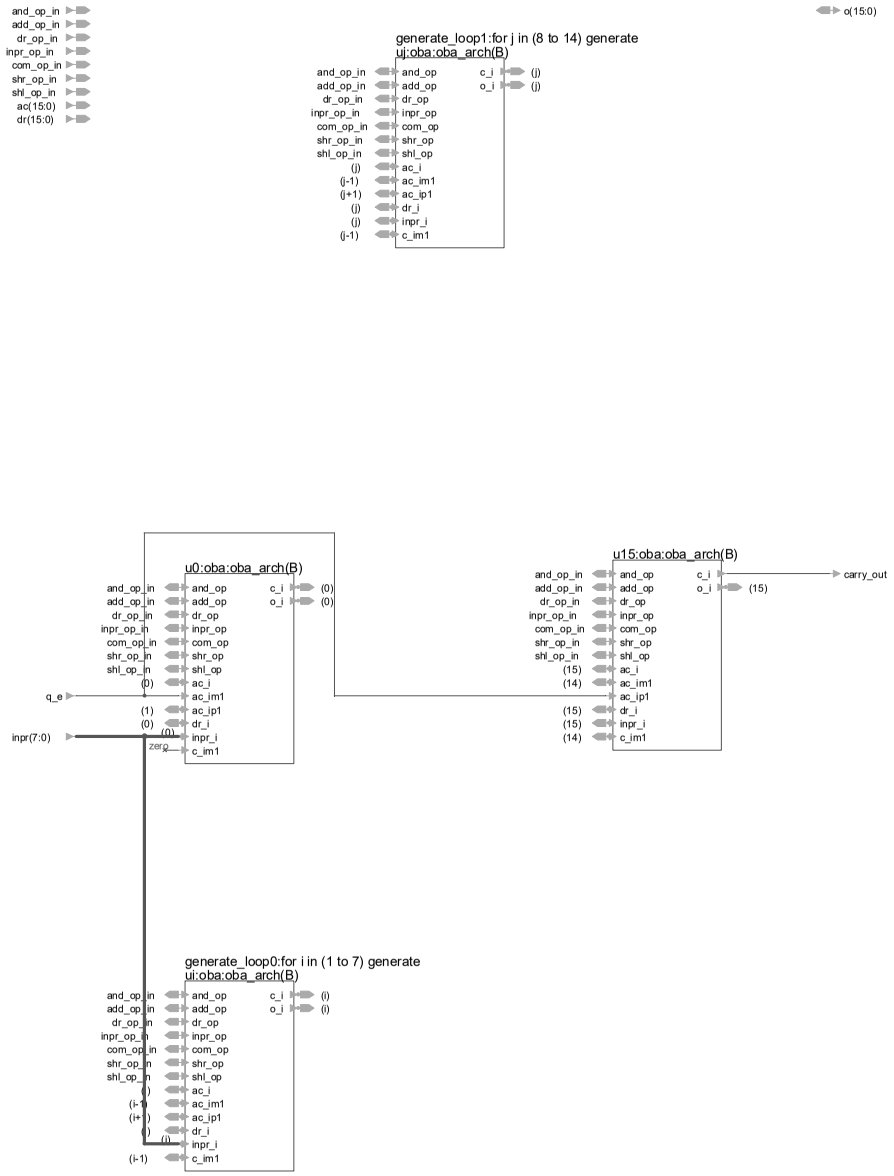


Architecture: design.cu.cu_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	

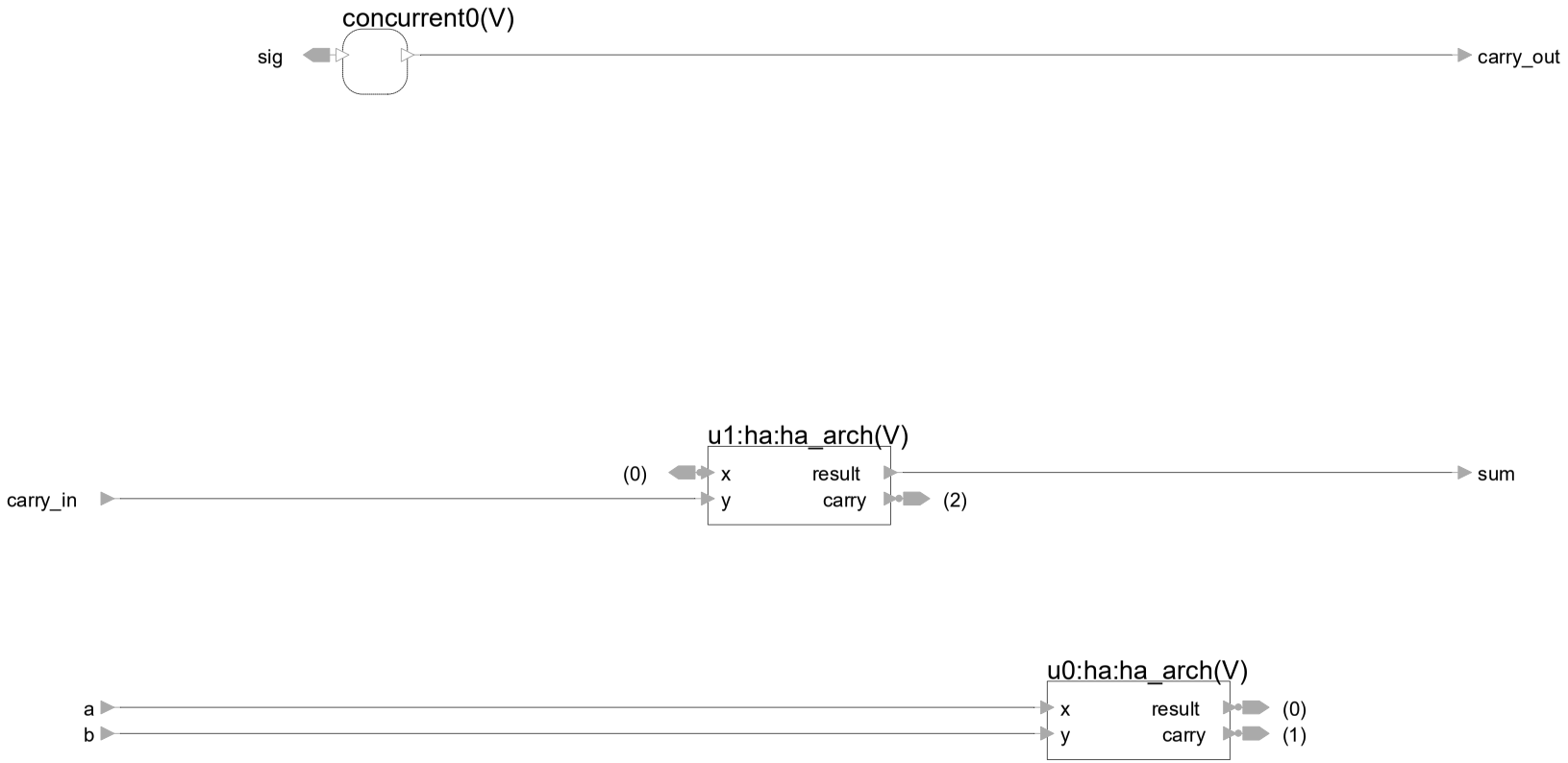

u14:dec12_en:dec12_en_arch(V)
(0)
(13)
a
en
b(1:0)
(15 DOWNTO 14)
u0:dec12_en:dec12_en_arch(V)
(3)
one
a
en
b(1:0)
(1 DOWNTO 0)
i(3:0)
o(15:0)
u1:dec12_en:dec12_en_arch(V)
(2)
(0)
a
en
b(1:0)
(3 DOWNTO 2)
u4:dec12_en:dec12_en_arch(V)
(1)
(3)
a
en
b(1:0)
(9 DOWNTO 8)
u8:dec12_en:dec12_en_arch(V)
(0)
(7)
a
en
b(1:0)
(3 DOWNTO 2)
u2:dec12_en:dec12_en_arch(V)
(2)
(1)
a
en
b(1:0)
(5 DOWNTO 4)
u5:dec12_en:dec12_en_arch(V)
(1)
(4)
a
en
b(1:0)
(11 DOWNTO 10)
u9:dec12_en:dec12_en_arch(V)
(0)
(8)
a
en
b(1:0)
(5 DOWNTO 4)
u3:dec12_en:dec12_en_arch(V)
(1)
(2)
a
en
b(1:0)
(7 DOWNTO 6)
u6:dec12_en:dec12_en_arch(V)
(1)
(5)
a
en
b(1:0)
(13 DOWNTO 12)
u10:dec12_en:dec12_en_arch(V)
(0)
(9)
a
en
b(1:0)
(7 DOWNTO 6)
u7:dec12_en:dec12_en_arch(V)
(0)
(6)
a
en
b(1:0)
(1 DOWNTO 0)
u12:dec12_en:dec12_en_arch(V)
(0)
(11)
a
en
b(1:0)
(11 DOWNTO 10)
u13:dec12_en:dec12_en_arch(V)
(0)
(12)
a
en
b(1:0)
(13 DOWNTO 12)
u11:dec12_en:dec12_en_arch(V)
(0)
(10)
a
en
b(1:0)
(9 DOWNTO 8)
Architecture: design:dec416.dec416_arch
Designer: Mehdi Mohammadi
Date: Aug 22 2010



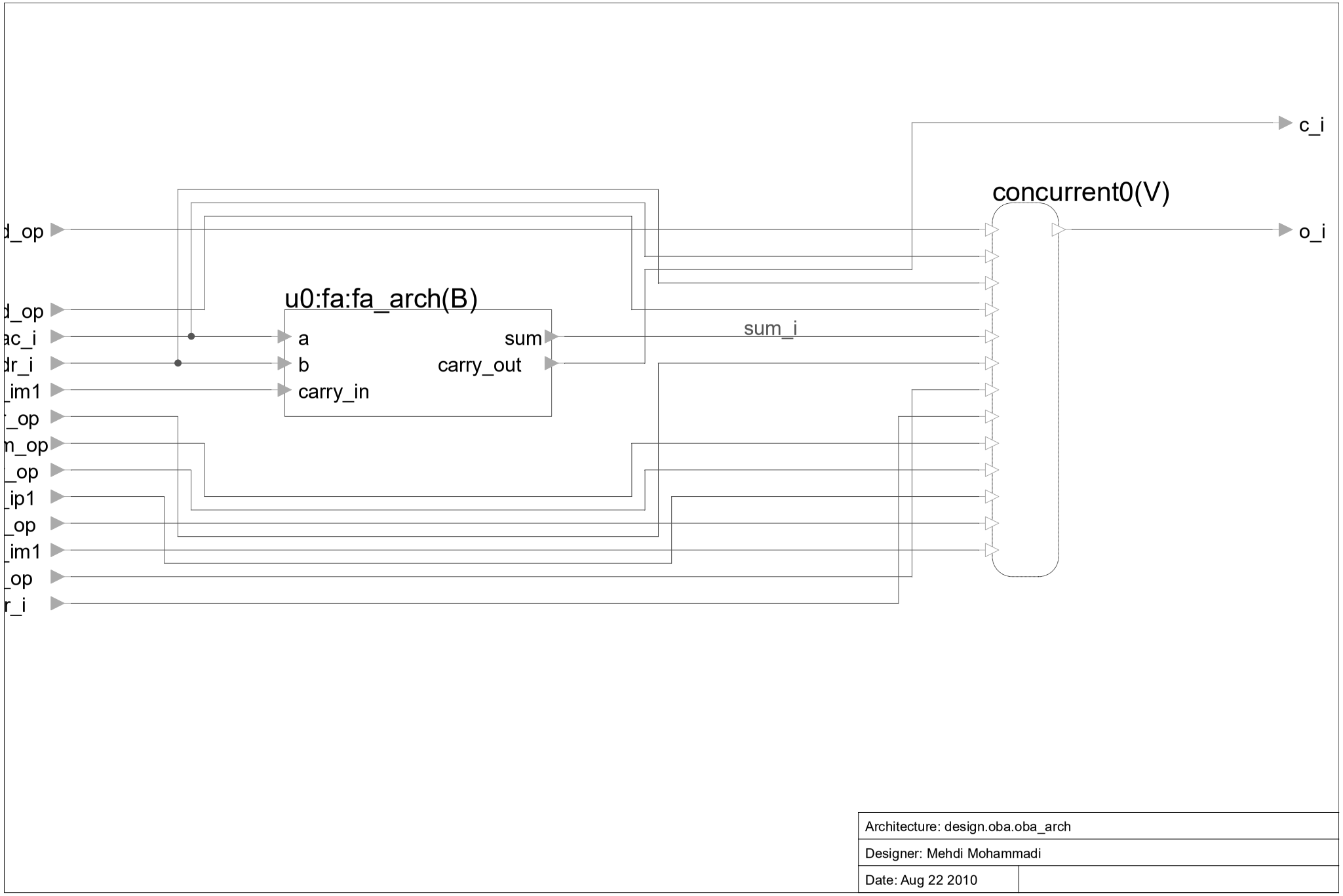
Architecture: design.dec38.dec38_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	



Architecture: designs.baa.baa_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	



Architecture: design.fa.fa_arch	
Designer: Mehdi Mohammadi	
Date: Aug 22 2010	


c_i
concurrent0(V)
o_i
u0:fa:fa_arch(B)
a
b
carry_in
sum
carry_out
sum_i
Architecture: design.oba.oba_arch
Designer: Mehdi Mohammadi
Date: Aug 22 2010