



## Features:

- Based on self -copyright IC
- To achieve Ethernet data transparent transmission in 1-8E1 circuits
- Can realize the local and the remote device reset
- Ethernet Interface is 100BASE-FX, support VLAN protocol

This interface converter is based on FPGA, using reverse direction multiplexing technology to bundle for multiple E1 circuits to transmit the Ethernet data of 4Channel 100BASE-TX. It can realize 1~8E1 channel to convert between Ethernet optical interface. This device can transmit the transceiver signal point to point to Ethernet optical interface to make E1 channels interconnected with Ethernet optical interface. Unlike general remote network bridge, this device can support 1~8Channel E1 channel configuration, can automatically detect the number of E1 and select the E1 available. It allows E1 lines transmission time delay difference.

## FEATURES

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- To achieve Ethernet data transparent transmission in 1-8E1 circuits
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- Ethernet Interface is 100BASE-FX, support VLAN protocol
- Inter-set dynamic Ethernet MAC address (4,096) with local data frame filtering function
- Single Channel lines rate is 1984Kbit/s, 8Channel Bandwidth is up to 15872Kbit/s
- Channel 10M/100M Ethernet interface can isolate each other to realize communication independently
- Ethernet interface supports 10M/100M, half/full duplex auto- adaptable and AUTO-MDIX (crossed line and straightly connected line self-adaptable )
- CRC automatic alarm threshold can be set to isolate the poor quality transmission lines and cut off a single-direction. When 2M branch circuit one direction error rate exceeds threshold, cutting off this direction the other direction is not affected; that is to say, both of the Ethernet direction transmission can be asymmetric
- Allow 8Channel E1 transmission time delay difference 100ms. When the margin exceed the allowed range, the system can automatically stop on the E1 that time delay is too large to send data
- E1 interface conform to ITU-T G.703, G.704 and G.823, not support the use of signal timeslot
- E1 interface module with inter-set clock recovery circuit and HDB3 code circuit
- Support E1 channel hot-plug in the device, and automatically detects the effective E1 channel and will not interrupt data transmission

- Can support 1-8Channel E1 channel configuration, can automatically detect the number of E1 and select the E1 available;

## PARAMETERS:

### ◆ E1 Interface

Interface Standard: comply with protocol G.703;  
 Interface Rate: 2048Kbps±50ppm;  
 Interface Code: HDB3;  
 E1 Impedance: 75Ω (unbalance), 120Ω (balance);  
 Jitter tolerance: In accord with protocol G.742 and G.823  
 Allowed Attenuation: 0~6dBm

### ◆ Ethernet interface(10/100M)

Interface rate: 10/100 Mbps, half/full duplex auto-negotiation  
 Interface Standard: Compatible with IEEE 802.3, IEEE 802.1Q (VLAN)  
 MAC Address Capability: 4096  
 Connector: RJ45, support Auto-MDIX

### ◆ Working environment

Working temperature: -10°C ~ 50°C  
 Working Humidity: 5%~95 % (no condensation)  
 Storage temperature: -40°C ~ 80°C  
 Storage Humidity: 5%~95 % (no condensation)

Functional Description	8E1/4FE interface converter, 4channel 100M Ethernet transmission over 8E1, 4channel Ethernet support Vlan, snmp or console managed
Port Description	8 E1 interfaces, 4 Fast Ethernet ports, SNMP port
Power	Power supply: AC180V ~ 260V ; DC -48V ; DC + 24V  Power consumption: ≤10W

Dimension	Product Size: 19 inch 1U 483X138X44mm (WDXXH)
Weight	2.0KG/piece

