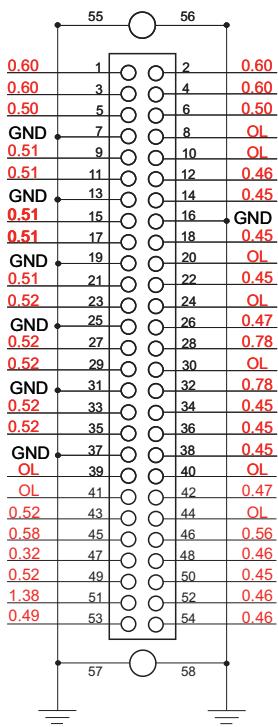


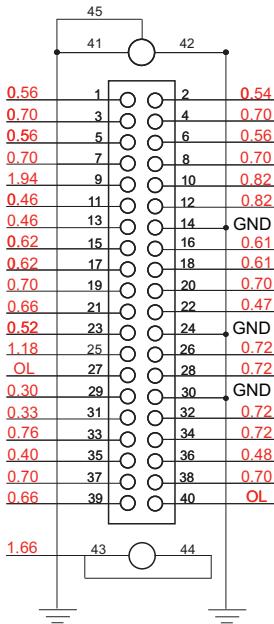
iPhone 6S Plus

J4200



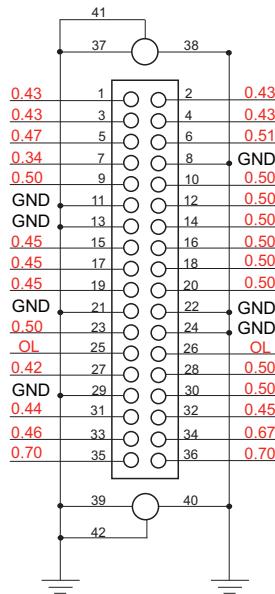
显示触摸集成

J4600

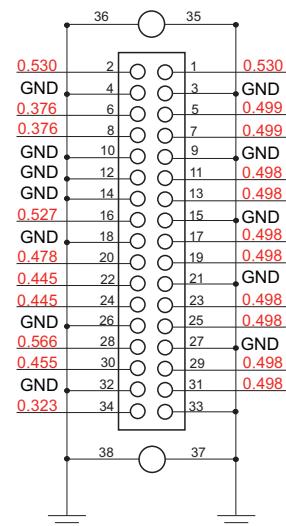


尾插

J3100



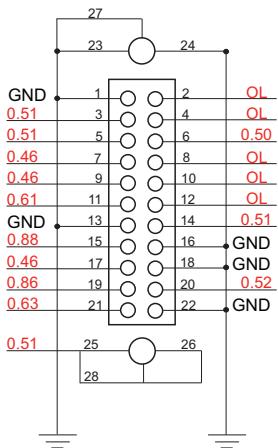
J3200



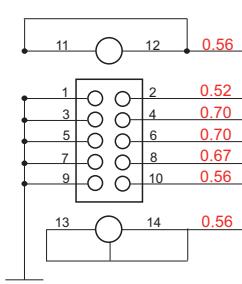
后置相机

前置相机
听筒/MIC3
PROX/ALS

J4100

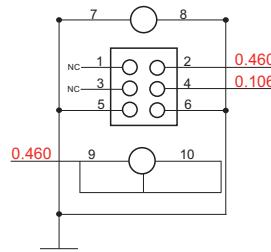


J4700



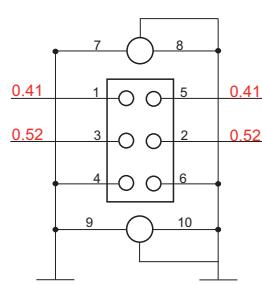
LED/MIC2
HOLD_KEY

J4701



RINGER
VOL_UP
VOL_DOWN

J2400



BATT

指纹/菜单

N66 MLB - DVT_AD

LAST_MODIFICATION=Sun NOV 18 18:50:11 2015

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3	4	SYSTEM:N66 SPECIFIC		
4	5	SYSTEM: MECHANICAL COMPONENTS		
5	6	SOC:JTAG,USB,XTAL		
6	7	SOC:PCIE		
7	8	SOC:CAMERA & DISPLAY		
8	9	SOC:SERIAL & GPIO		
9	10	SOC:OWL		
10	11	SOC:POWER (1/3)		
11	12	SOC:POWER (2/3)		
12	13	SOC:POWER (3/3)		
13	15	NAND		
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15	21	SYSTEM POWER:PMU (2/3)		
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35		TABLE OF CONTENTS		
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38		WLAN LAT 2.4GHZ BAW BPF		
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40		CELLULAR BASEBAND: POWER1		
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42		CELLULAR BASEBAND: CONTROL AND INTERFACES		
43		CELLULAR BASEBAND: GPIOs		
44		CELLULAR PMU: CONTROL AND CLOCKS		
45		CELLULAR PMU: SWITCHERS AND LDOS		
46		CELLULAR PMU: ET MODULATOR		
47		CELLULAR TRANSCEIVER: POWER		
48		CELLULAR TRANSCEIVER: PRX PORTS		
49		CELLULAR TRANSCEIVER: DRX/GPS PORTS		
50		CELLULAR TRANSCEIVER: TX PORTS		
51		CELLULAR FRONT END: LB PAD		
52		CELLULAR FRONT END: MB PAD		
53		CELLULAR FRONT END: HB PAD		
54		CELLULAR FRONT END: 2G PA		
55		CELLULAR FRONT END: LB ASM		
56		CELLULAR FRONT END: MB-HB ASM		
57		CELLULAR FRONT END: DIVERSITY		
58		SIM		
59		WIFI/BT: WIFI/BT MODULE		
60		STOCKHOLM		

SCH 051-33994

BOM 639-00299 (BETTER)

BOM 639-01119 (BETTER, M)

BRD 820-28040

BOM 639-00301 (ULTRA)

BOM 639-01122 (ULTRA, M)

MCO 056-00472

BOM 639-00302 (SUPREME)

BOM 639-01125 (SUPREME, M)

BOM 639-01063 (BETTER, RF2)

BOM 639-01120 (BETTER, RF2, M)

BOM 639-01064 (ULTRA, RF2)

BOM 639-01123 (ULTRA, RF2, M)

BOM 639-01065 (SUPREME, RF2)

BOM 639-01126 (SUPREME, RF2, M)

BOM 639-01116 (BETTER, RFC)

BOM 639-01121 (BETTER, RFC, M)

BOM 639-01117 (ULTRA, RFC)

BOM 639-01124 (ULTRA, RFC, M)

BOM 639-01118 (SUPREME, RFC)

BOM 639-01127 (SUPREME, RFC, M)

Active Diode Alternate

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
376S00106	376S00047	ALTERNATE	Q2300	DIODES INC. ACT DIODE

NAND BOM Options

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S00039	1	NAND_T,B0,1Y,MLC,16Gx8,S38,VLGA70	U1500	CRITICAL	NAND_16G
335S00040	1	NAND_T,B0,1Y,MLC,64Gx8,S38,VLGA70	U1500	CRITICAL	NAND_64G
335S00079	1	NAND_H,B0,1Y,TLC,128Gx8,S38,VLGA70	U1500	CRITICAL	NAND_128G

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S00074	335S00039	ALTERNATE	U1500	H, B0, 1Y, MLC, 16Gx8
335S00075	335S00040	ALTERNATE	U1500	H, B0, 1Y, MLC, 64Gx8
335S00078	335S00040	ALTERNATE	U1500	H, B0, 1Y, TLC, 64Gx8
335S00082	335S00040	ALTERNATE	U1500	T, B0, 1Z, TLC, 64Gx8
335S00064	335S00040	ALTERNATE	U1500	S, B0, 1Z, TLC, 64Gx8
335S00083	335S00079	ALTERNATE	U1500	T, B0, 1Z, TLC, 128Gx8
335S00065	335S00079	ALTERNATE	U1500	S, B0, 1Z, TLC, 128Gx8

Carbon BOM Options

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S1163	1	DISCRETE ACCEL, BOSCH	U3030	CRITICAL	NOSTUFF
338S1163	1	DISCRETE ACCEL, BOSCH	U3030	CRITICAL	CARBON_INVENSENSE
338S00017	1	CARBON, INVENSENSE	U3010	CRITICAL	CARBON_INVENSENSE
132S0395	1	C3013, 0.1UF, INVENSENSE OPTION	C3013	CRITICAL	CARBON_INVENSENSE
338S00029	1	CARBON, ST	U3010	CRITICAL	CARBON_ST
132S0391	1	C3013, 0.01UF, ST OPTION	C3013	CRITICAL	CARBON_ST
338S00087	1	CARBON, INVENSENSE PMU-6800	U3010	CRITICAL	CARBON_INVENSENSE_6800
132S0395	1	C3013, 0.1UF, INVENSENSE OPTION	C3013	CRITICAL	CARBON_INVENSENSE_6800

Power Inductor Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S00117	152S00074	ALTERNATE	\$?	IND_PWB,IND,1,0 IN,3,4A,0.160 OHM,2016
152S00118	152S00075	ALTERNATE	\$?	IND_PWB,IND,1,2 IN,3,4A,0.080 OHM,2016
152S00120	152S00077	ALTERNATE	\$?	IND_PWB,IND,1,0 IN,2.12A,0.150 OHM,2016
152S00121	152S00081	ALTERNATE	\$?	IND_PWB,IND,0.47 IN,3,4A,0.048 OHM,2012
152S00123	152S1936	ALTERNATE	\$?	IND_PWB,IND,15 UM,0.72A,0.900 OHM,3225
152S2052	152S1929	ALTERNATE	\$?	IND_MULT,10W,1,1A,0.320 OHM,0603

SIM Callouts

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
512S00013	1	SIM, Integrated Eject, N66	J3001_RF	CRITICAL	COMMON
512S00015	512S00013	ALTERNATE	J3001_RF	SIM. INTEGRATED EJECT, N71	

NOTE: Revisit for Carrier

Shield Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
613-01503	806-02349	ALTERNATE	SH0500	Upper Front shield
613-01504	806-02350	ALTERNATE	SH0501	Lower Front shield
806-02655	806-02352	ALTERNATE	SH0503	Upper Back shield
806-03410	806-02352	ALTERNATE	SH0503	Upper Back shield
806-02656	806-02353	ALTERNATE	SH0504	Lower Back shield
806-03411	806-02353	ALTERNATE	SH0504	Lower Back shield

NOTE: Revisit for Carrier

Schematic & PCB Callouts

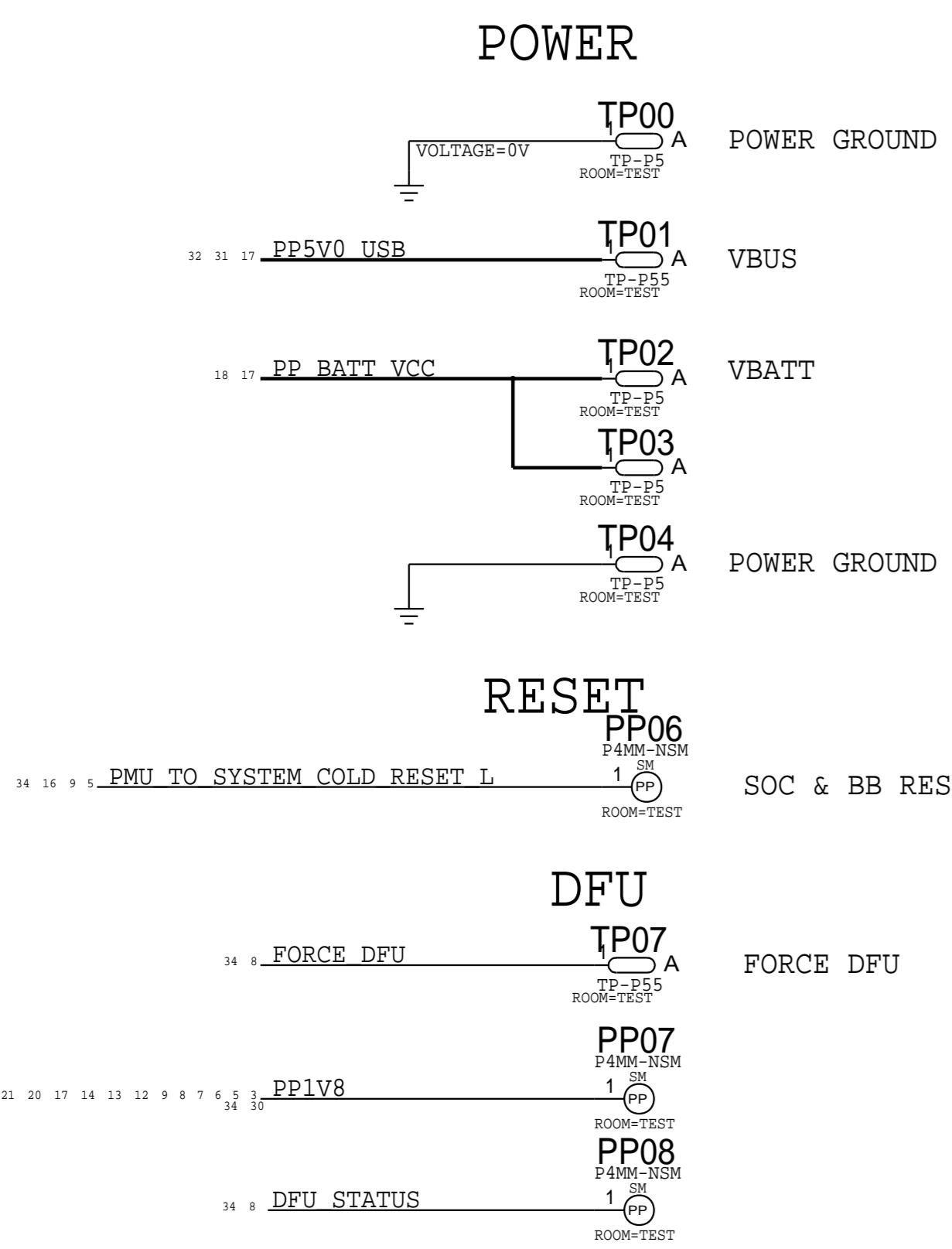
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-00094	1	SCH, SINGLE_BRD, N66	SCH	CRITICAL	?
820-00040	1	PCBF, SINGLE_BRD, N66	PCB	CRITICAL	?
825-6838	1	EEE CODE FOR 639-00299	EEEE_G360	CRITICAL	EEE_BETTER
825-6838	1	EEE CODE FOR 639-00301	EEEE_G35W	CRITICAL	EEE_ULTRA
825-6838	1	EEE CODE FOR 639-00302	EEEE_G35V	CRITICAL	EEE_SUPREME
825-6838	1	EEE CODE FOR 639-01063	EEEE_GKXY	CRITICAL	EEE_BETTER_RF2
825-6838	1	EEE CODE FOR 639-01064	EEEE_GL10	CRITICAL	EEE_ULTRA_RF2
825-6838	1	EEE CODE FOR 639-01065	EEEE_GL11	CRITICAL	EEE_SUPREME_RF2
825-6838	1	EEE CODE FOR 639-01116	EEEE_GL17	CRITICAL	EEE_BETTER_RFC
825-6838	1	EEE CODE FOR 639-01117	EEEE_GL14	CRITICAL	EEE_ULTRA_RFC
825-6838	1	EEE CODE FOR 639-01118	EEEE_GL11	CRITICAL	EEE_SUPREME_RFC
825-6838	1	EEE CODE FOR 639-01119	EEEE_GLL2	CRITICAL	EEE_BETTER_M
825-6838	1	EEE CODE FOR 639-01122	EEEE_GLL3	CRITICAL	EEE_ULTRA_M
825-6838	1	EEE CODE FOR 639-01125	EEEE_GLL6	CRITICAL	EEE_SUPREME_M
825-6838	1	EEE CODE FOR 639-01120	EEEE_GLL8	CRITICAL	EEE_BETTER_RF2_M
825-6838	1	EEE CODE FOR 639-01123	EEEE_GLL0	CRITICAL	EEE_ULTRA_RF2_M
825-6838	1	EEE CODE FOR 639-01126	EEEE_GLLC	CRITICAL	EEE_SUPREME_RF2_M
825-6838	1	EEE CODE FOR 639-01121	EEEE_GLY	CRITICAL	EEE_BETTER_RFC_M
825-6838	1	EEE CODE FOR 639-01124	EEEE_GLL5	CRITICAL	EEE_ULTRA_RFC_M
825-6838	1	EEE CODE FOR 639-01127	EEEE_GLL9	CRITICAL	EEE_SUPREME_RFC_M

PMU/SOC BOM Options

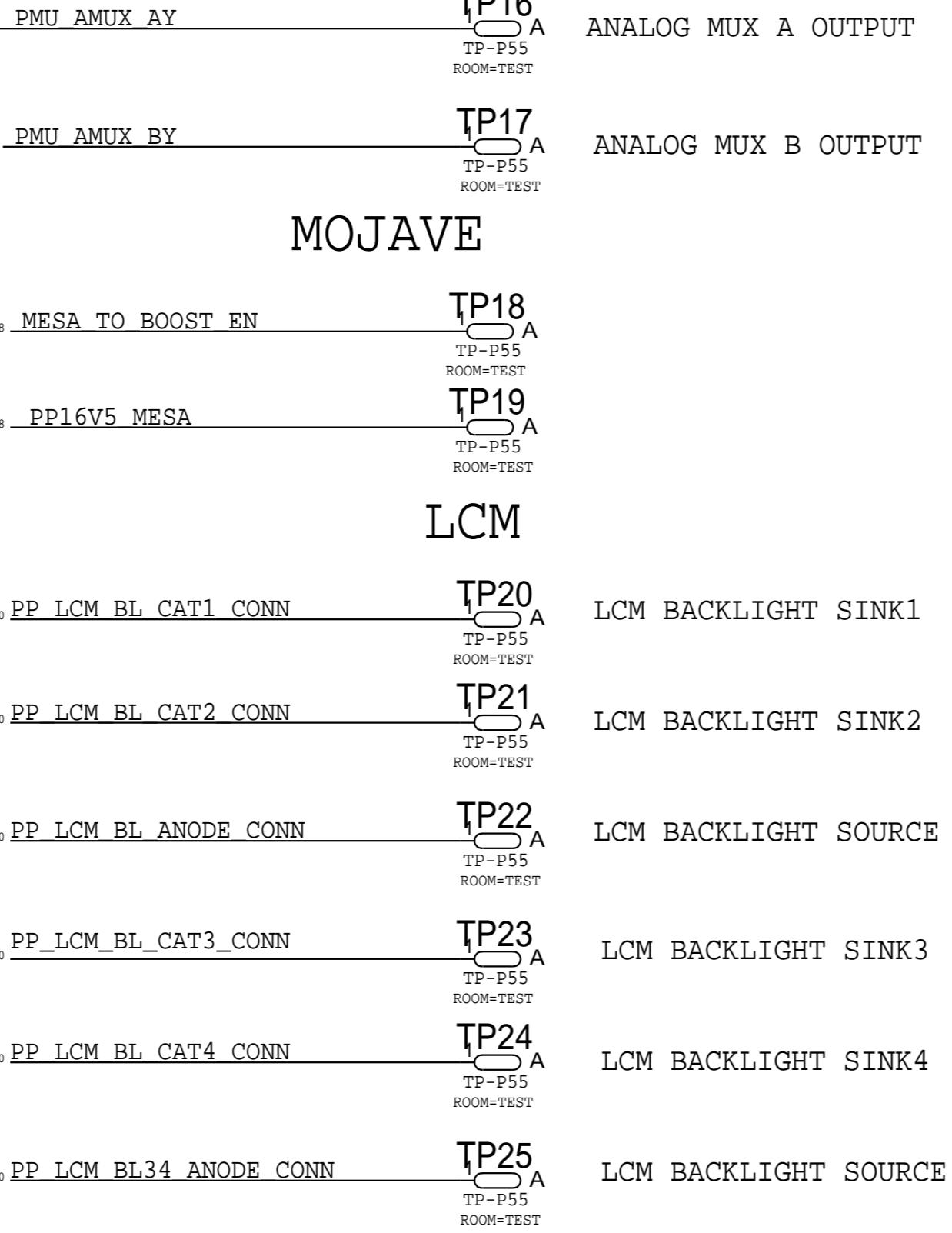
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
998-01699	1	IC_PMO,ANTIGUA,AI,EZ,2000MH,2108B,CSP380	U2000	POR
118S0631	1	RES_MP,100 OHM,1%,1/32W,01005	R0730	POR
131S0307	1	CAP_CER,NPO/COG,100PF,5%,16V,01005	C0730	POR
339S00057	1	DEV_FUSED, M DRAM	U0600	POR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
998-02438	1	IC_PMO,ANTIGUA,AI,EZ,2000MH,2108B,CSP380	U2000	M
118S00099	1	RES_MP,3,01KOHM,1%,1/32W,01005	R0730	M
131S0307	1	CAP_CER,NPO/COG,100PF,5%,16V,01005	C0730	NOSTUFF
339S00067	1	M DEV FUSED, M DRAM	U0600	M

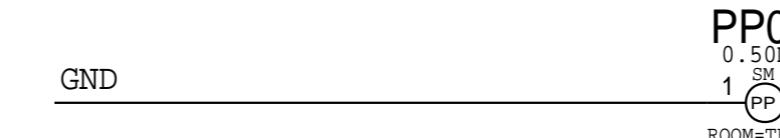
TESTPOINTS



AMUX



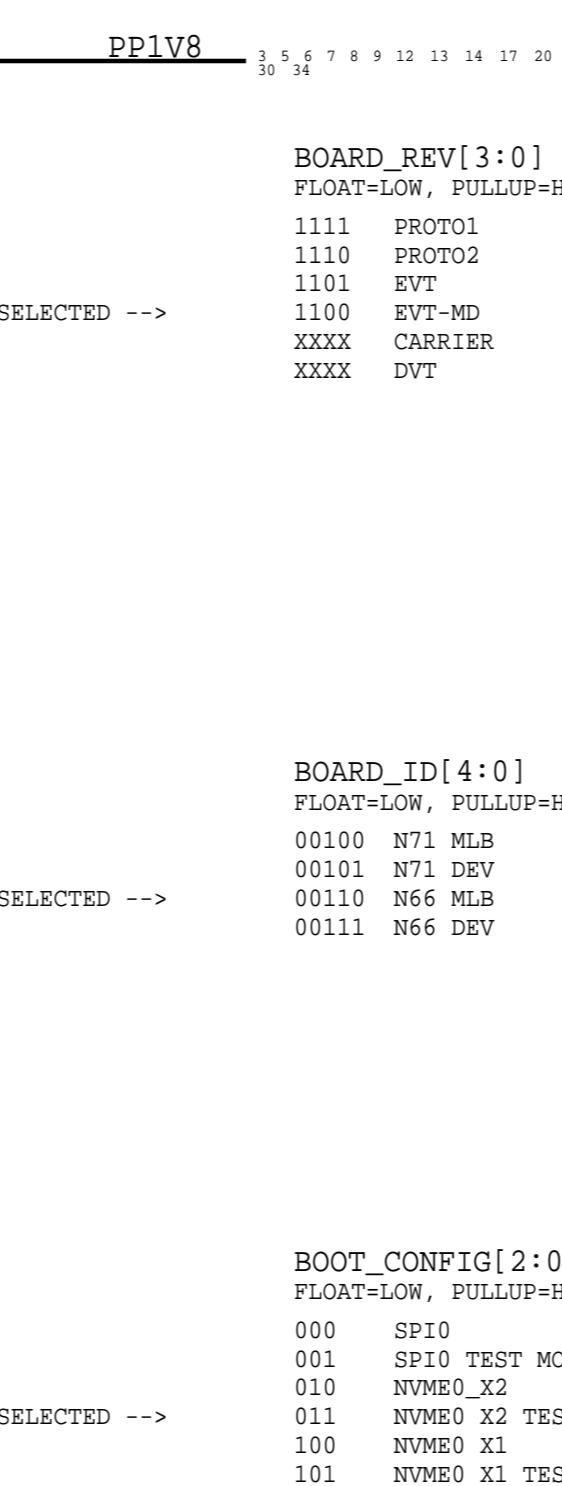
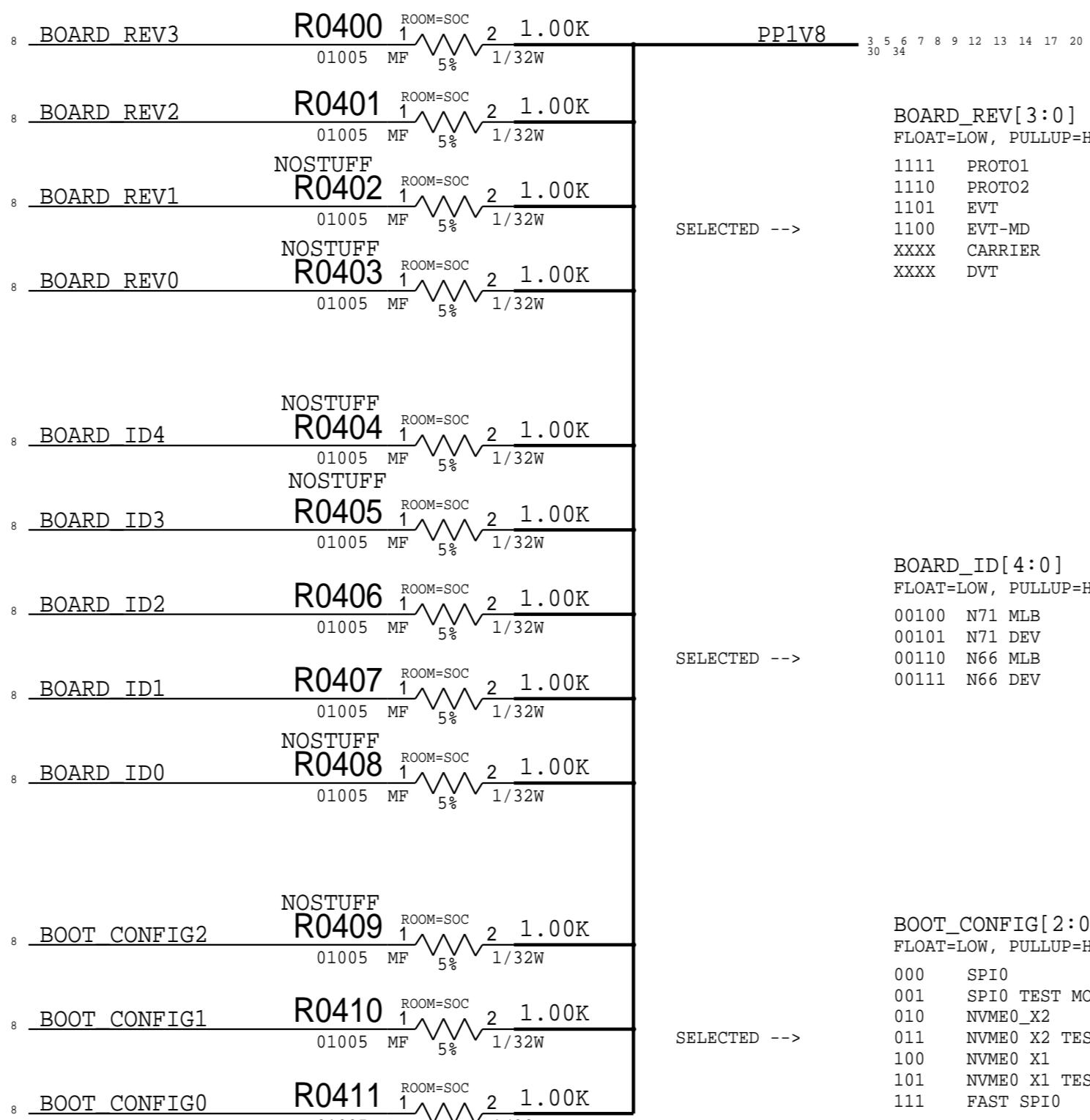
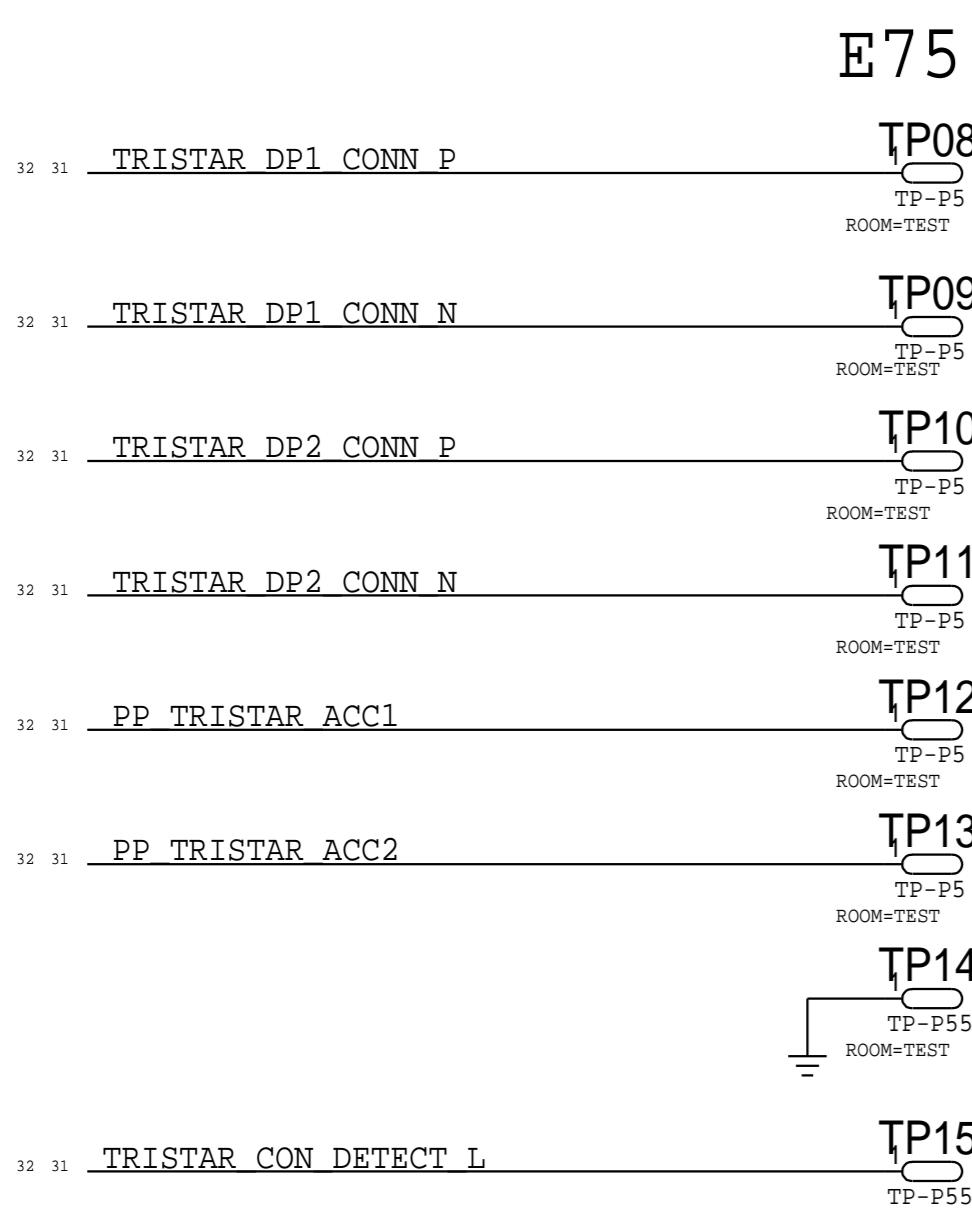
UAT GND Ring Opening



N66 I2C DEVICE MAP

I2C BUS	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ANTIGUA PMU	1110100X	0X74	0XE8
	CHESTNUT	0100111X	0X27	0XA4
	BACKLIGHT 1	1100011X	0X62	0XC4
I2C1	TIGRIS	1110101X	0X75	0XA8
	ARC DRIVER	1000001X	0X41	0XB2
	SPEAKER AMP	1000000X	0X40	0XB0
I2C2	TRISTAR	0011010X	0XA1	0X34
	ALS	0101001X	0X29	0XA2
	DISP EEPROM	1010001X	0X51	0XA2
ISP I2C0	BACKLIGHT 2	1100011X	0X62	0XC4
	OWL	UNUSED	N/A	N/A
	REAR CAM	TBD	TBD	TBD
ISP I2C1	LED DRIVER	1100011X	0X63	0XC6
	FRONT CAM	0010000X	0X10	0XA0
	TOUCH I2C	MESON	1000000X	0XA0
DOPPLER	MAMBA	1100000X	0X60	0xC0
	DOPPLER	1011000X	0X58	0xB0
SEP I2C	SEP EEPROM	1010001X	0X51	0xA2

BOOTSTRAPPING: BOARD REV BOARD ID BOOT CONFIG



8

7

6

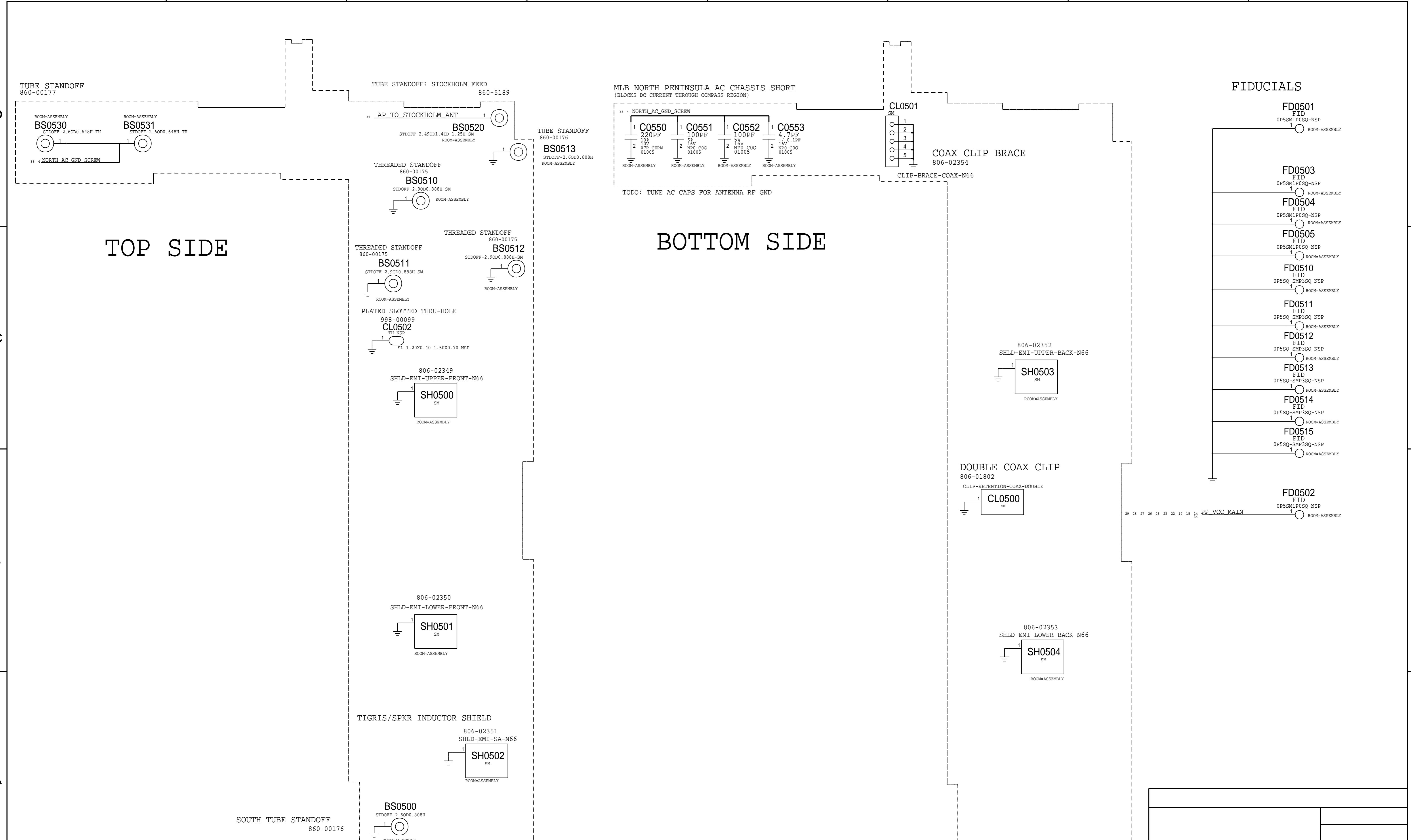
5

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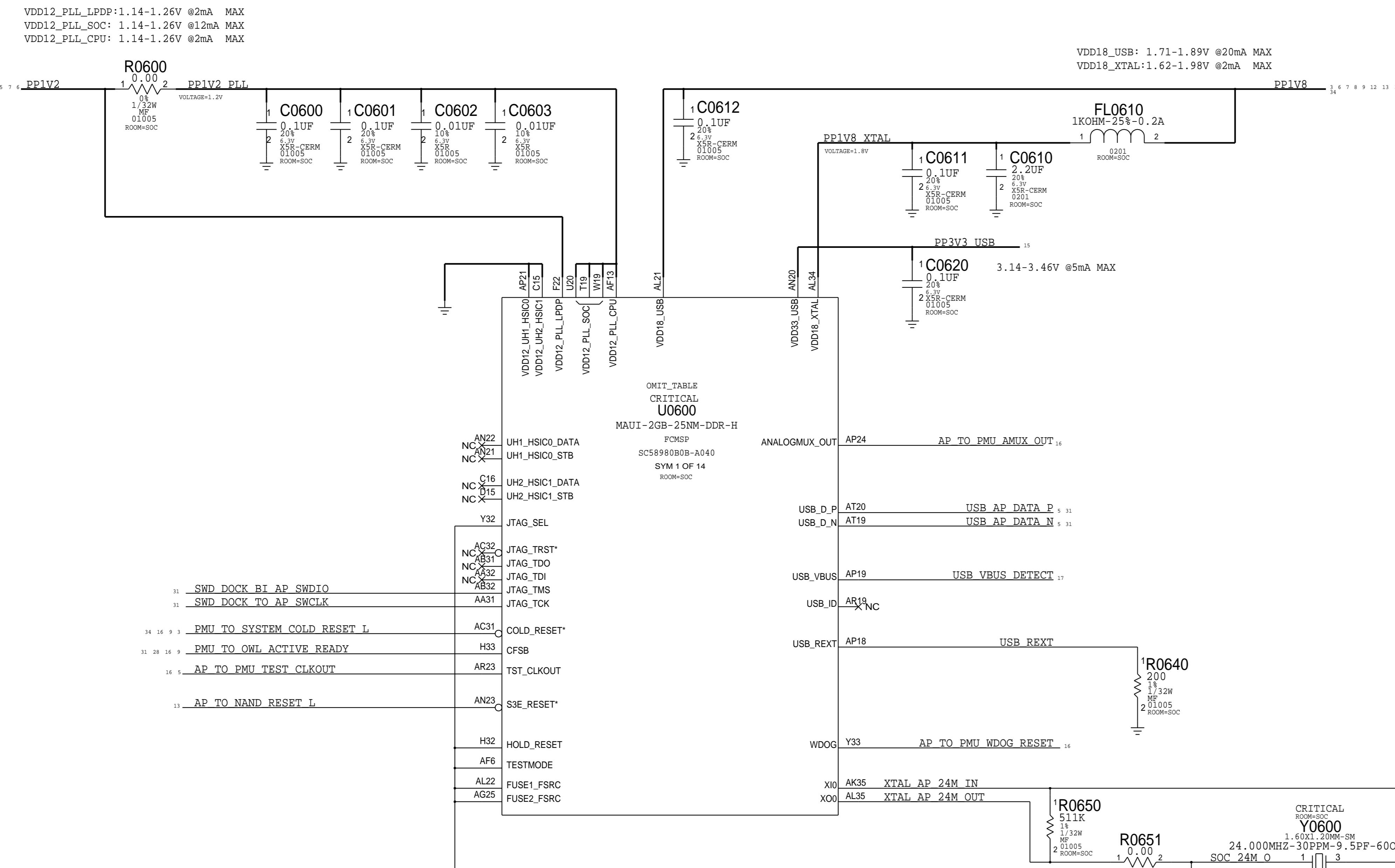
3

2

1

MAUI - USB, JTAG, XTAL

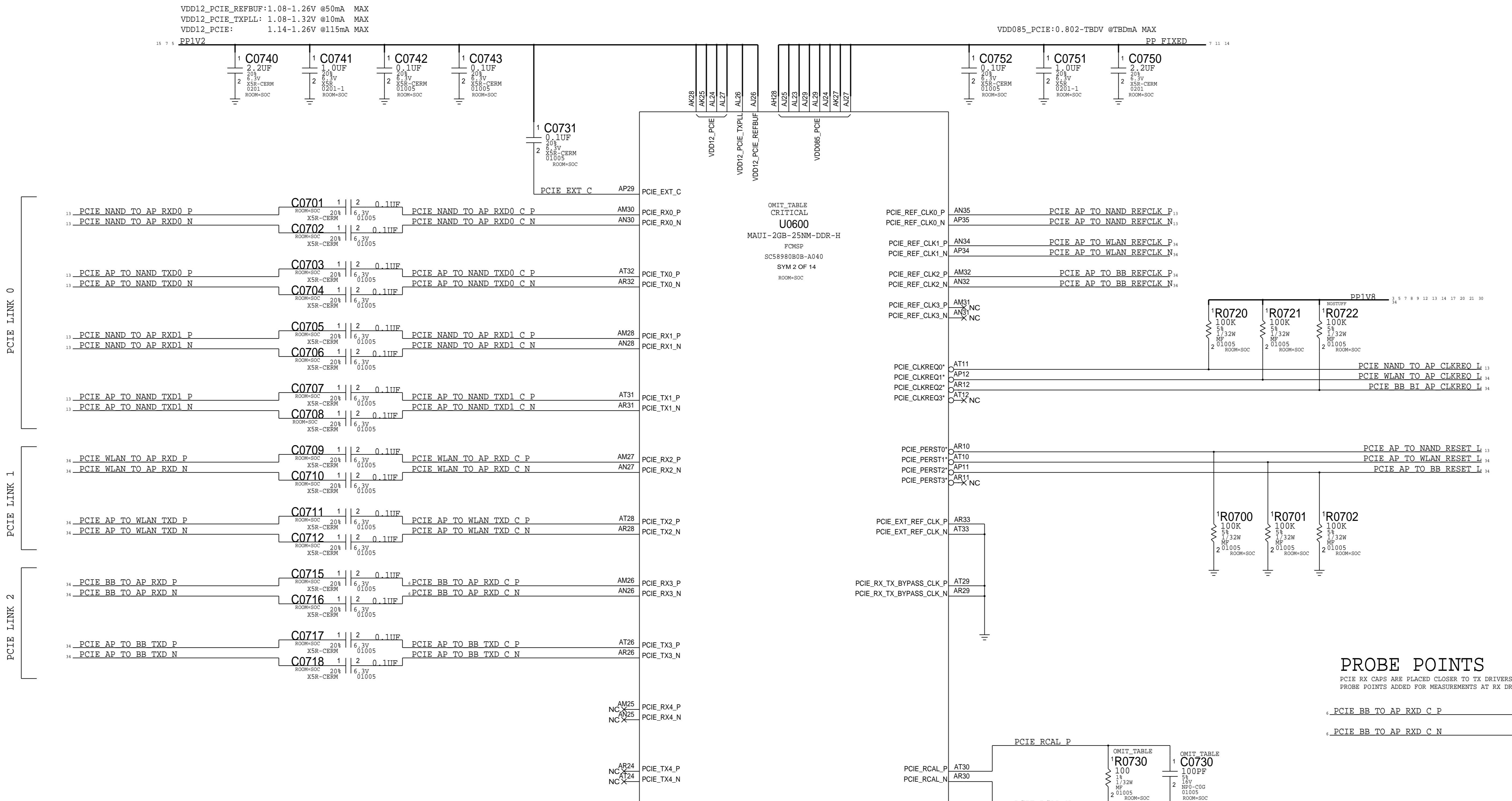
D



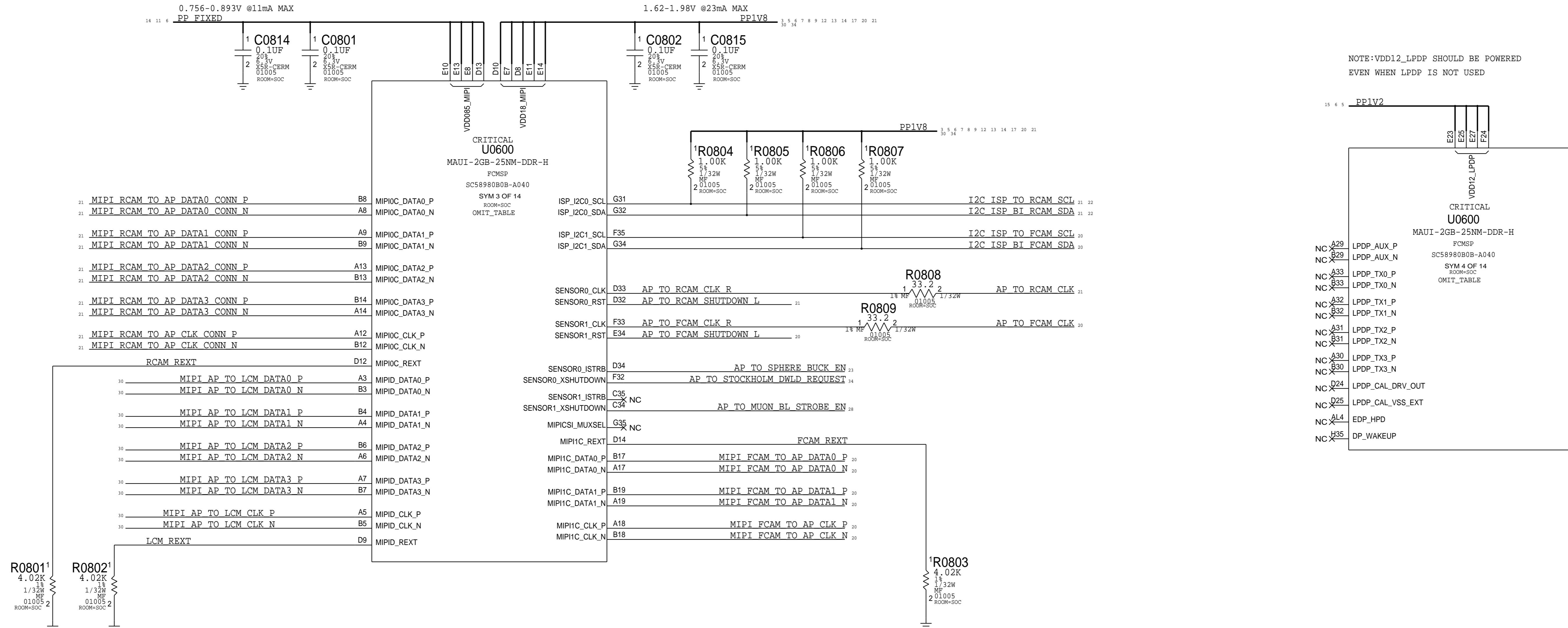
PROBE POINTS

- | | |
|-----------------------------------|-----------------|
| 31 5 <u>USB_AP_DATA_P</u> | 1 <u>PP0600</u> |
| 31 5 <u>USB_AP_DATA_N</u> | 1 <u>PP0601</u> |
| 16 5 <u>AP_TO_PMU_TEST_CLKOUT</u> | 1 <u>PP0610</u> |

MAUI - PCIE INTERFACES

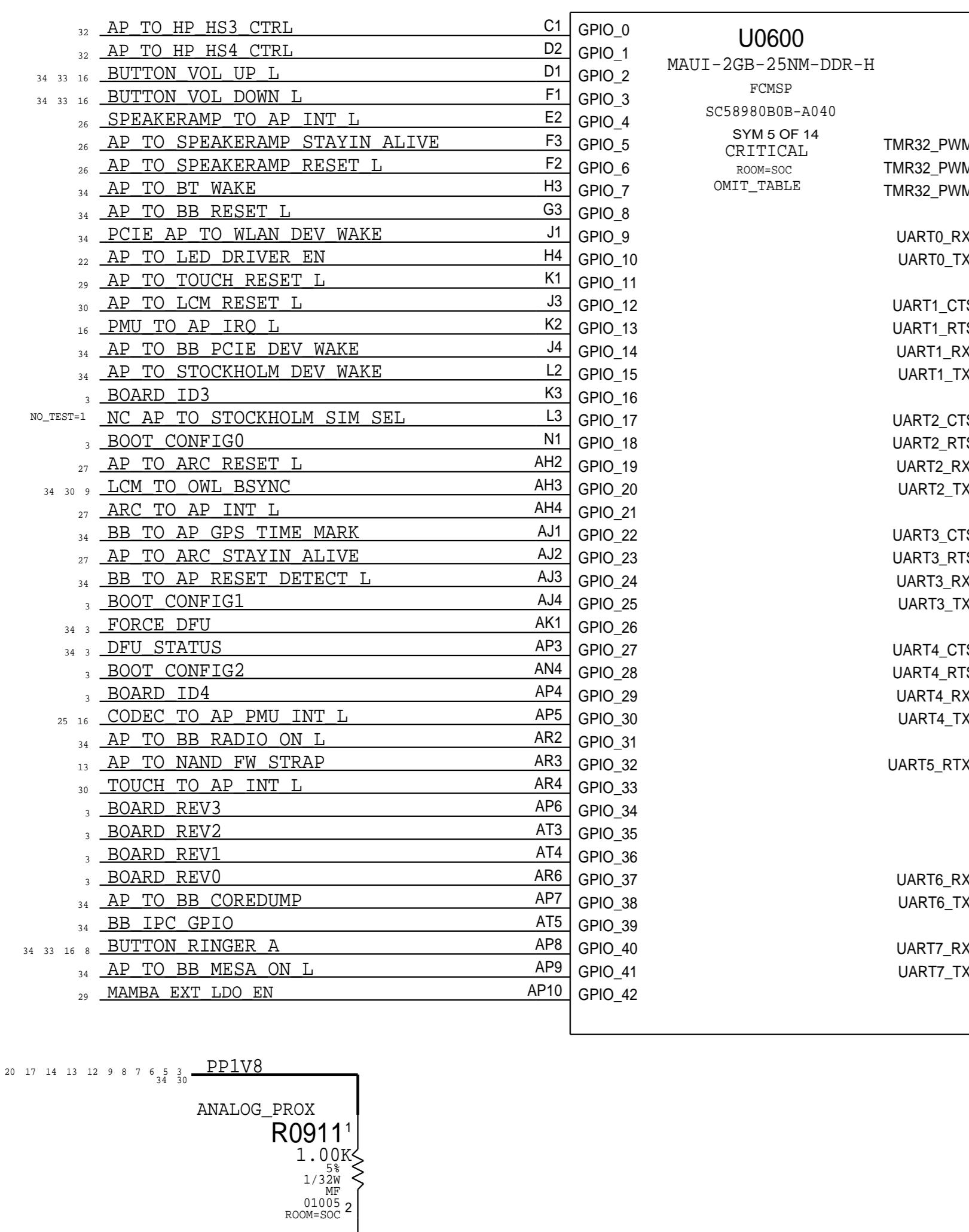


MAUI - CAMERA & DISPLAY INTERFACES



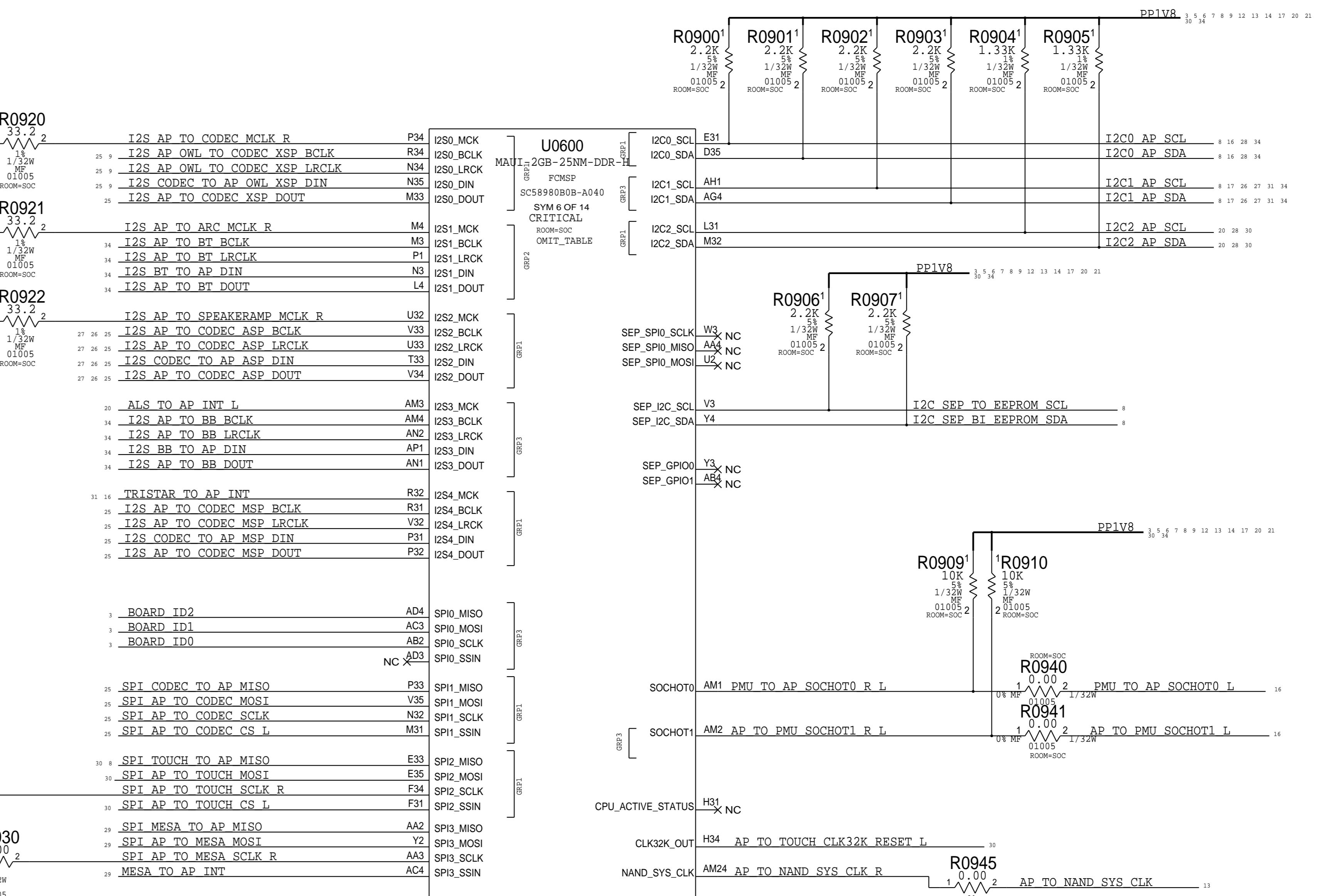
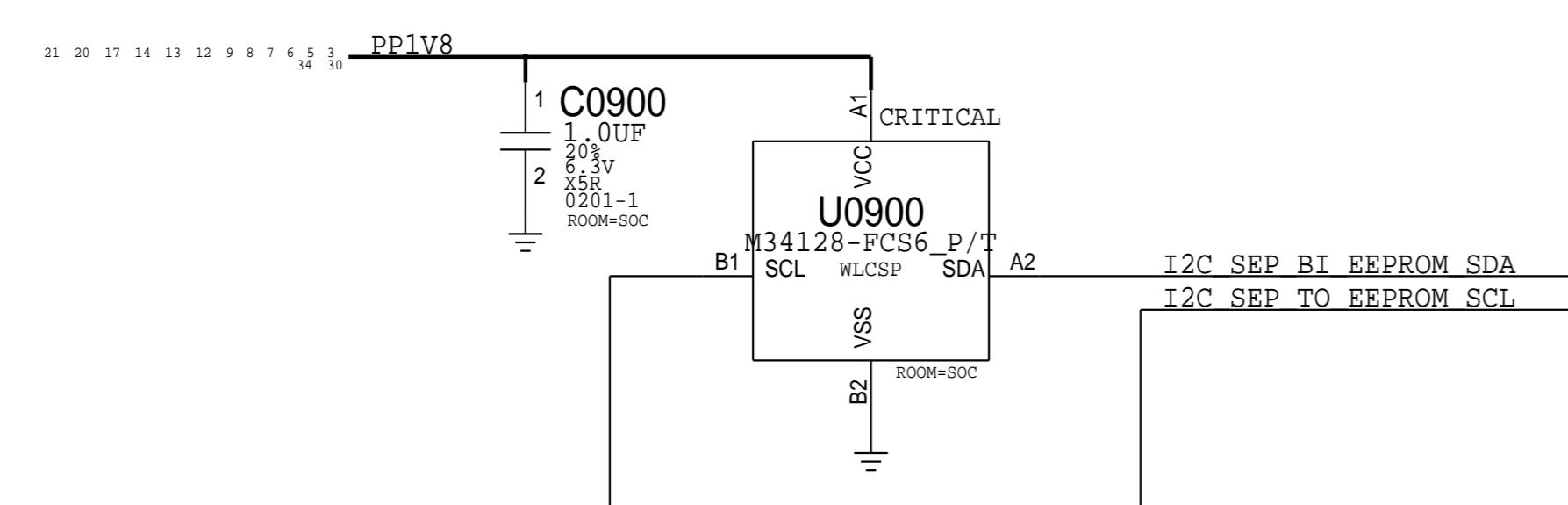
MAUI - GPIO & SERIAL INTERFACES

D



PIN J31 (UART7_RXD) SHOULD BE
SET TO INTERNAL PULL-DOWN.
STUFF R0911 FOR ANALOG PROX.
NOSTUFF R0911 FOR DOPPLER PROX.

ANTI-ROLLBACK EEPROM
128kbit
APN: 335S0946



SPI PROBE POINTS

30 8 SPI TOUCH TO AP MISO

1 SM PP PP0906

34 28 16 8 I2C0 AP SCL

1 SM PP P3MM-NSM ROOM=SOC PLACE_SIDE=TOP PP0900

34 28 16 8 I2C0 AP SDA

1 SM PP P3MM-NSM ROOM=SOC PLACE_SIDE=TOP PP0901

34 31 27 17 8 I2C1 AP SCL

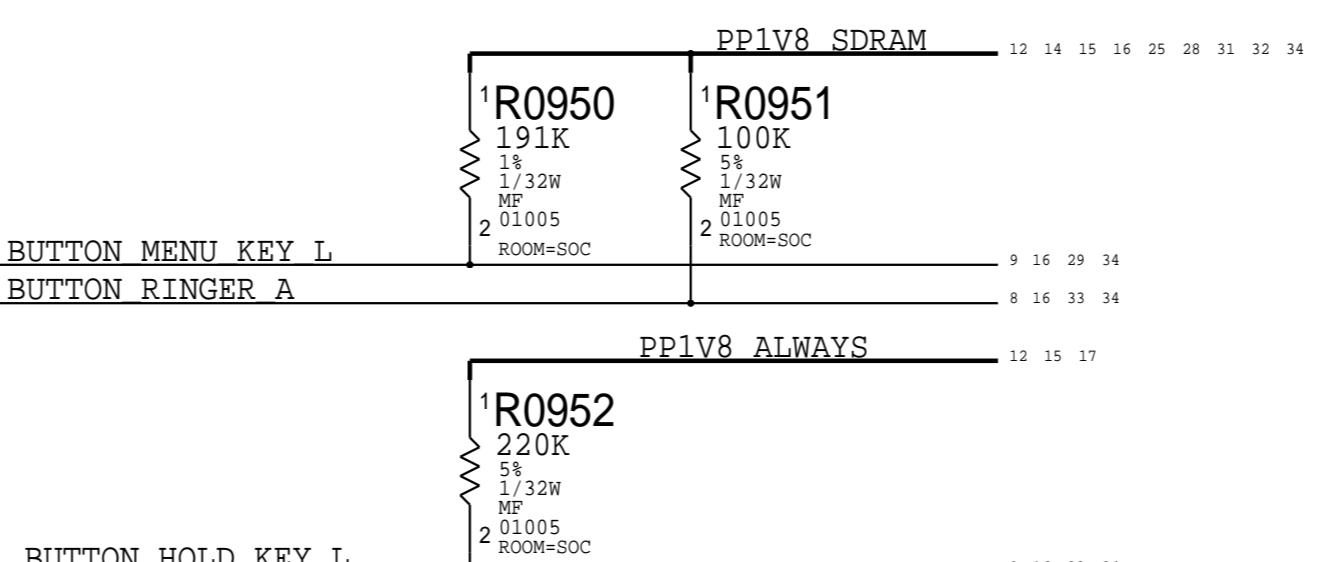
1 SM PP P3MM-NSM ROOM=SOC PLACE_SIDE=TOP PP0902

34 31 27 26 17 8 I2C1 AP SDA

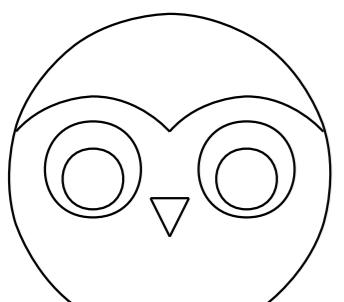
1 SM PP P3MM-NSM ROOM=SOC PLACE_SIDE=TOP PP0903

I2C PROBE POINTS

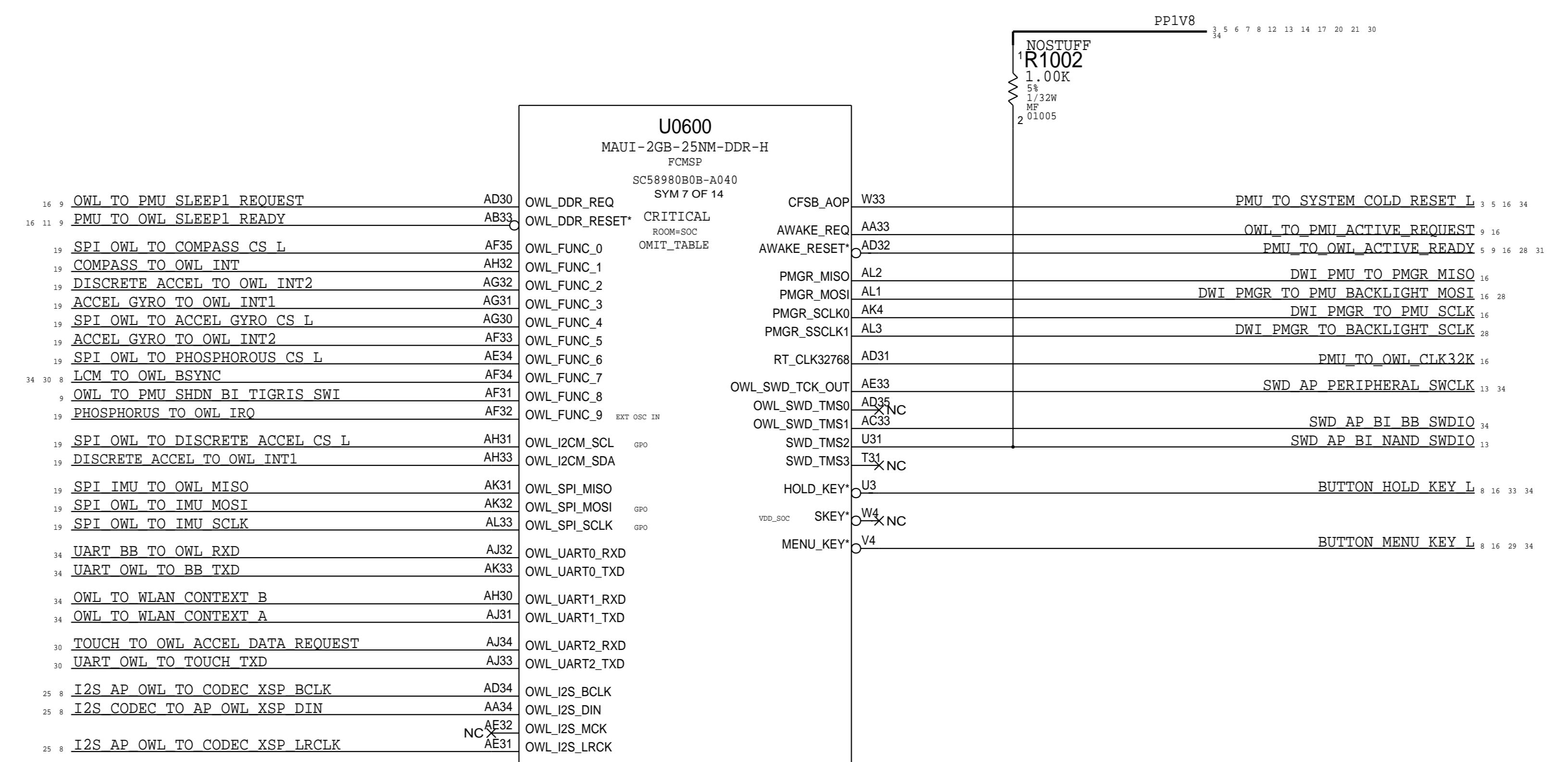
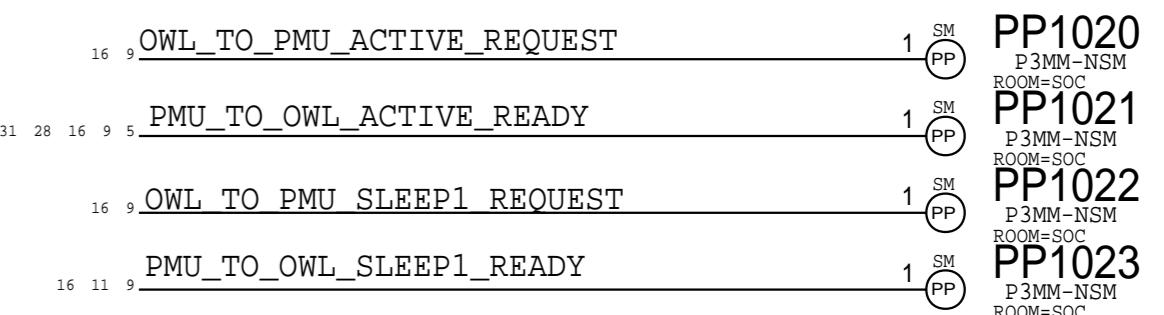
BUTTON PULL-UP RESISTORS



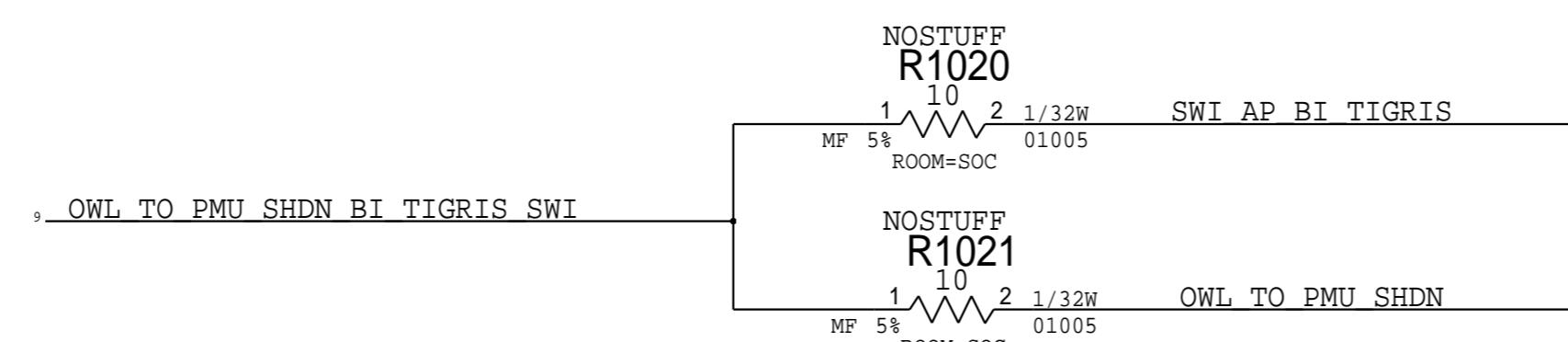
MAUI - OWL



POWER STATE CONTROL PROBE POINTS



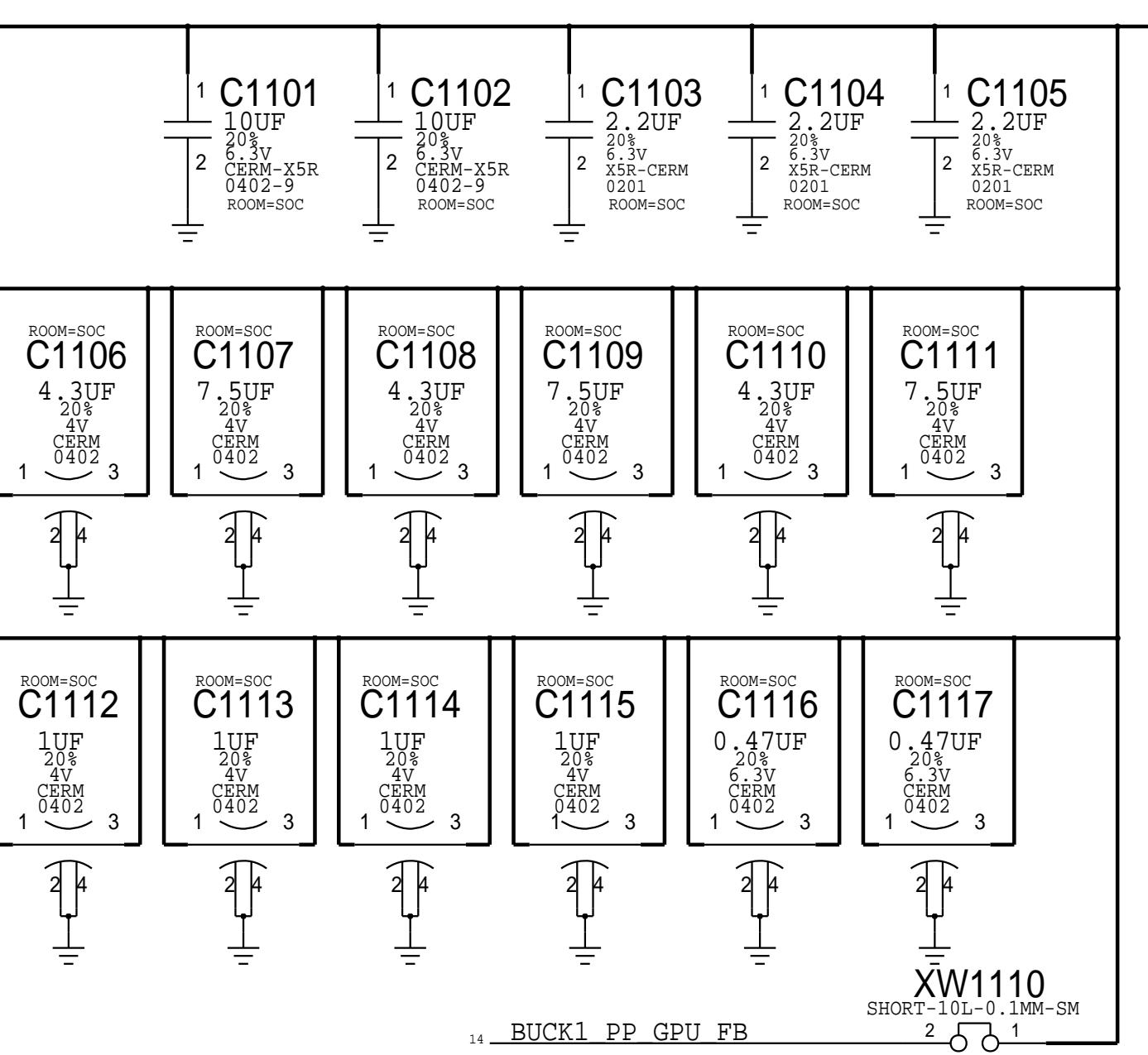
OWL SYSTEM SHUTDOWN OPTION



MAUI - CPU, GPU & SOC RAILS

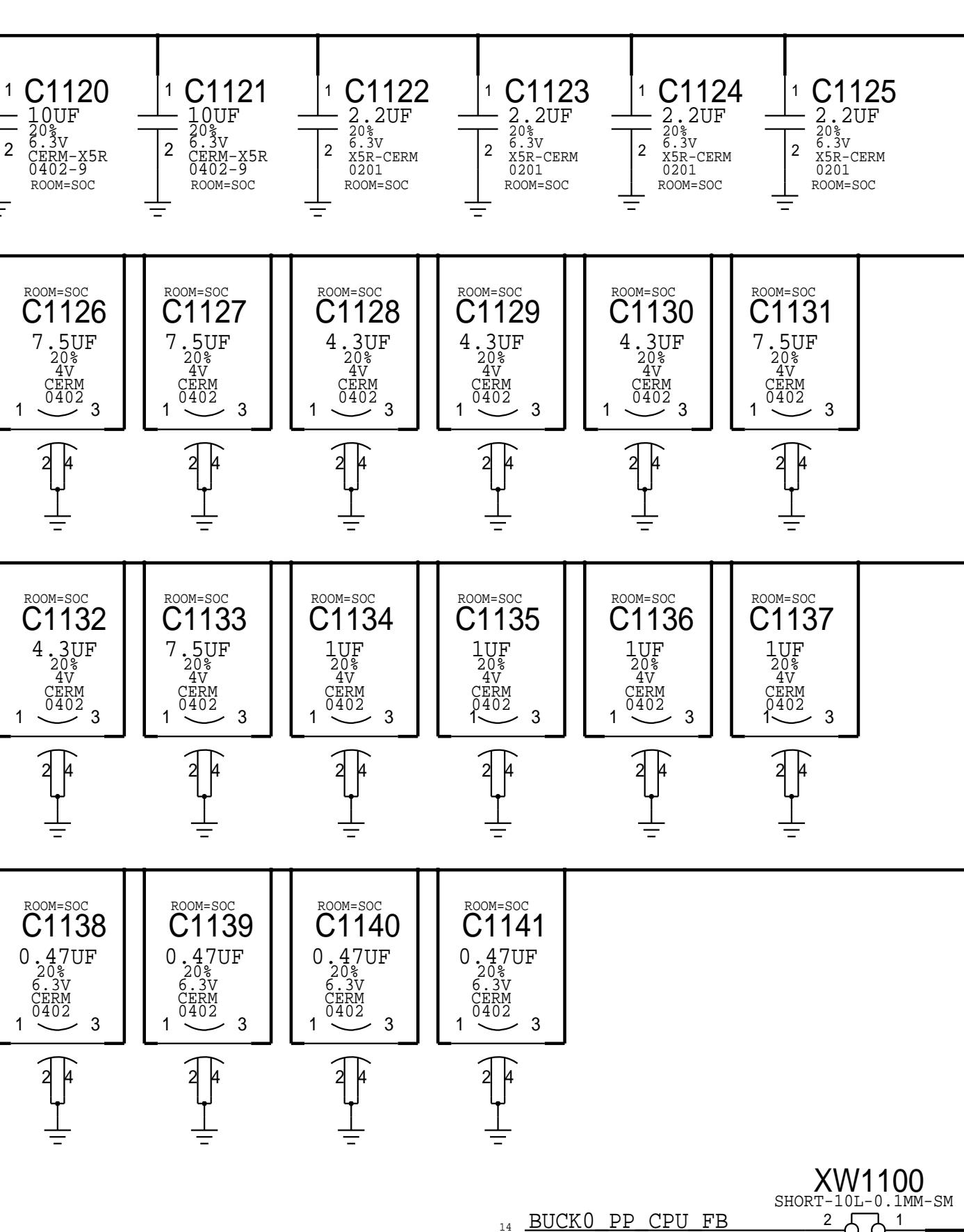
PP_GPU
0.8V @10.5A MAX

TP1120
0.50MM
SM 1 PP_PP GPU



PP_CPU
0.625V @TBDA MAX
0.9V @10.5A MAX
1.0V @12.5A MAX

TP1100
0.50MM
SM 1 PP_PP CPU



PP1100 SM 1 AP CPU SENSE P
PP1101 SM 1 AP CPU SENSE N

Y6

VDD_CPU_SENSE

VDD_GPU_SENSE G20 AP GPU SENSE P

Y7

VSS_CPU_SENSE

H19 AP GPU SENSE N

Y8

VSS_GPU_SENSE

PP1102 SM 1 AP GPU SENSE P
PP1104 SM 1 AP GPU SENSE N

AJ20

VDD_SOC_SENSE

G21

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

AK21

VSS_SOC_SENSE

T22

T24

W22

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

V14

U11

T16

T10

G9

R23

G7

T9

T11

T13

T15

T17

P7

T23

T25

T27

T30

T35

U6

U10

U12

W11

Y28

V20

V16

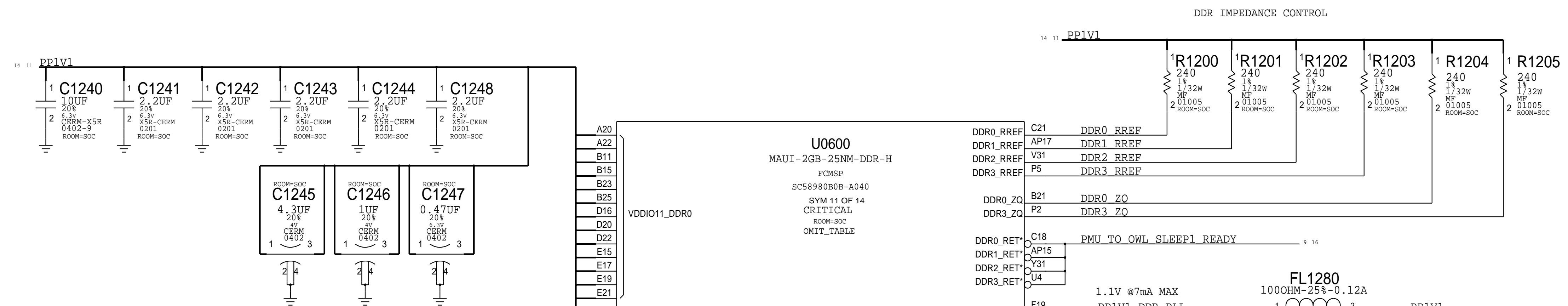
V14

U11

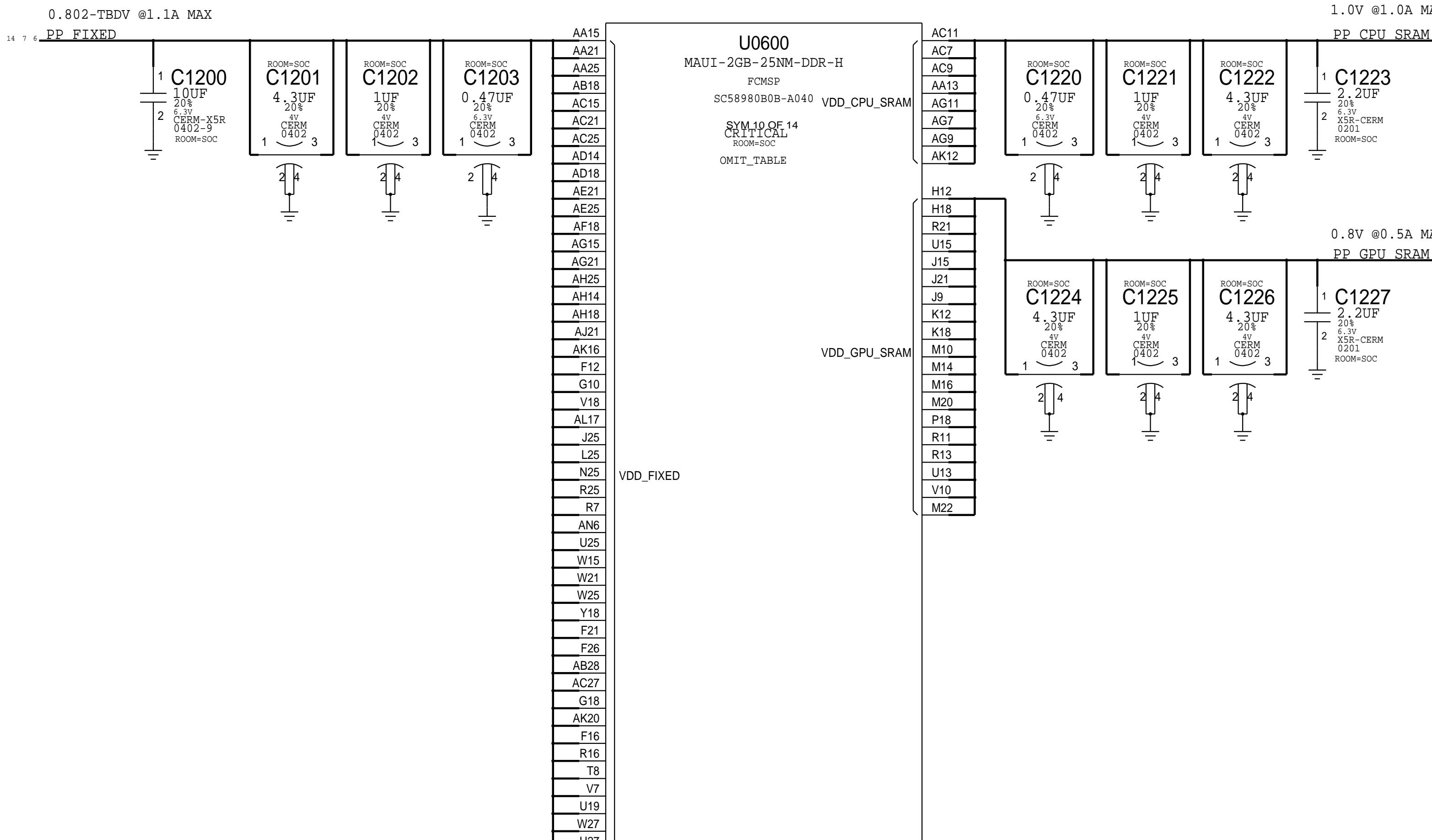
T16</p

MAUI - POWER SUPPLIES

D

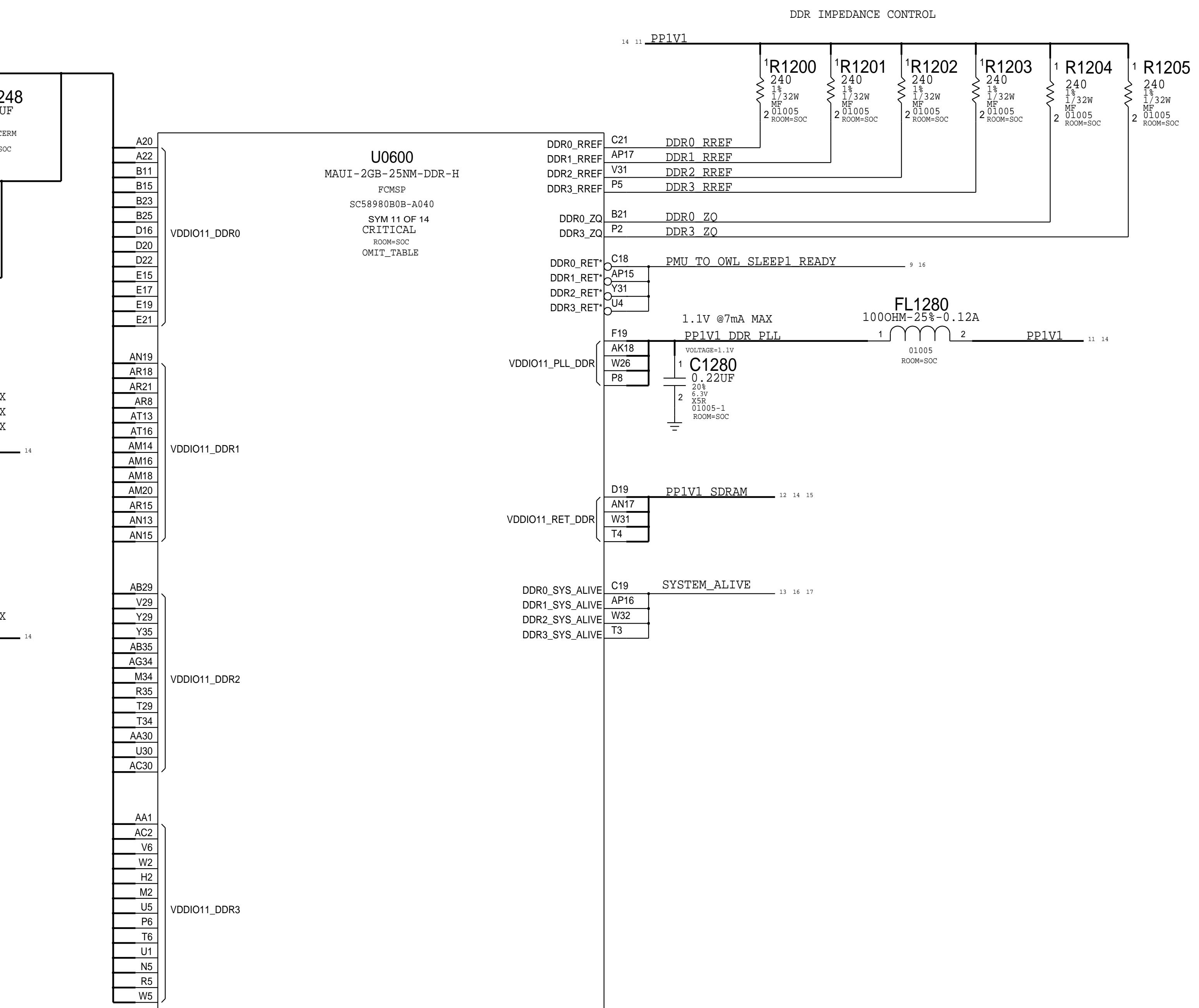
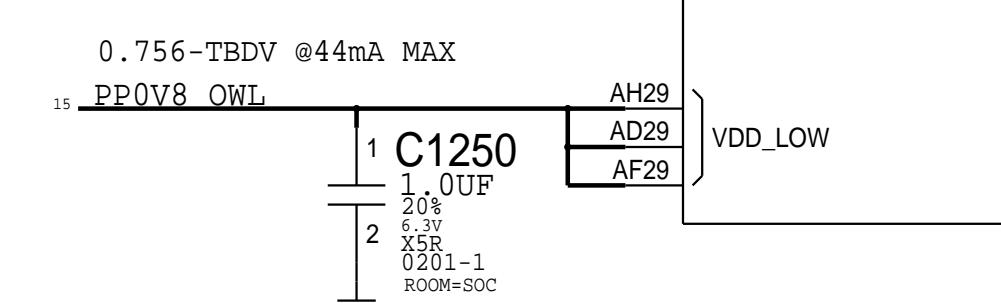


C

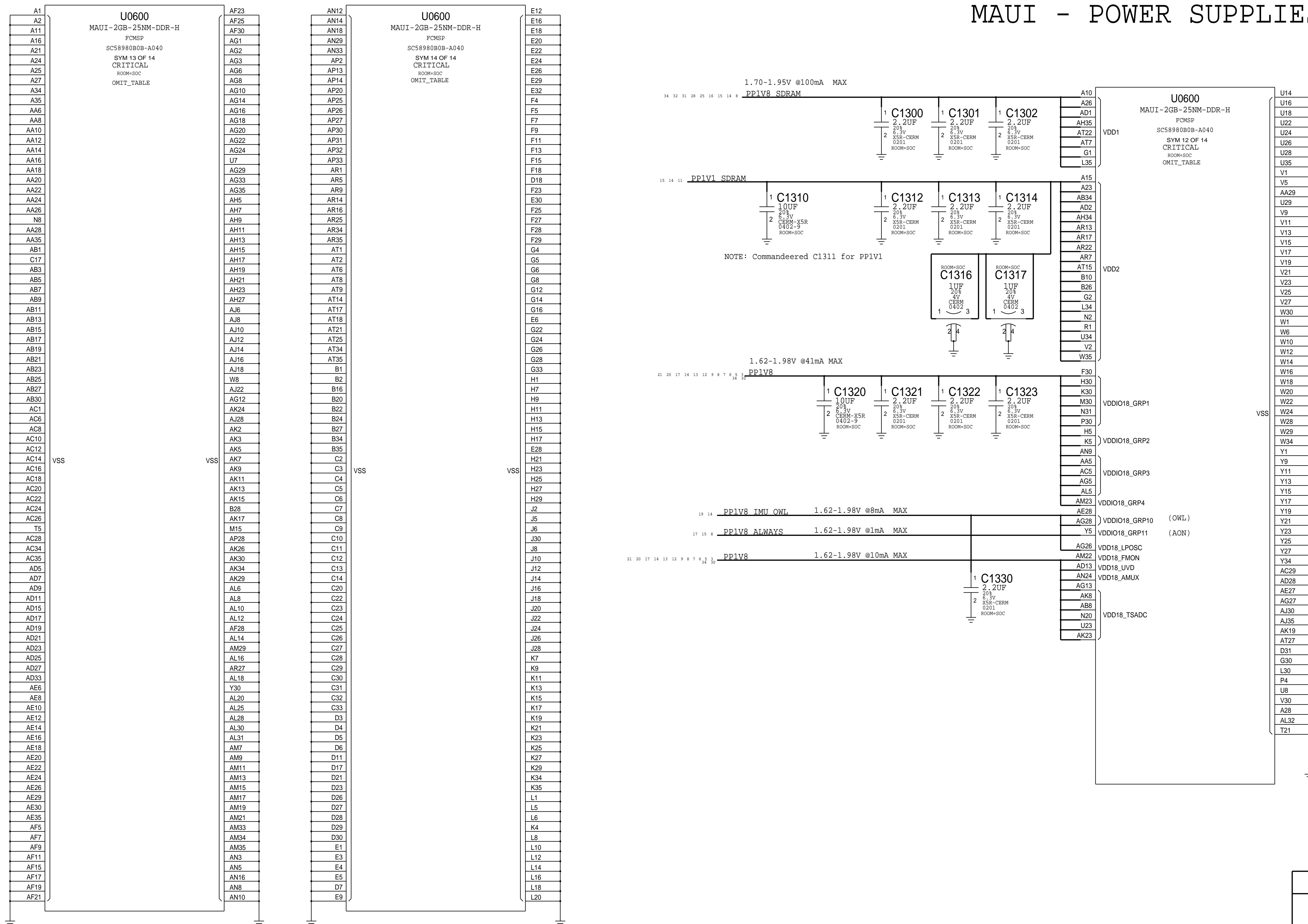


B

A

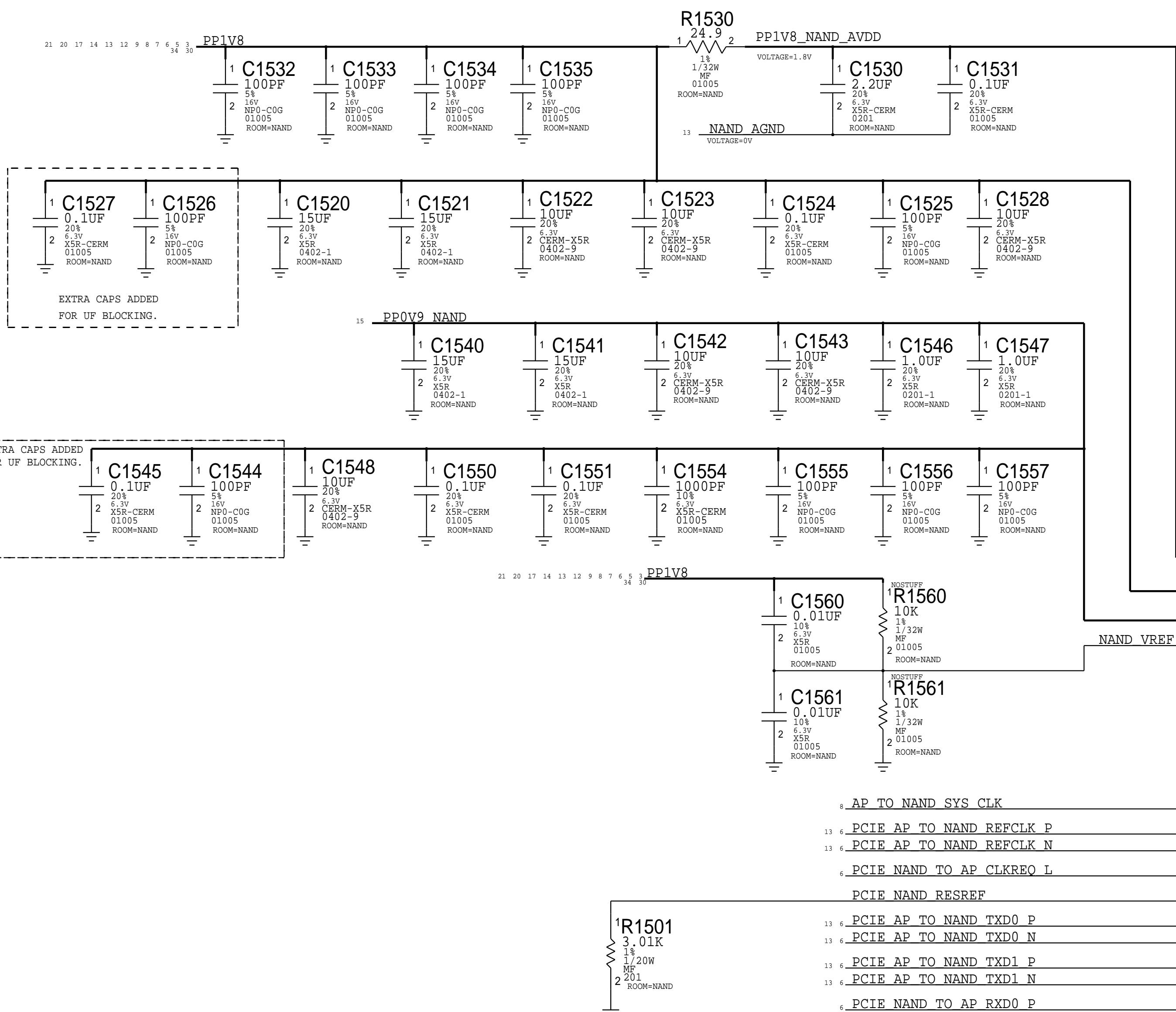


MAUI - POWER SUPPLIES

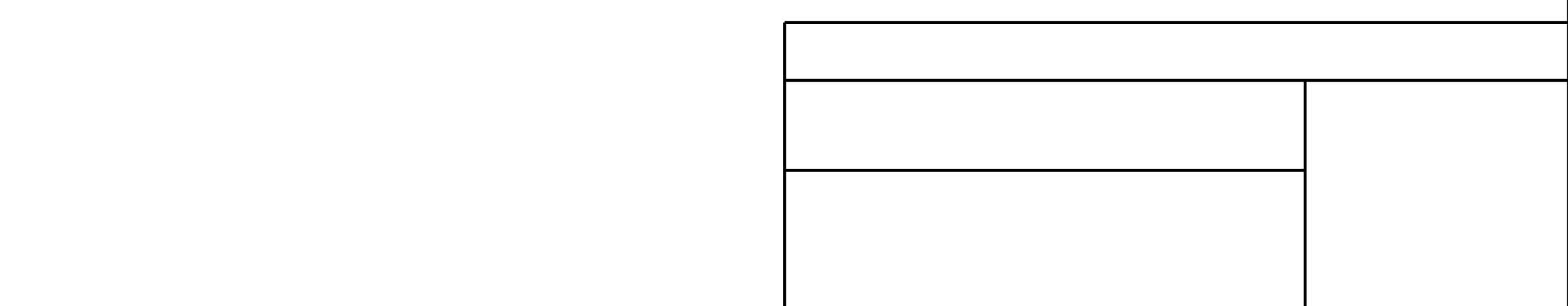
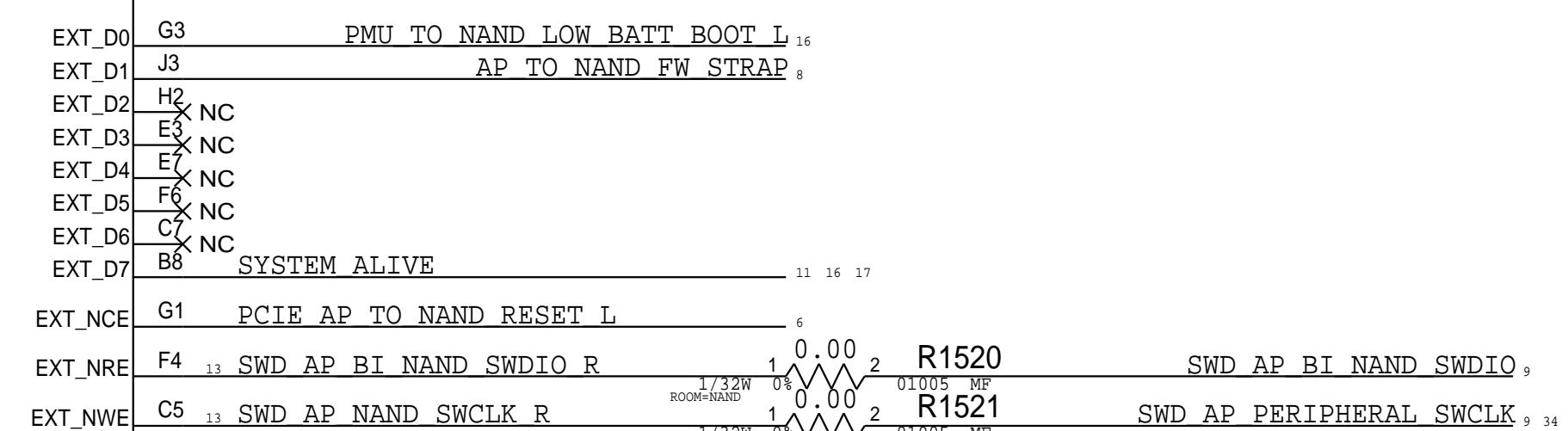
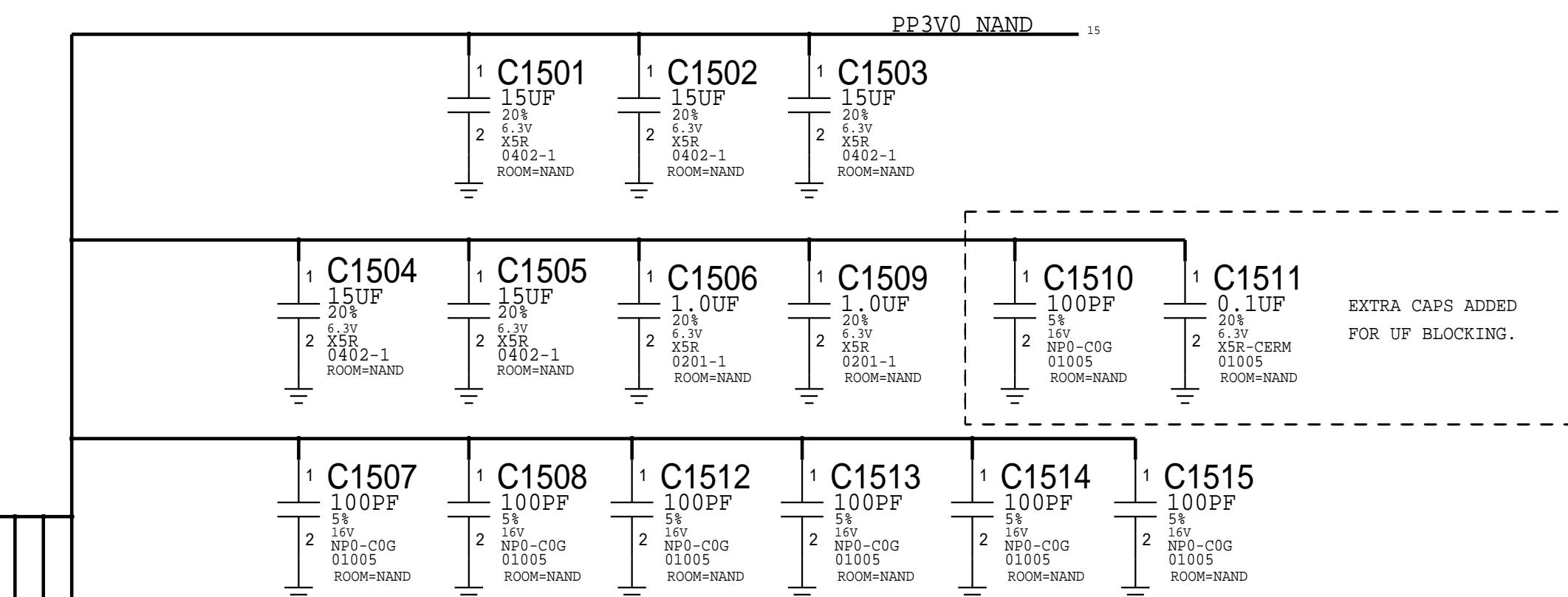
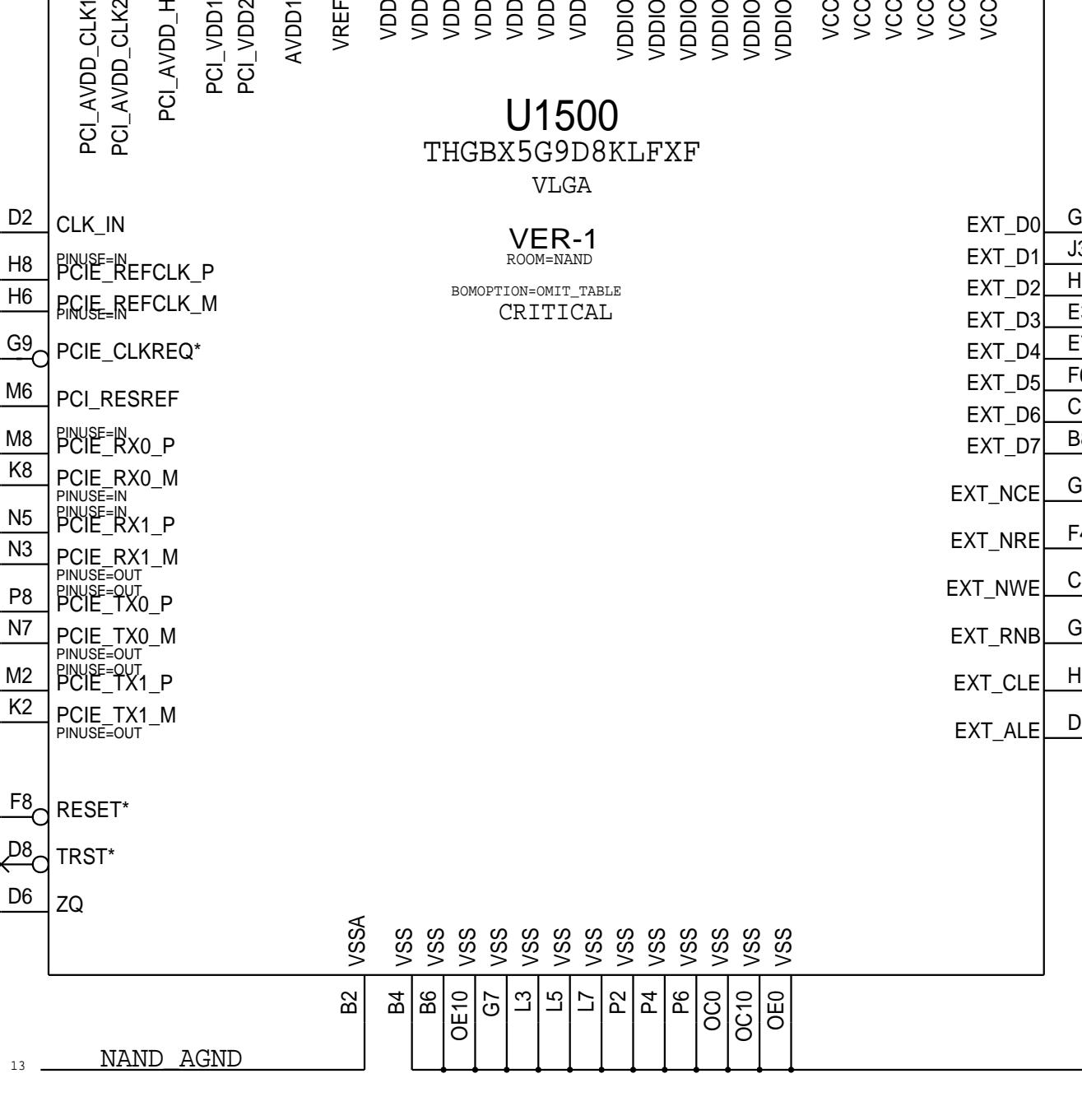
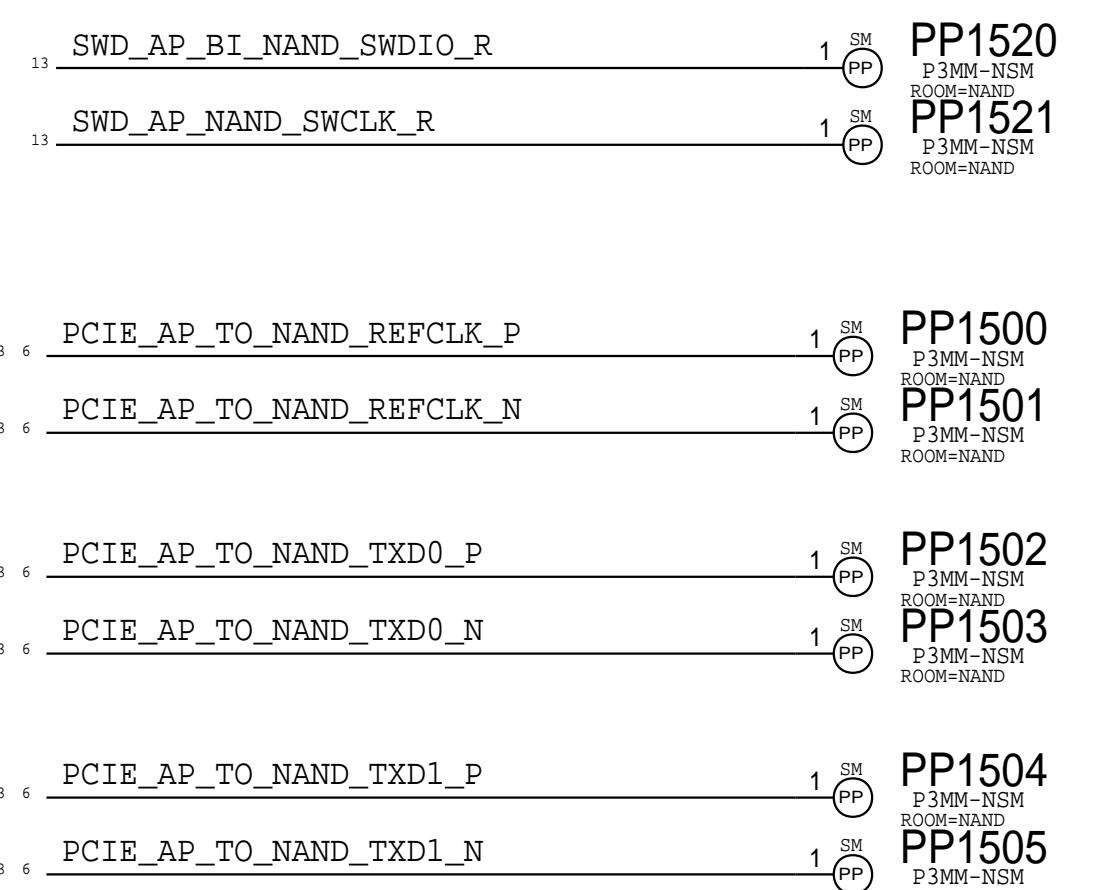


S3E NAND

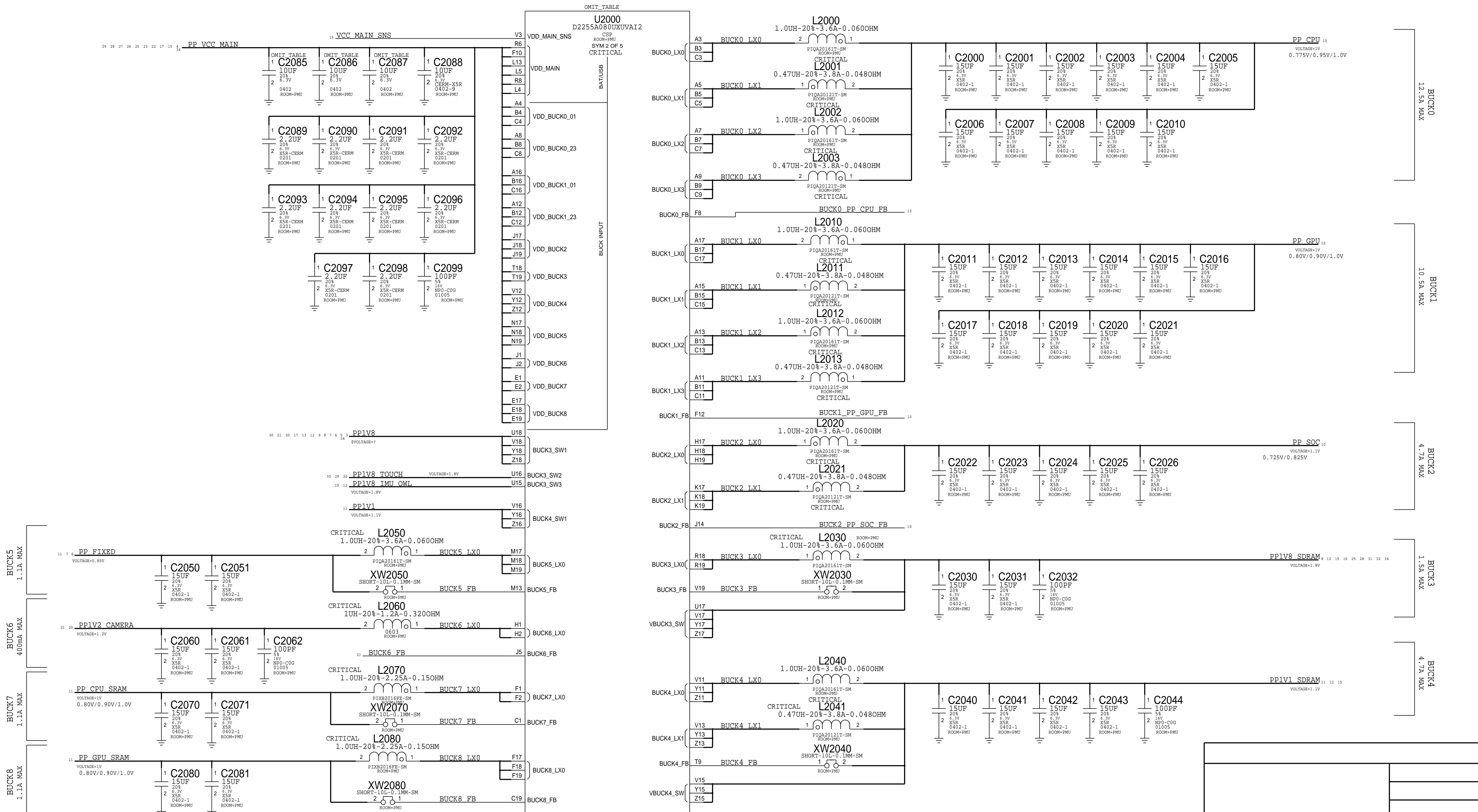
D



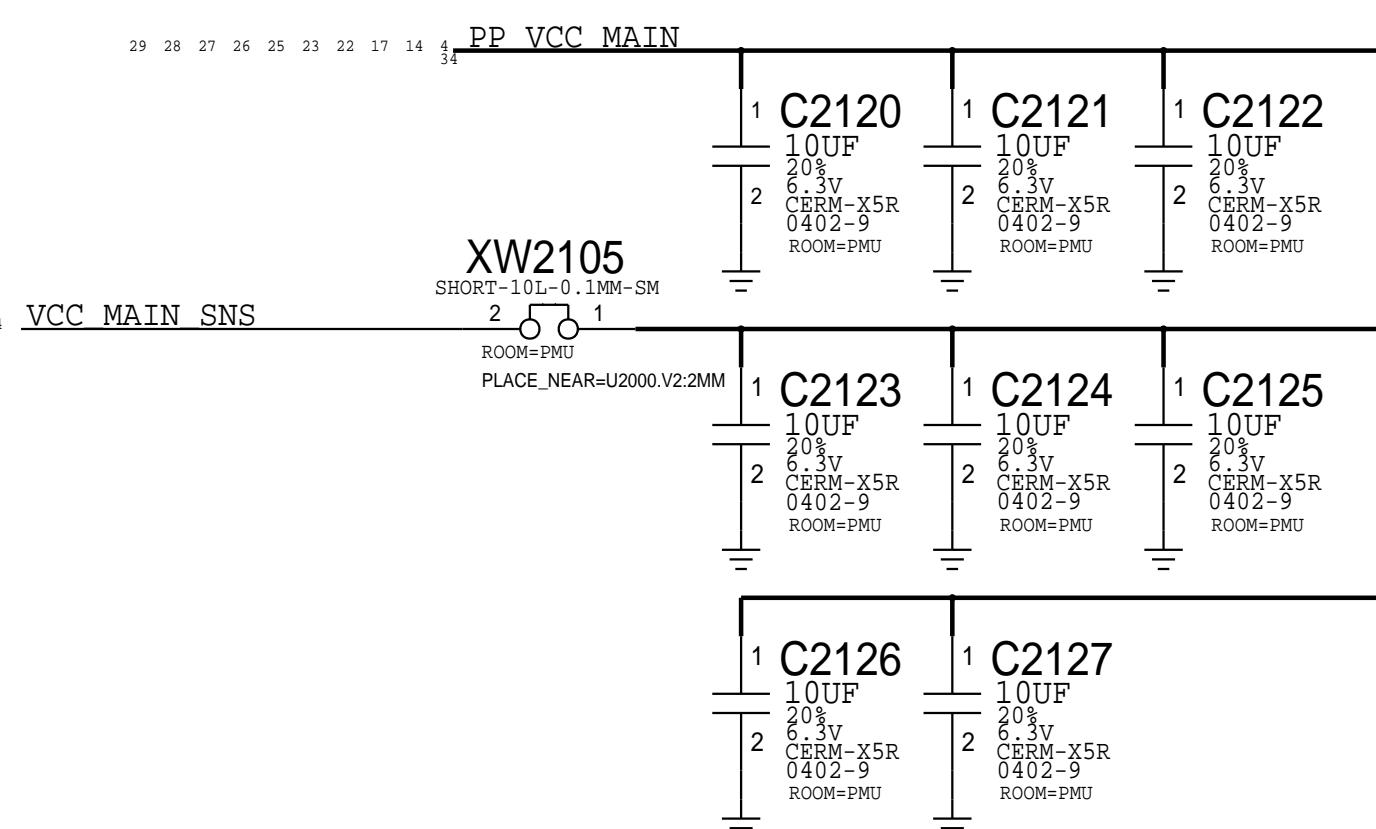
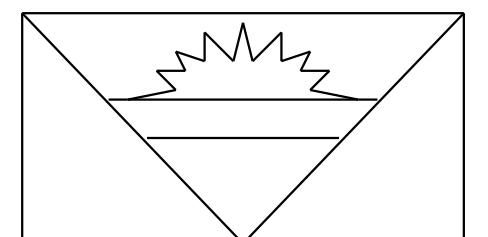
PROBE POINTS



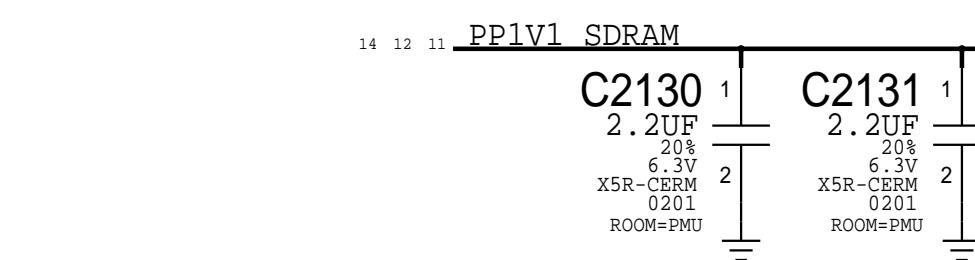
ANTIGUA PMU - Buck Supplies



ANTIGUA PMU - LDOs



OMIT_TABLE U2000
D2255A080UXUVAI2
CSP
SYM 1 OF 5

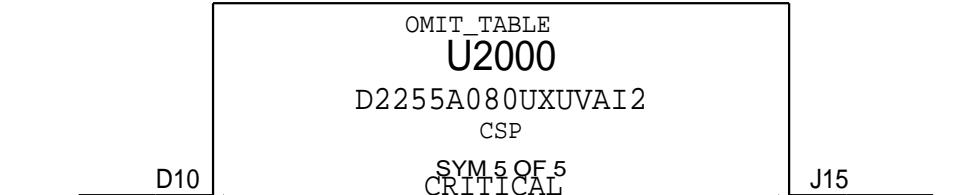


LDO INPUT

M3 VDD_LDO1_3CRITICAL
V2 VDD_LDO2
V3 VDD_LDO4
U1 VDD_LDO5
L2 VDD_LDO6
Y6 VDD_LDO7
Y4 VDD_LDO8
Y3 VDD_LDO9
Y9 VDD_LDO10
Z9 VDD_LDO11
Y5 VDD_LDO13
Y7 VDD_LDO14
N2 VDD_LDO15
K3 VDD_BYPASS
P12 VPP OTP

VPUMP U19 PMU VPUMP

VPUMP: 10nF min. @ 4.6V



J15

J16

J3

J4

K15

K16

L15

L16

M14

M15

M16

N14

N15

N16

P13

P14

P15

P16

P17

R13

B6

C10

C14

C18

C2

C6

D1

D19

E14

G1

G17

G18

G19

H7

J6

K12

K7

L17

L18

L19

L6

V4

V5

V6

NC

A1

A10

A14

A18

A19

A2

A6

B1

B10

B14

B18

B19

B2

B6

C10

C14

C18

C2

C6

T3

T6

T8

U3

U9

V10

V14

V8

V9

Y1

Y10

Y14

Y19

Z1

Z10

Z14

Z19

Z2

OMIT_TABLE U2000
D2255A080UXUVAI2
CSP
SYM 4 OF 5
CRITICAL
ROOM=PMU

A1

A10

A14

A18

A19

A2

A6

B1

B10

B14

B18

B19

B2

B6

C10

C14

C18

C2

C6

T3

T6

T8

U3

U9

V10

V14

V8

V9

Y1

Y10

Y14

Y19

Z1

Z10

Z14

Z19

Z2

Xtal GND

XTAL GND

VSS

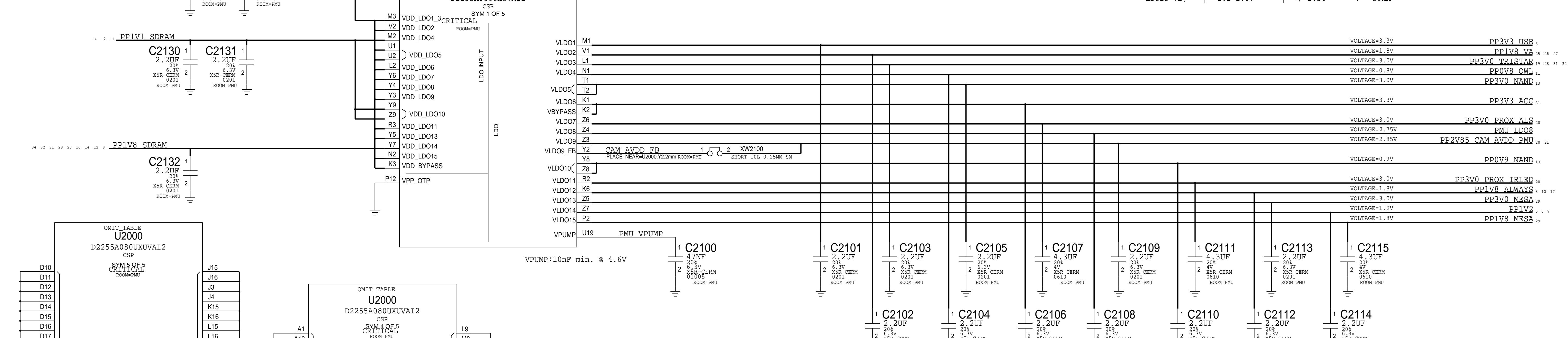
PMU_VSS_RTC

16

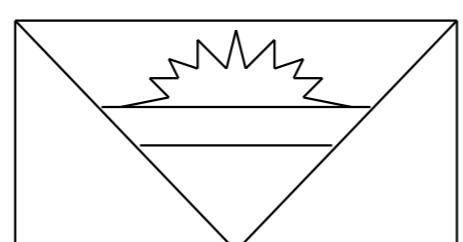
ANTIGUA LDO SPECS

LDO#	ADJ. RANGE	ACCURACY	MAX. CURRENT
LDO1 (A)	2.5-3.3V	+/-1.4%	50mA
LDO2 (B)	1.2-2.0V	+/-2.5%	50mA
LDO3 (A)	2.5-3.3V	+/-1.4%	50mA
LDO4 (D)	0.7-1.2V	+/-2.5%	100mA
LDO5 (F)	2.5-3.3V	+/-2.5%	1000mA
LDO6 (C1)	1.2-3.6V	+/-2.5%	150mA
LDO7 (C)	2.5-3.3V	+/-25mV	250mA
LDO8 (C)	2.5-3.3V	+/-25mV	250mA
LDO9 (C)	2.5-3.3V	+/-25mV	250mA
LDO10 (G)	0.7-1.2V	+/-5.5%	1335mA
LDO11 (C)	2.5-3.3V	+/-25mV	250mA
LDO12 (E)	1.8V	+/-5%	10mA
LDO13 (C)	2.5-3.3V	+/-25mV	250mA
LDO14 (H)	0.8-1.5V	+/-2.5%	250mA
LDO15 (B)	1.2-2.0V	+/-2.5%	50mA

VOLTAGE=3.3V
PP3V3 USB
VOLTAGE=1.8V
PP1V8 VA
VOLTAGE=3.0V
PP3V0 TRISTAR
VOLTAGE=0.8V
PP0V8 OWL
VOLTAGE=3.0V
PP3V0 NAND
VOLTAGE=3.3V
PP3V3 ACC
VOLTAGE=3.0V
PP1V8 ALS
VOLTAGE=2.75V
PMU LDO8
VOLTAGE=2.85V
PP2V85 CAM AVDD PMU
VOLTAGE=0.9V
PP0V9 NAND
VOLTAGE=3.0V
PP3V0 PROX IRLED
VOLTAGE=1.8V
PP1V8 ALWAYS
VOLTAGE=3.0V
PP3V0 MESA
VOLTAGE=1.2V
PP1V2
VOLTAGE=1.8V
PP1V8 MESA



ANTIGUA PMU - GPIOs, NTCs



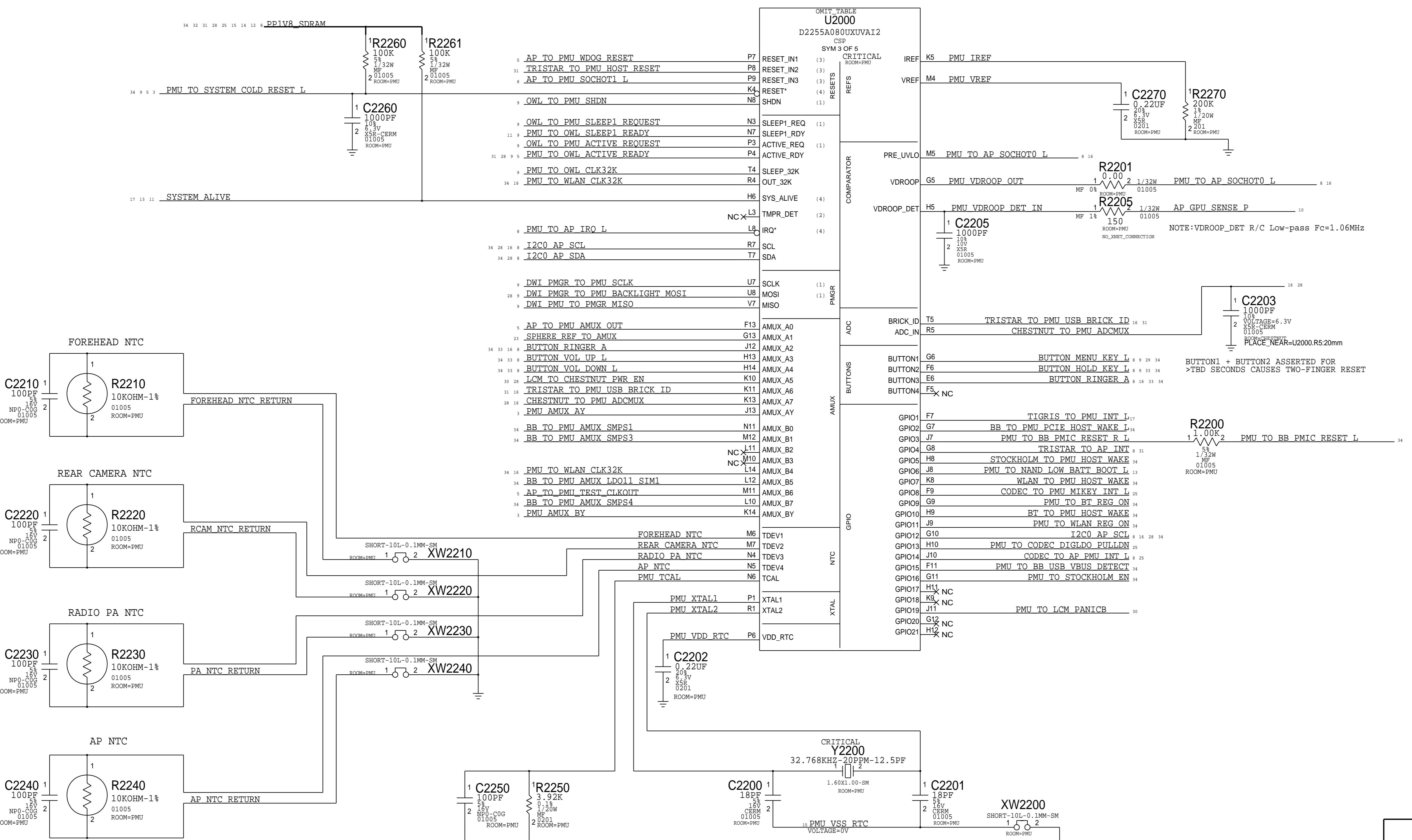
CONTROL PIN NOTES:

NOTE (1): INPUT PULL-DOWN 100-300k

NOTE (2): INPUT PULL-DOWN 1M

NOTE (3): INPUT PULL-UP OR DOWN 100k-300k

NOTE (4): OUTPUT OPEN-DRAIN, REQUIRES PULL-UP

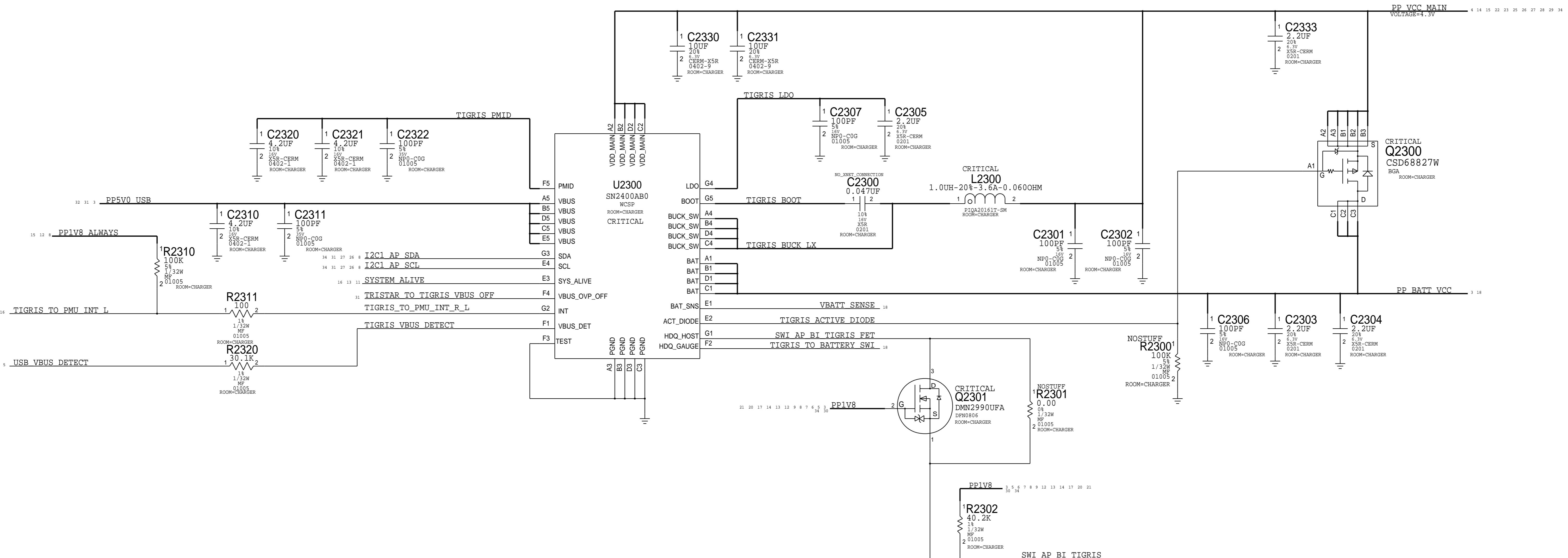


D

D

TIGRIS CHARGER

APN: 343S0693



A

A

D

D

C

C

B

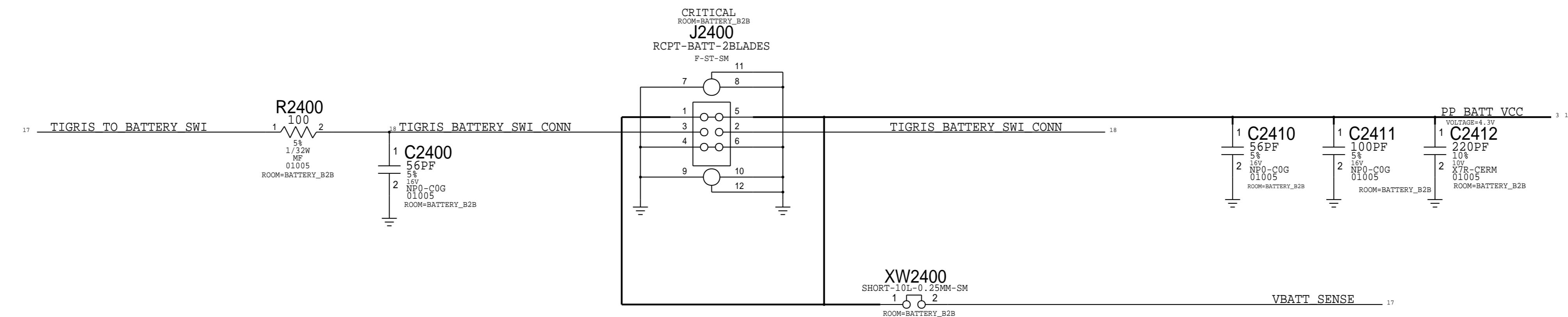
B

A

A

BATTERY CONNECTOR

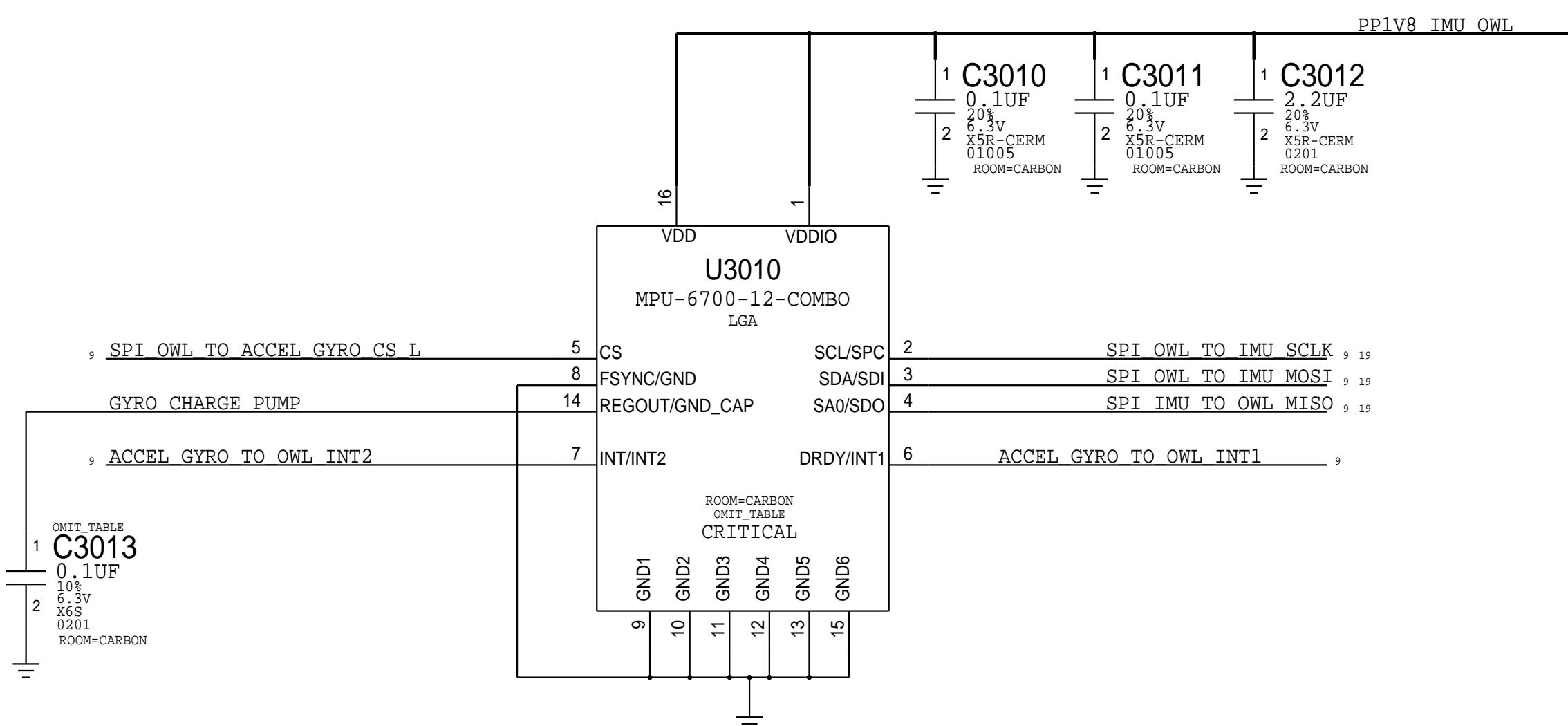
THIS ONE ON MLB ---> 516S00104 (RCPT)
516????? (PLUG)



D

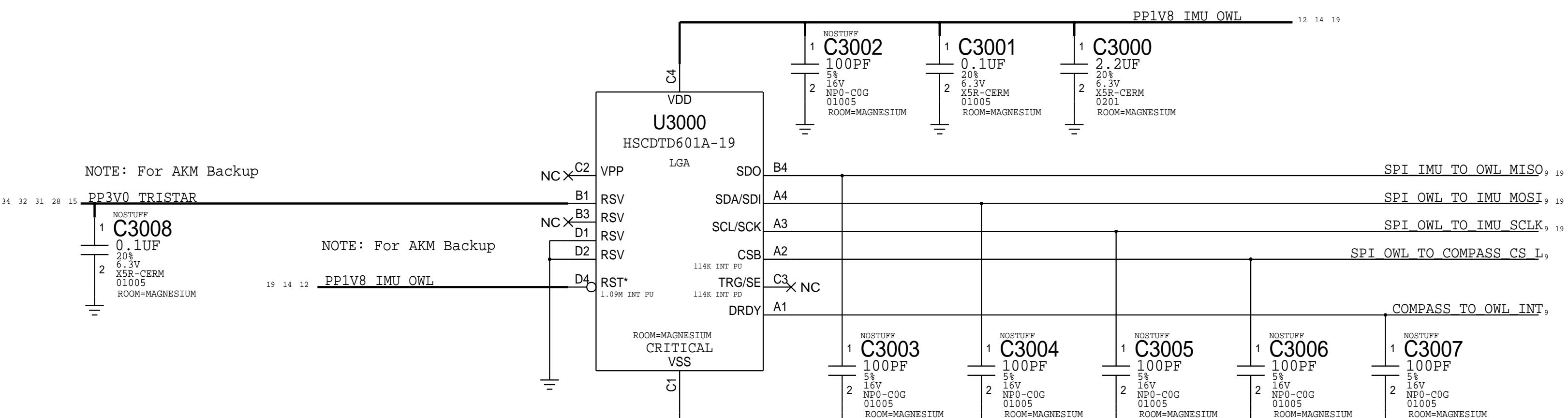
CARBON - ACCEL & GYRO

ST (APN 338S00029): C3013=0.01UF
 INVENSENSE, MPU-6700 (APN 338S00017): C3013=0.1UF
 DOE INVENSENSE, MPU-6800 (APN 338S00087): C3013=0.1UF



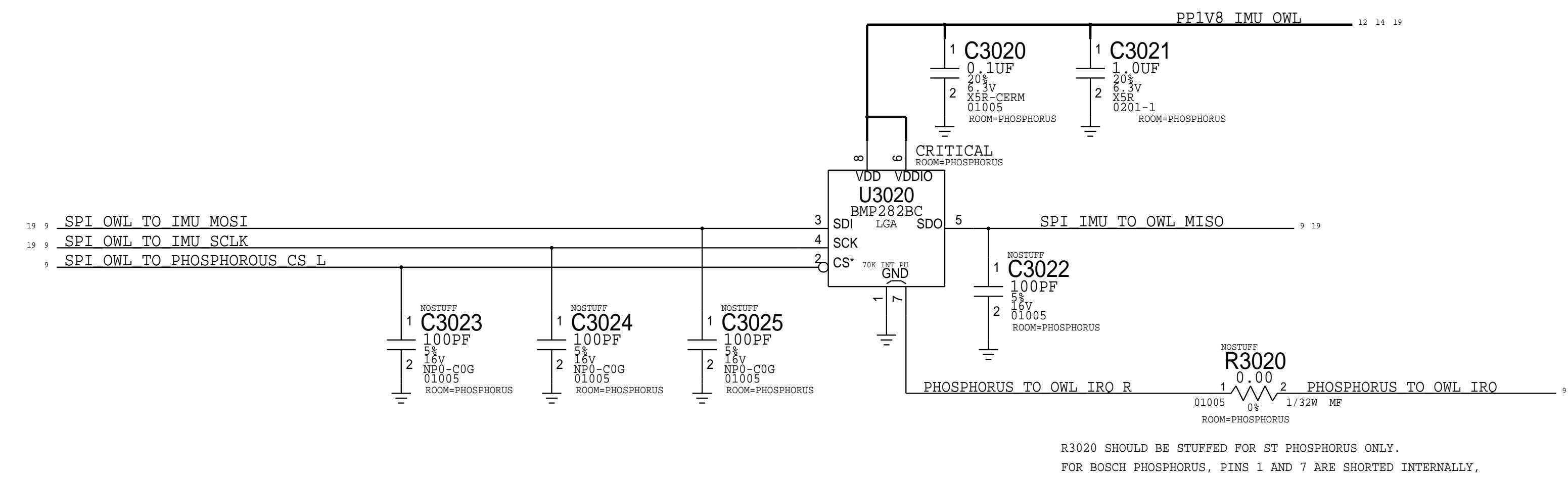
MAGNESIUM - COMPASS

APN:338S00084



PHOSPHOROUS

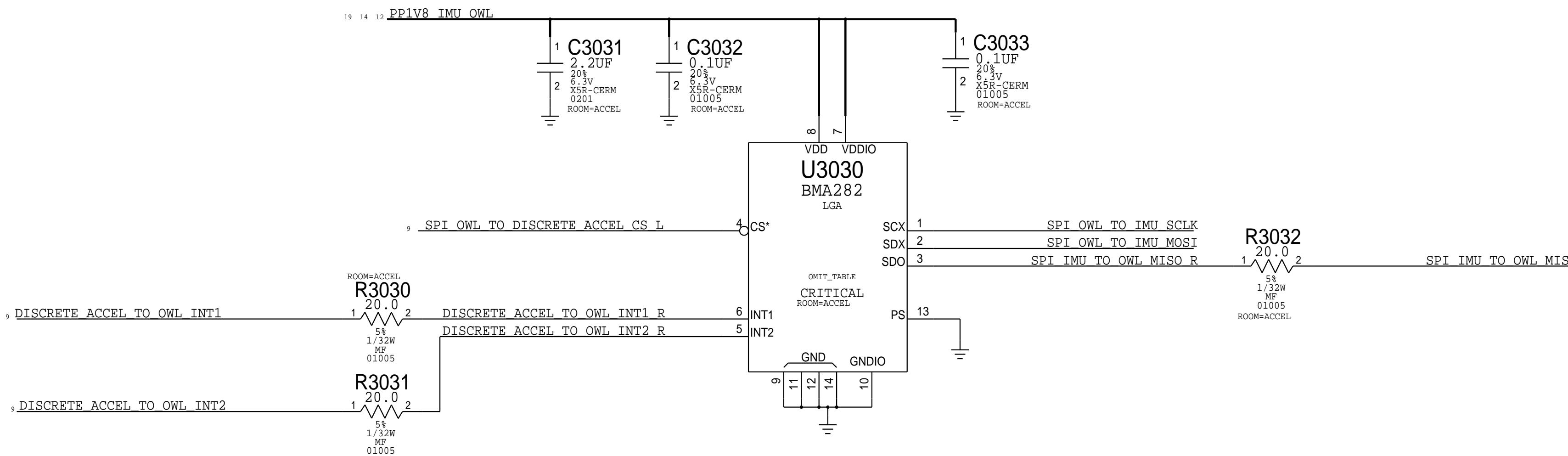
BOSCH (APN:338S00044)



B

DISCRETE ACCEL

BOSCH APN 338S1163
 NO-STUFF for Invensense DOE



D

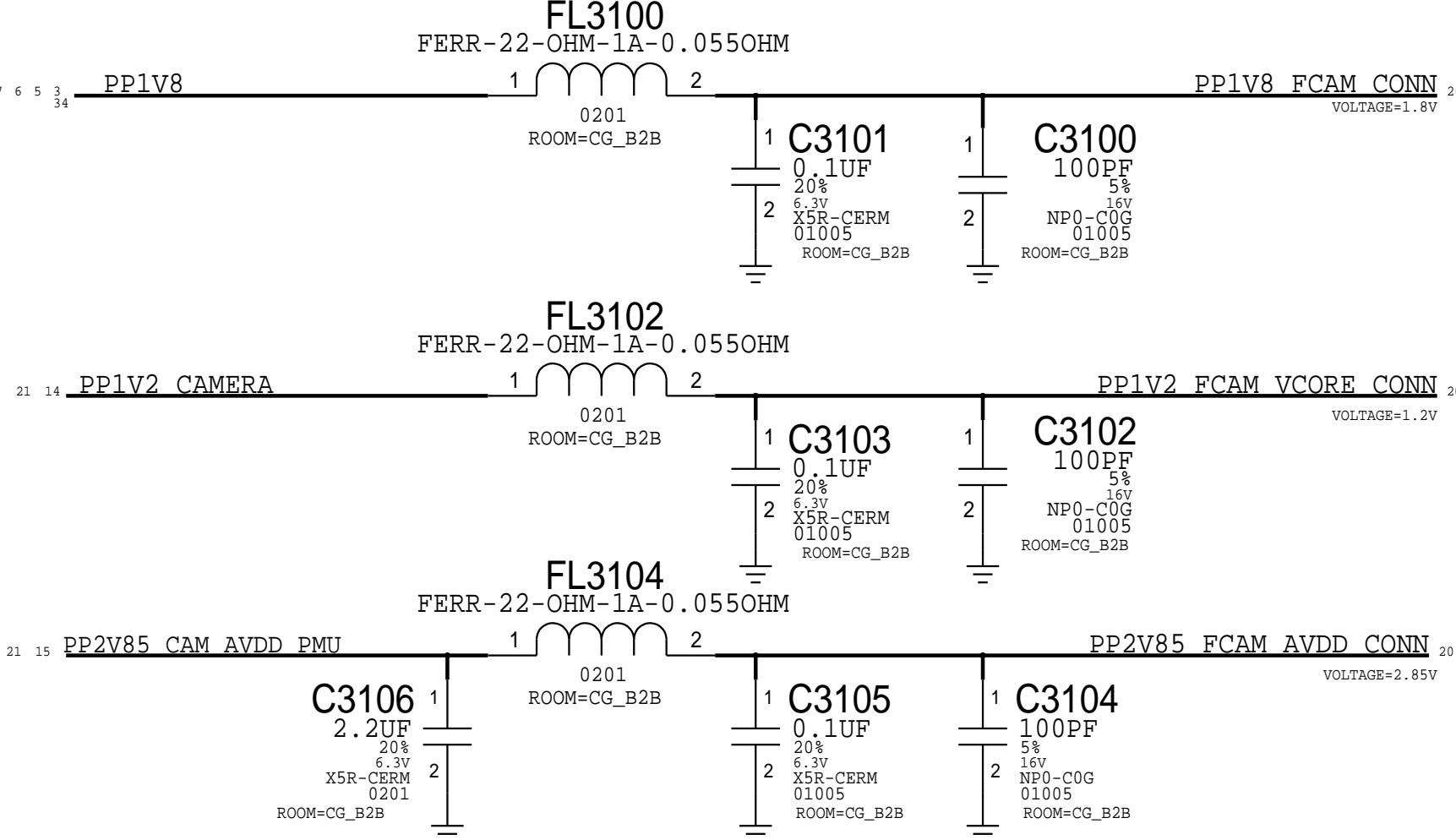
C

B

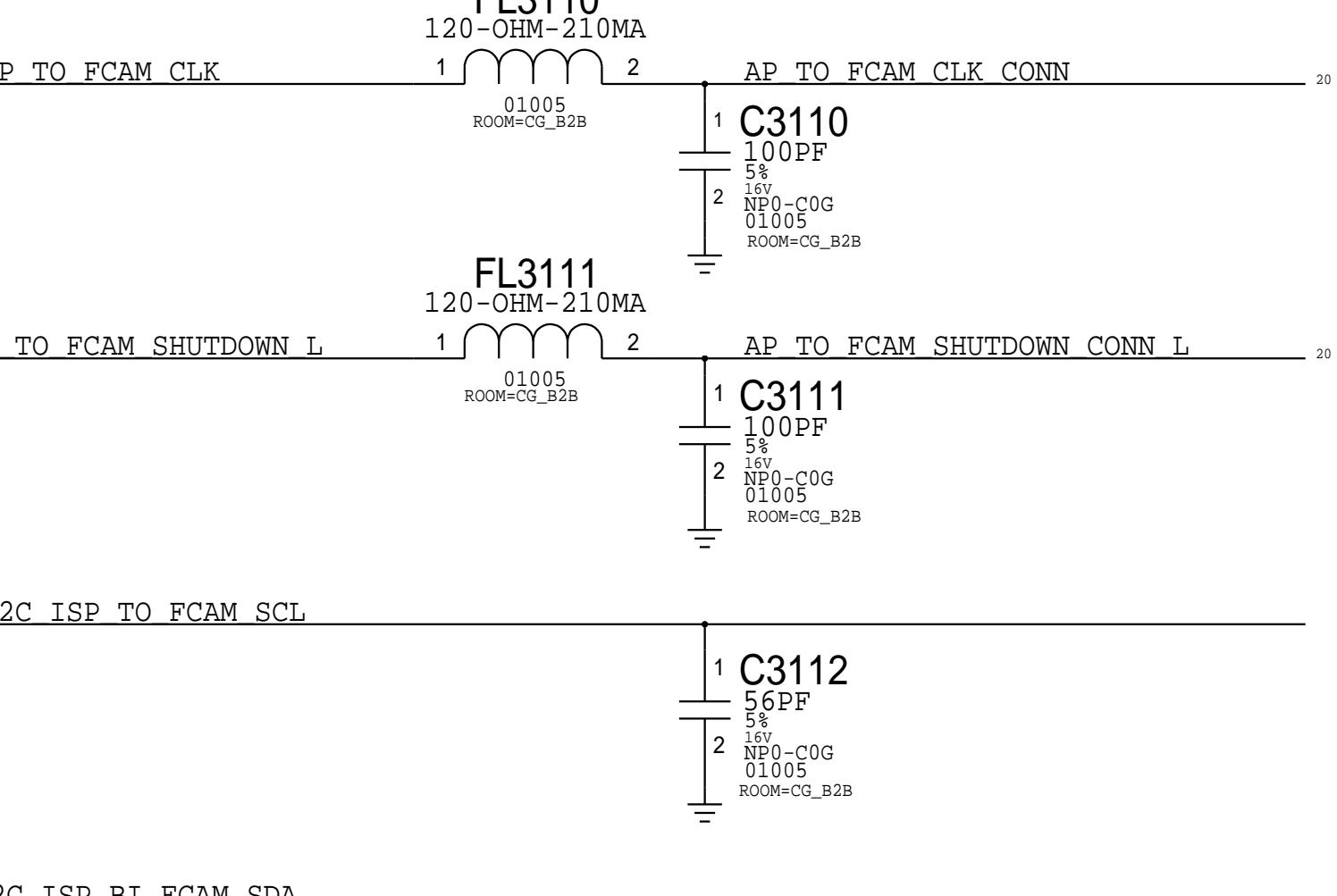
A

FRONT CAMERA FLEX

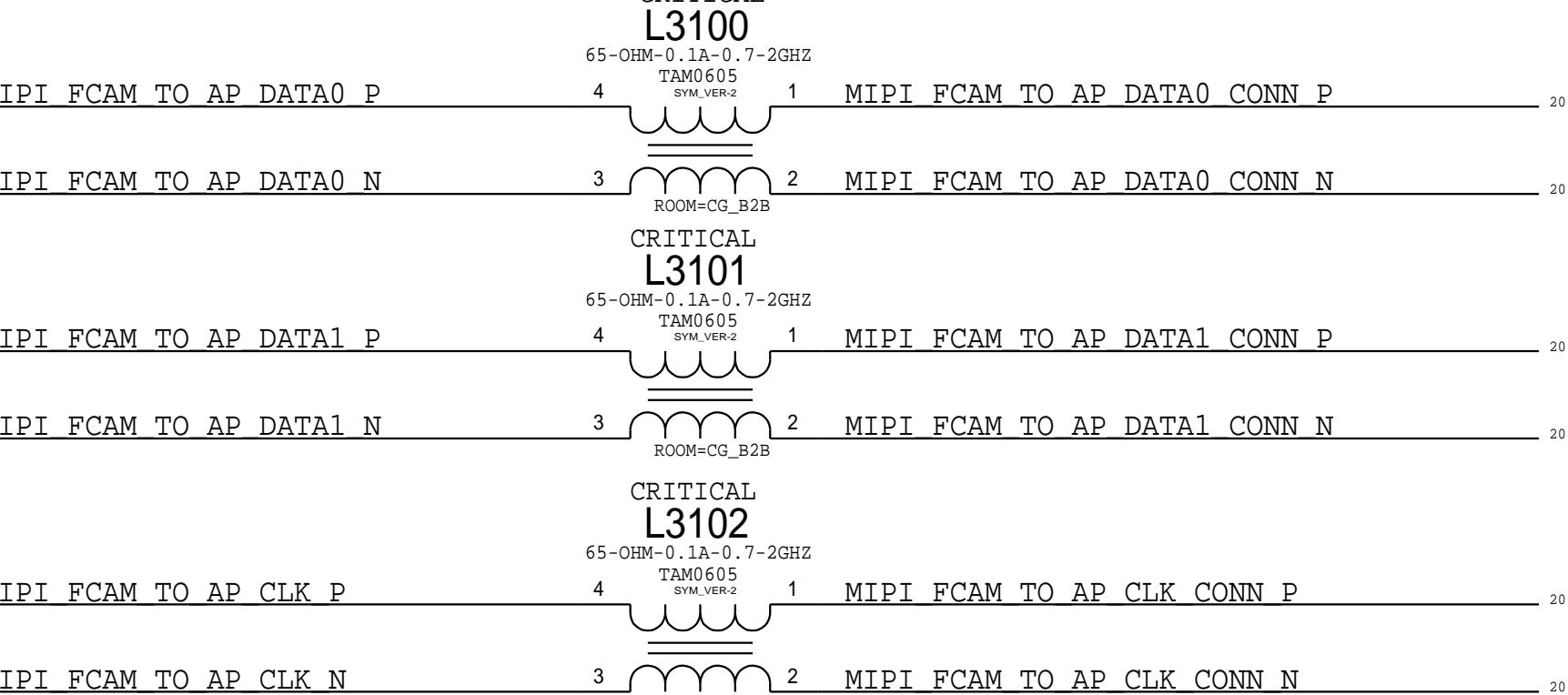
CAMERA POWER



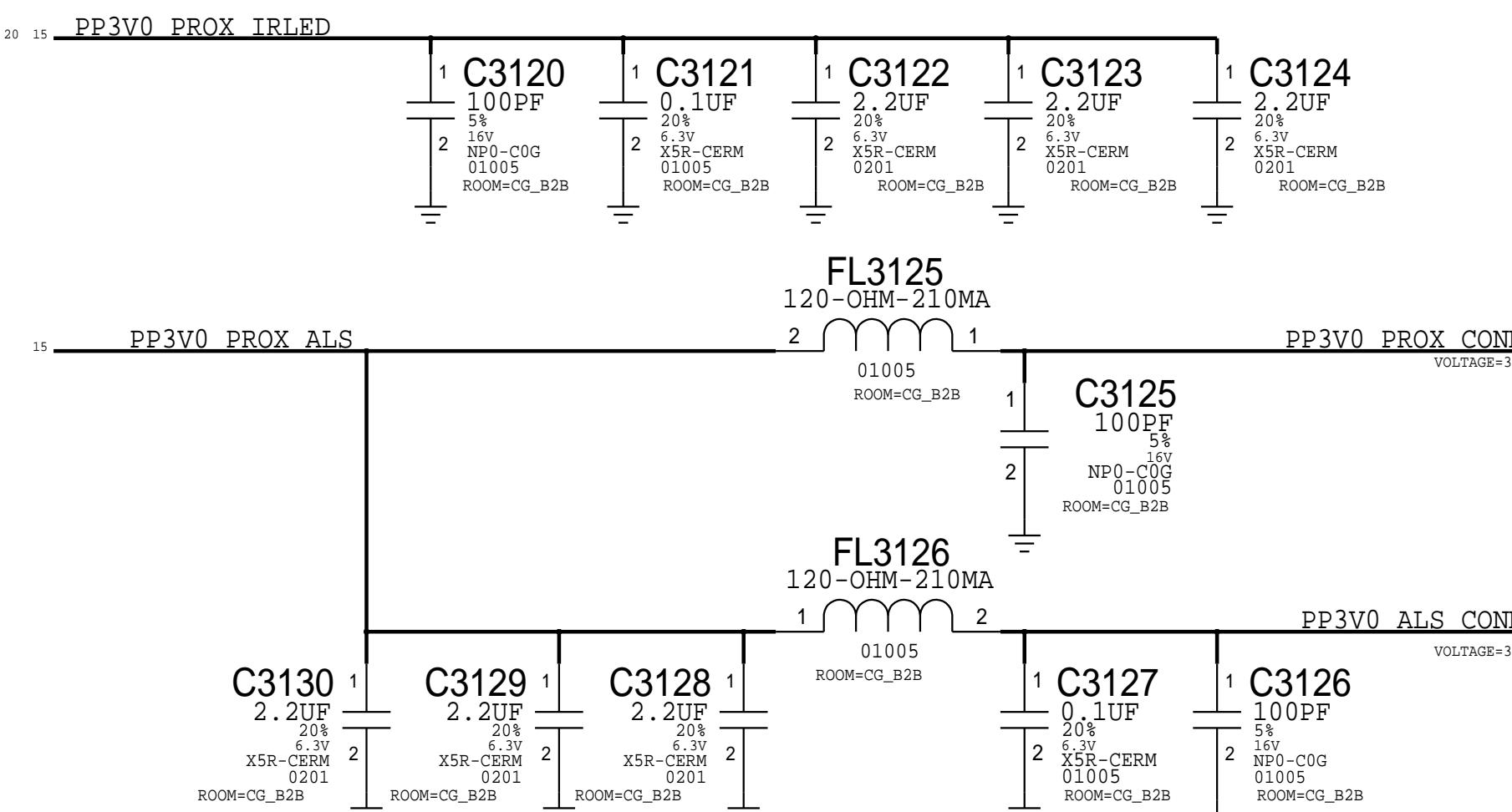
CAMERA I/O



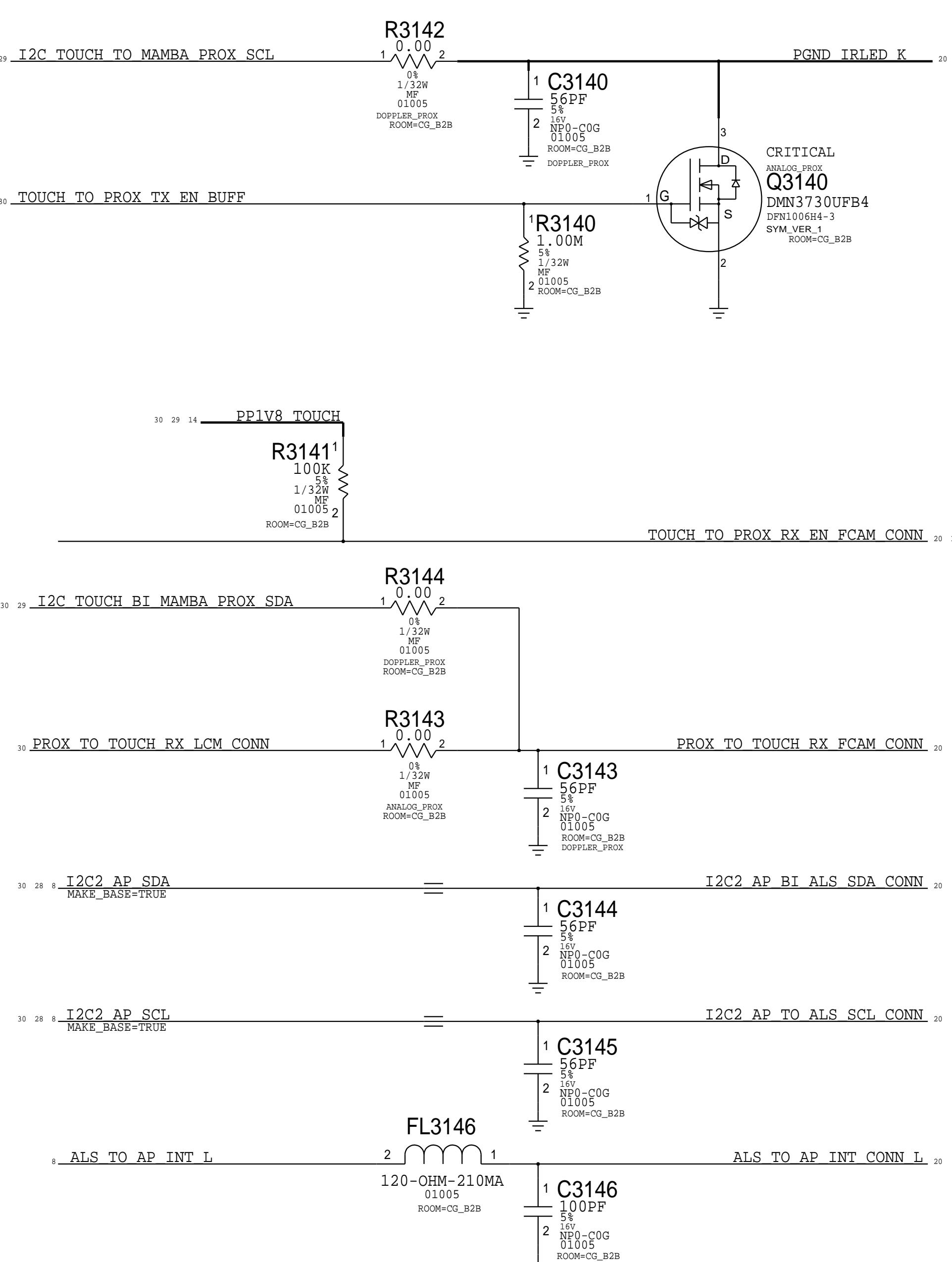
CAMERA MIPI



PROX & ALS POWER

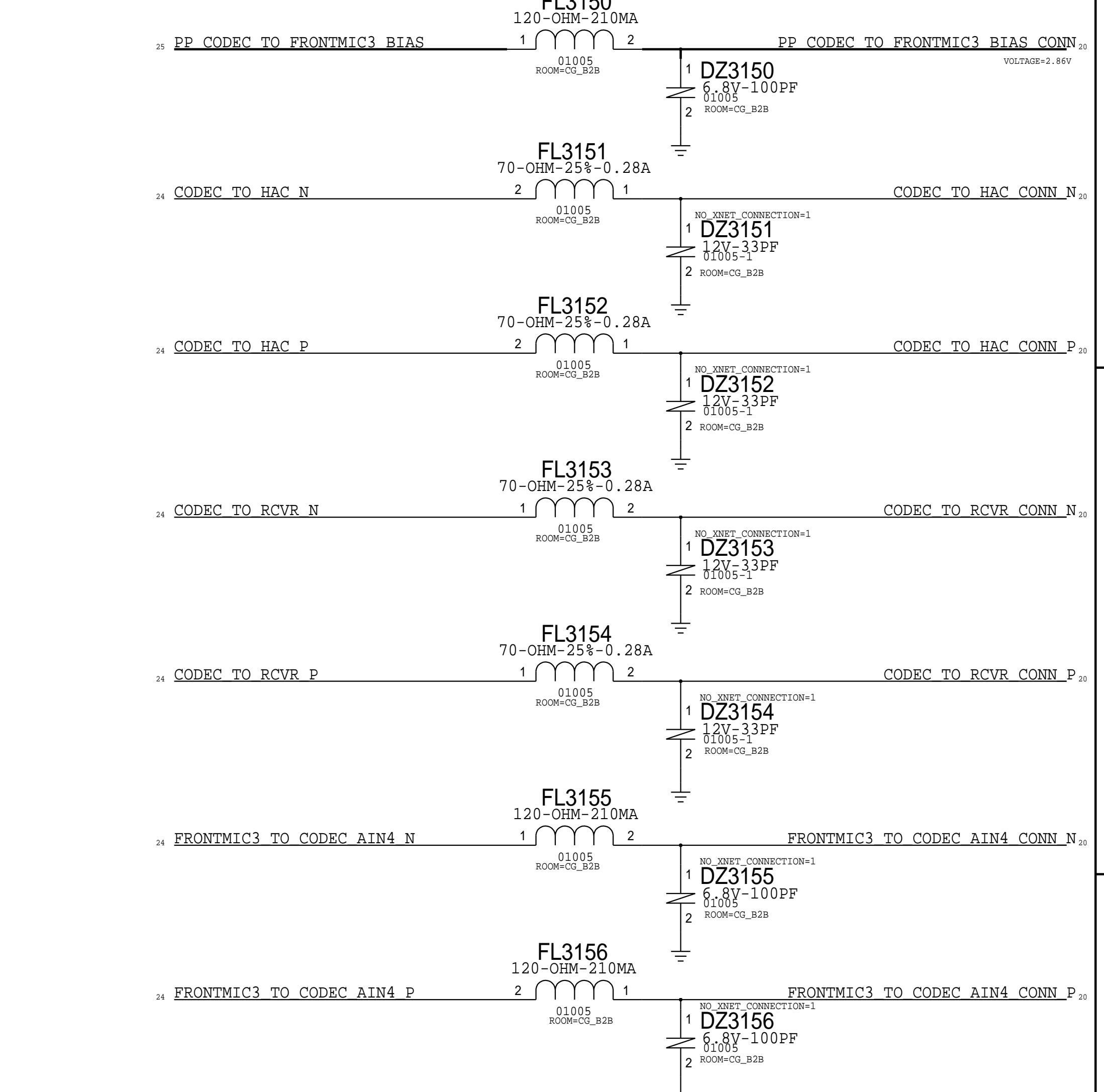
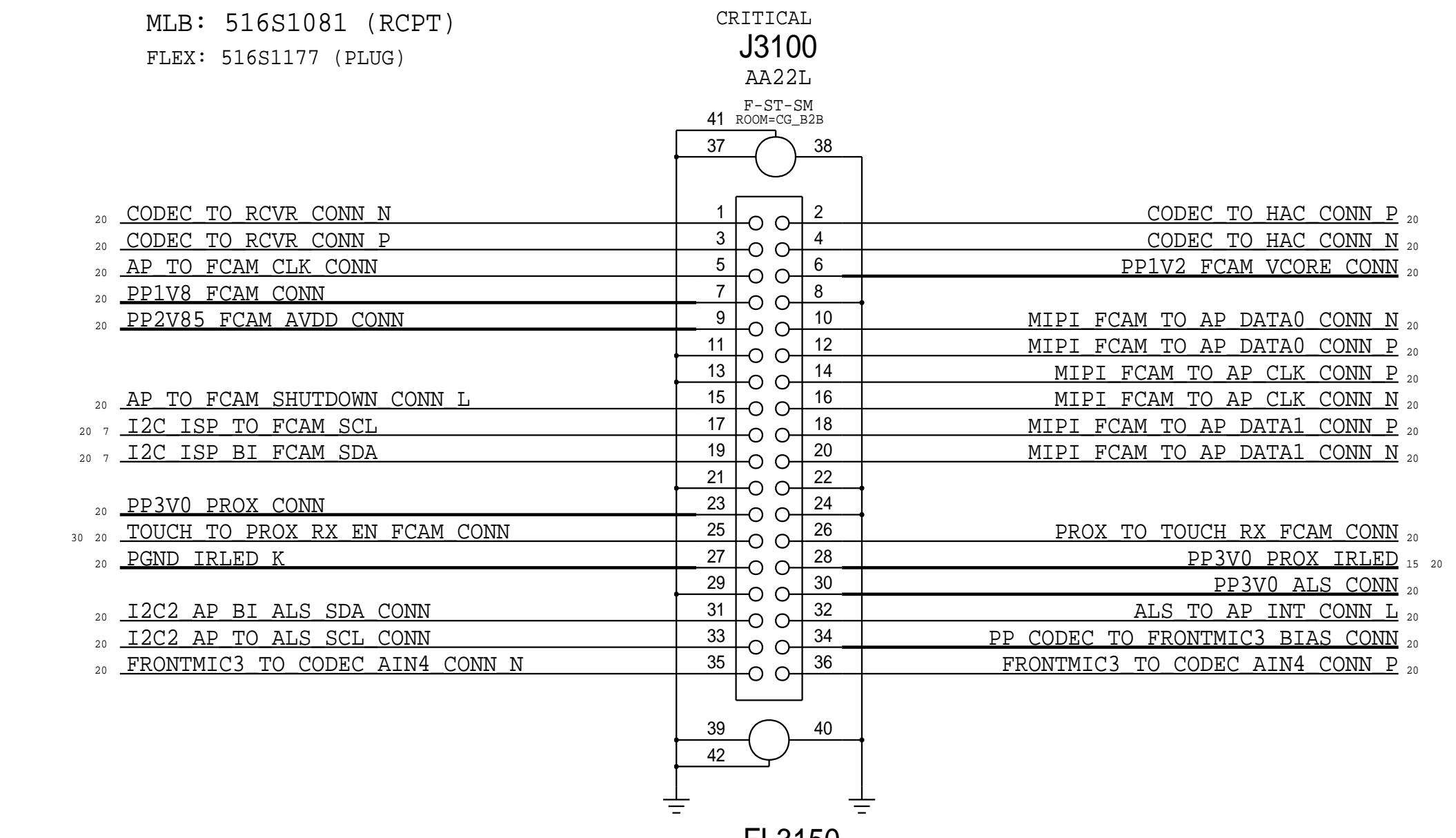


PROX & ALS INTERFACE



FCAM CONNECTOR

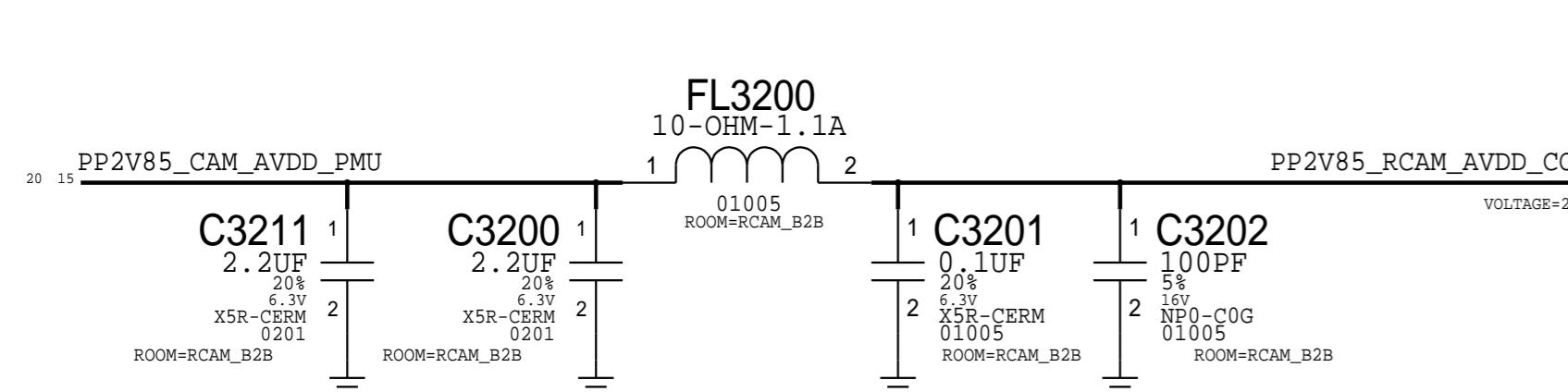
MLB: 516S1081 (RCPT)
FLEX: 516S1177 (PLUG)



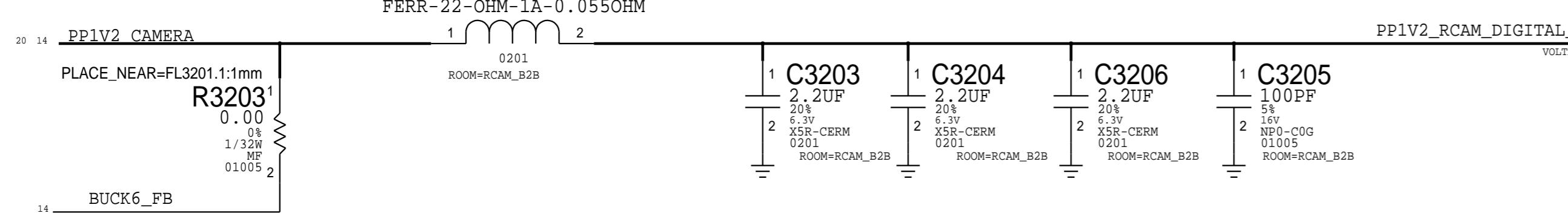
REAR CAMERA FLEX

CAMERA POWER/MAMBA LDO

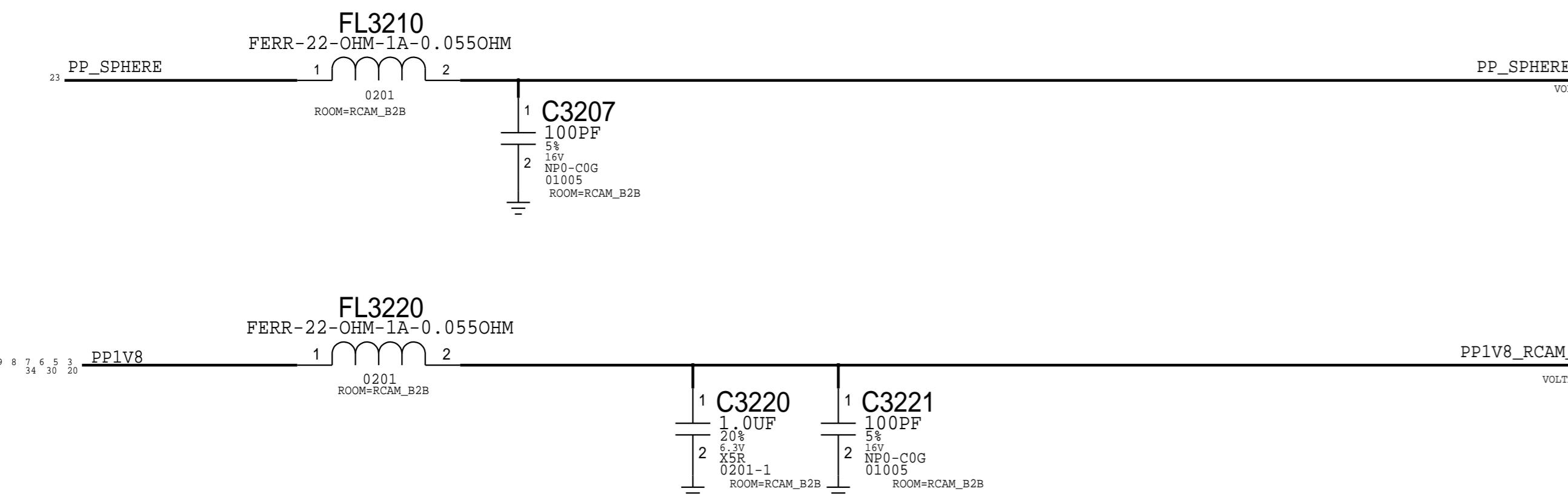
D



C

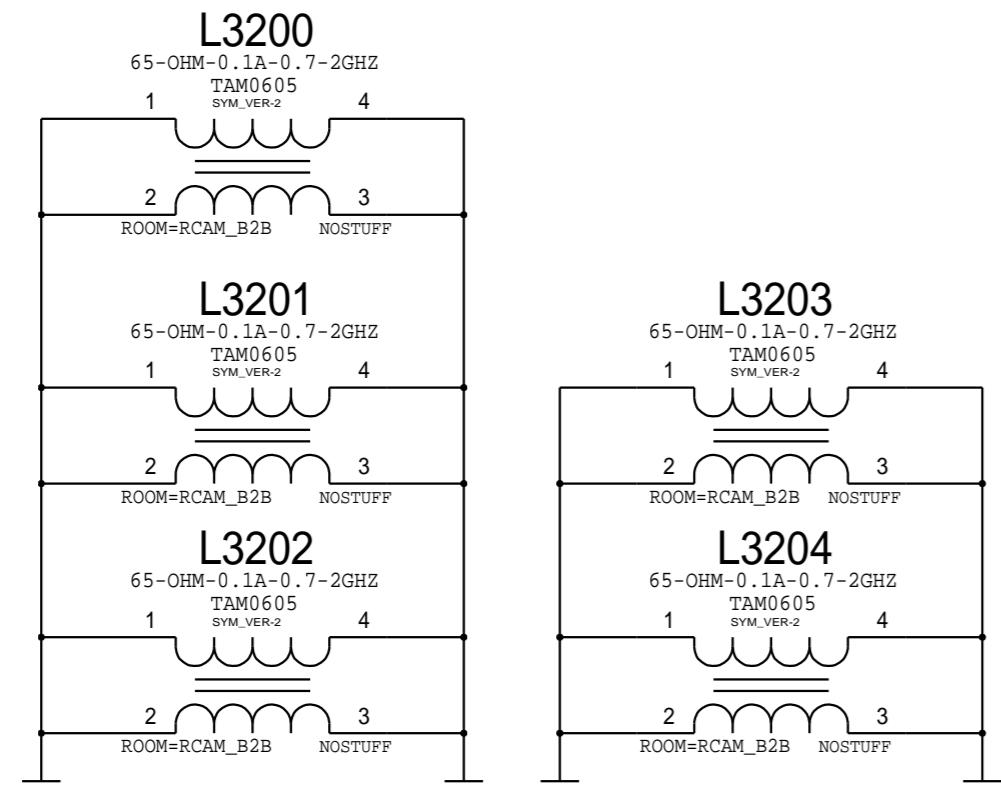


B



A

MIPI Common-Mode Chokes



Placeholder Footprints

RCAM CONNECTOR

MLB: 516S00043 (RCPT)

FLEX: 516S00042 (PLUG)

CRITICAL

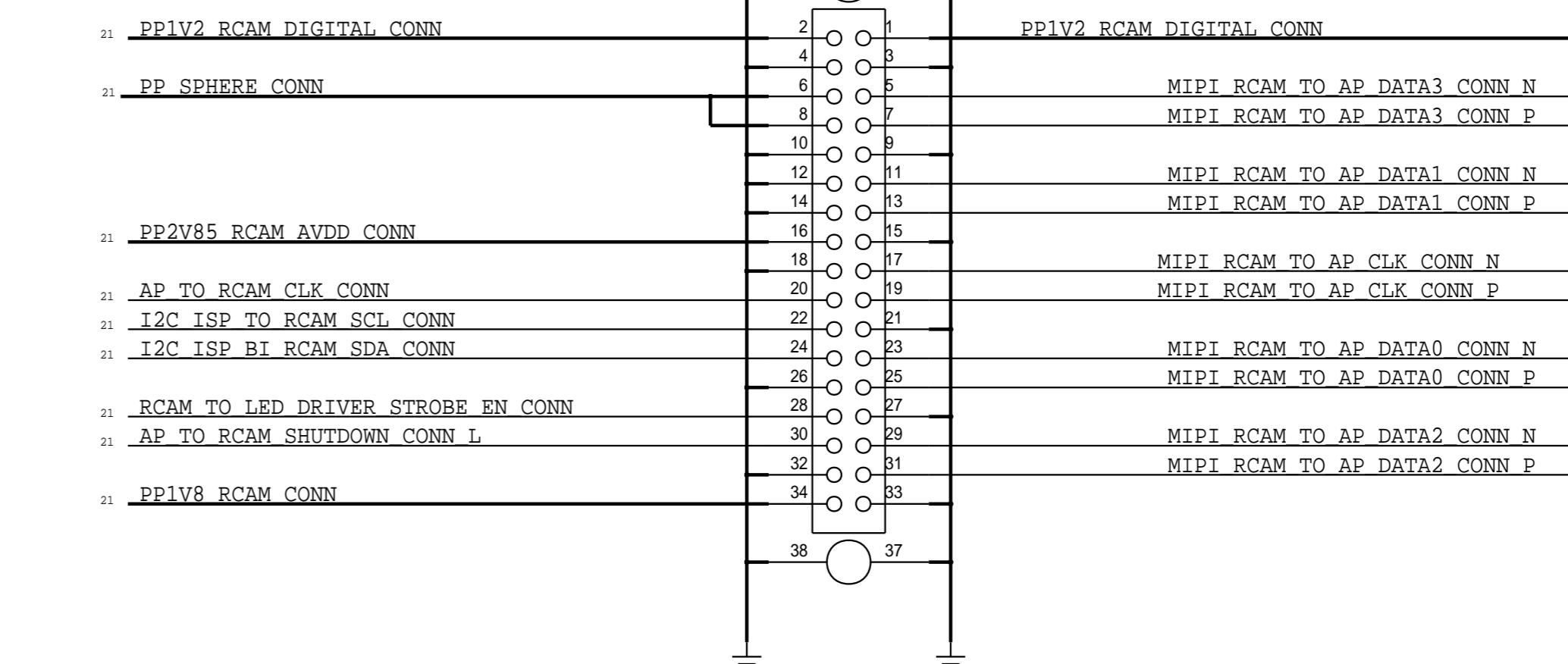
ROOM=RCAM_B2B

J3200

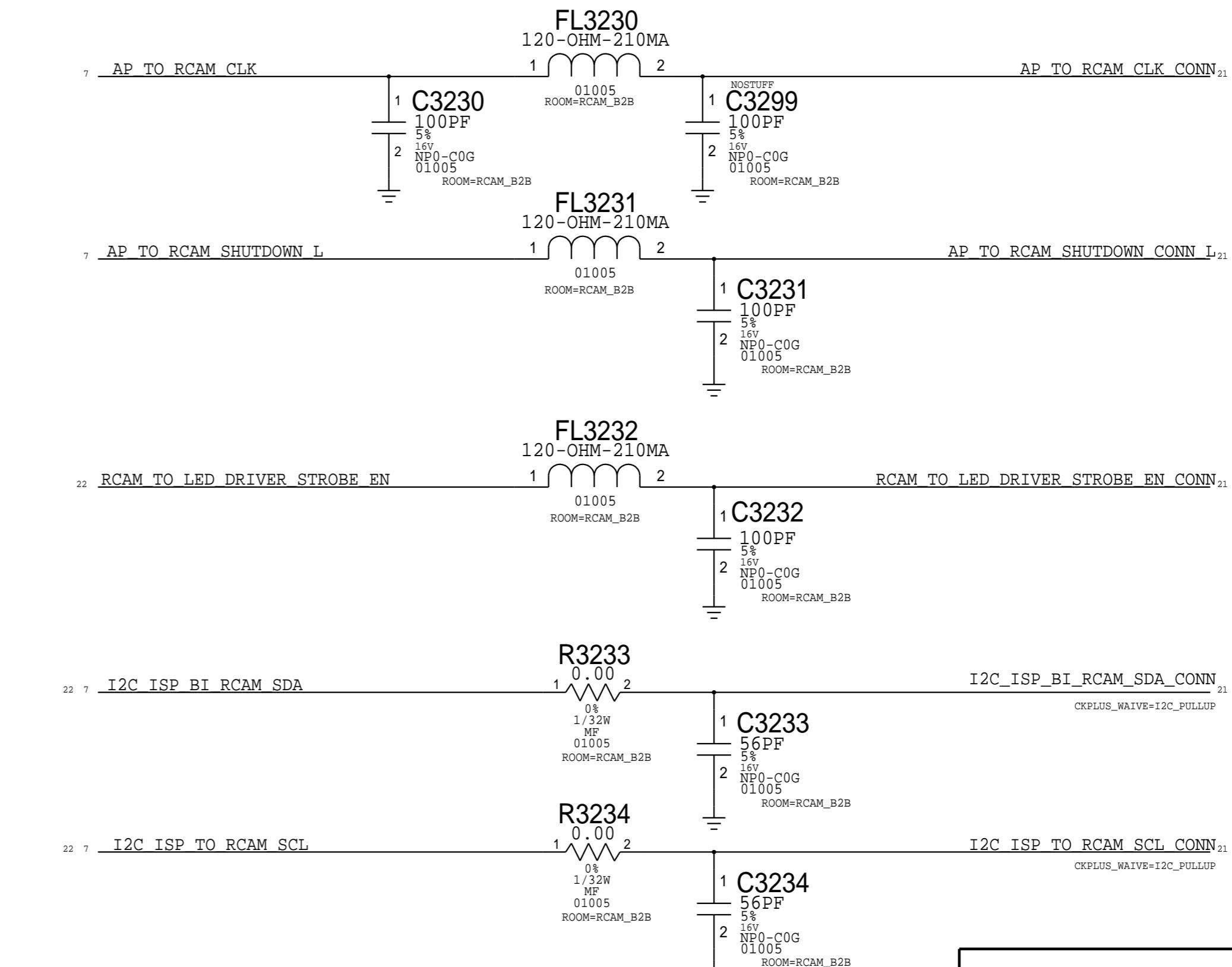
AA26-S034VA1

F-ST-SM

36 35



Digital I/O



D

C

B

A

D

D

D

C

C

B

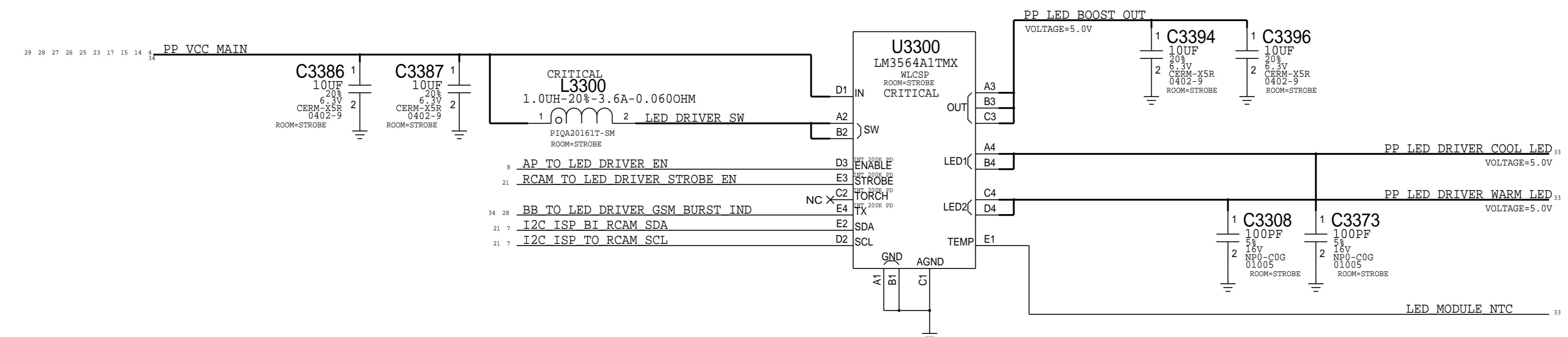
B

A

A

DUAL LED STROBE DRIVER

APN: 353S3899



D

D

C

C

B

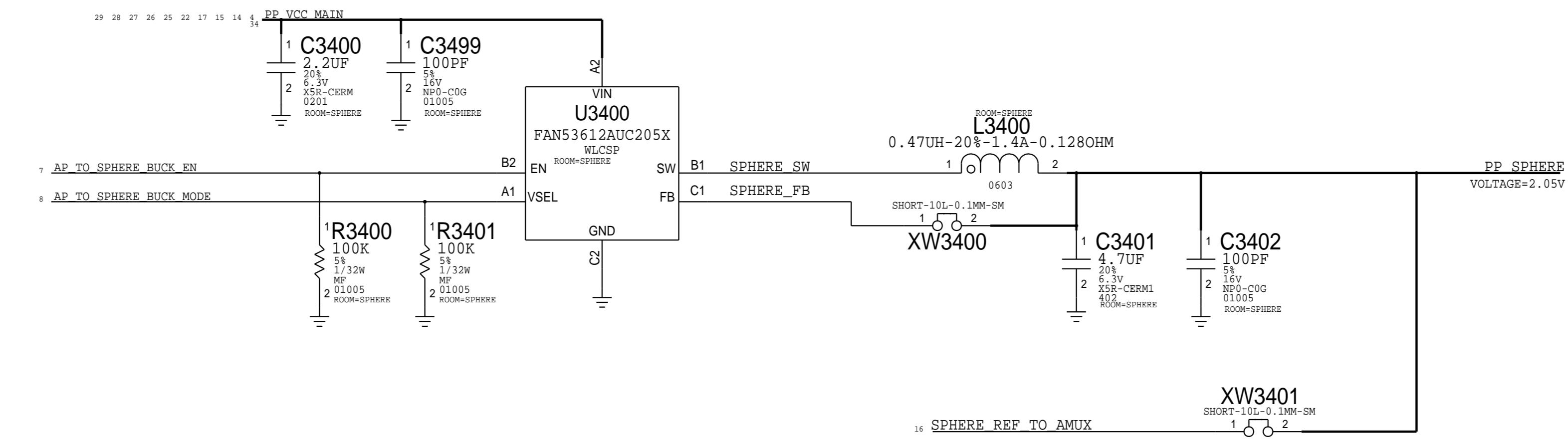
B

A

A

Sphere Driver

APN: 353S00413



CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)

D

D

VOICE MIC

32 LOWERMIC1 TO CODEC AIN1 P
32 LOWERMIC1 TO CODEC AIN1 N

LOWER MIC

32 LOWERMIC4 TO CODEC AIN2 P
32 LOWERMIC4 TO CODEC AIN2 N

ANC REF MIC

33 REARMIC2 TO CODEC AIN3 P
33 REARMIC2 TO CODEC AIN3 N

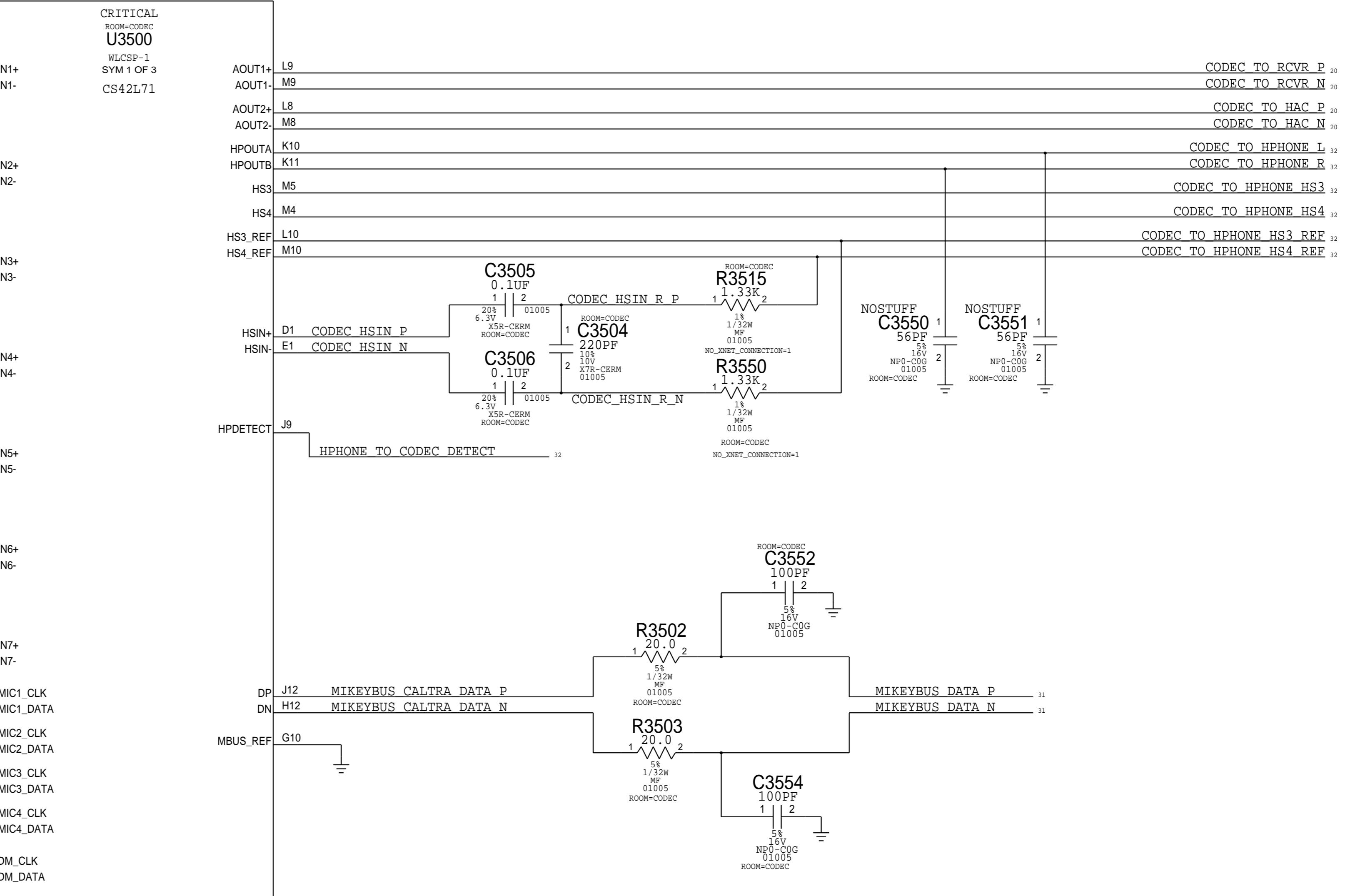
ANC ERROR MIC

20 FRONTMIC3 TO CODEC AIN4 P
20 FRONTMIC3 TO CODEC AIN4 N

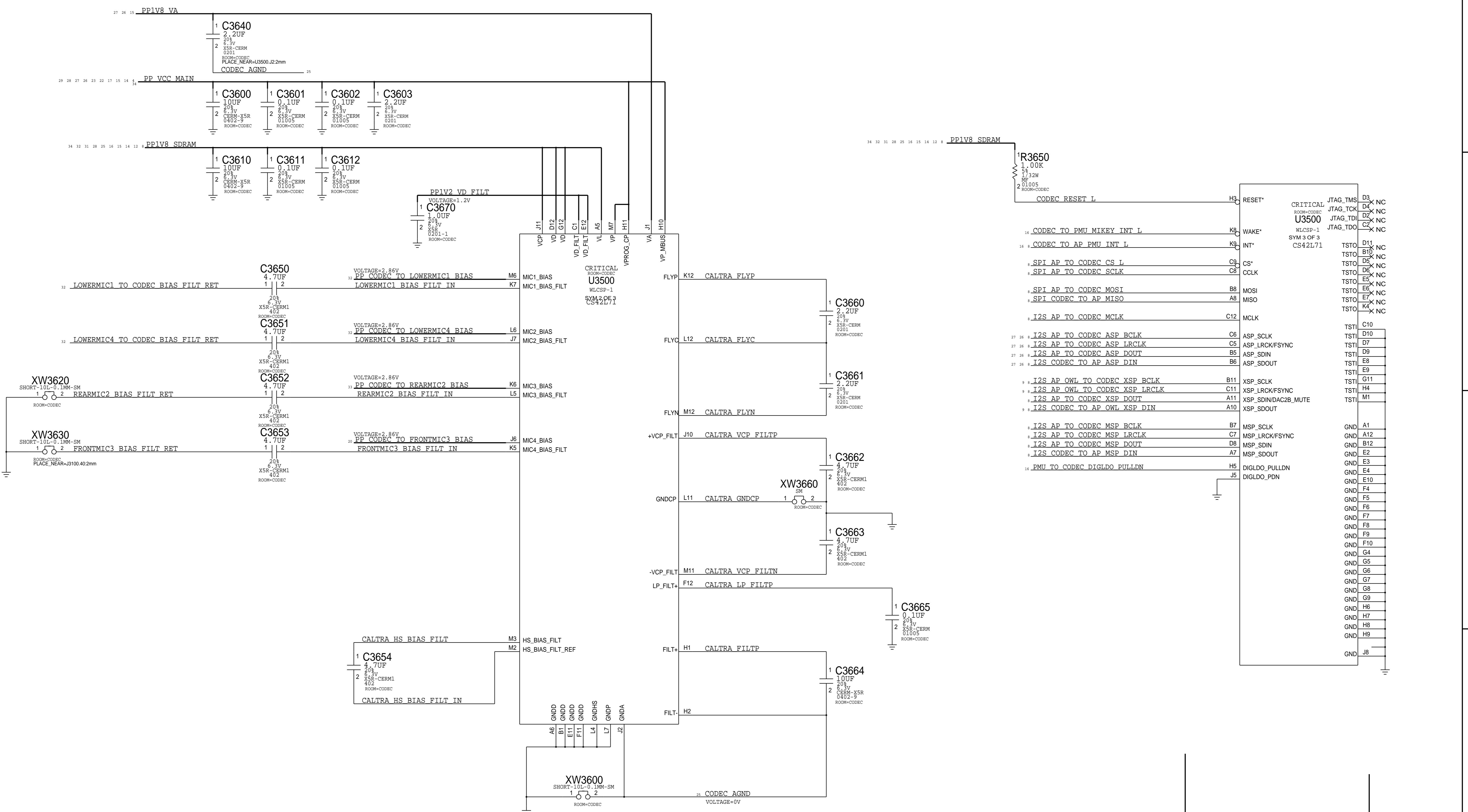
A

A

APN:338S00105



CALTRA AUDIO CODEC (POWER & I/O)

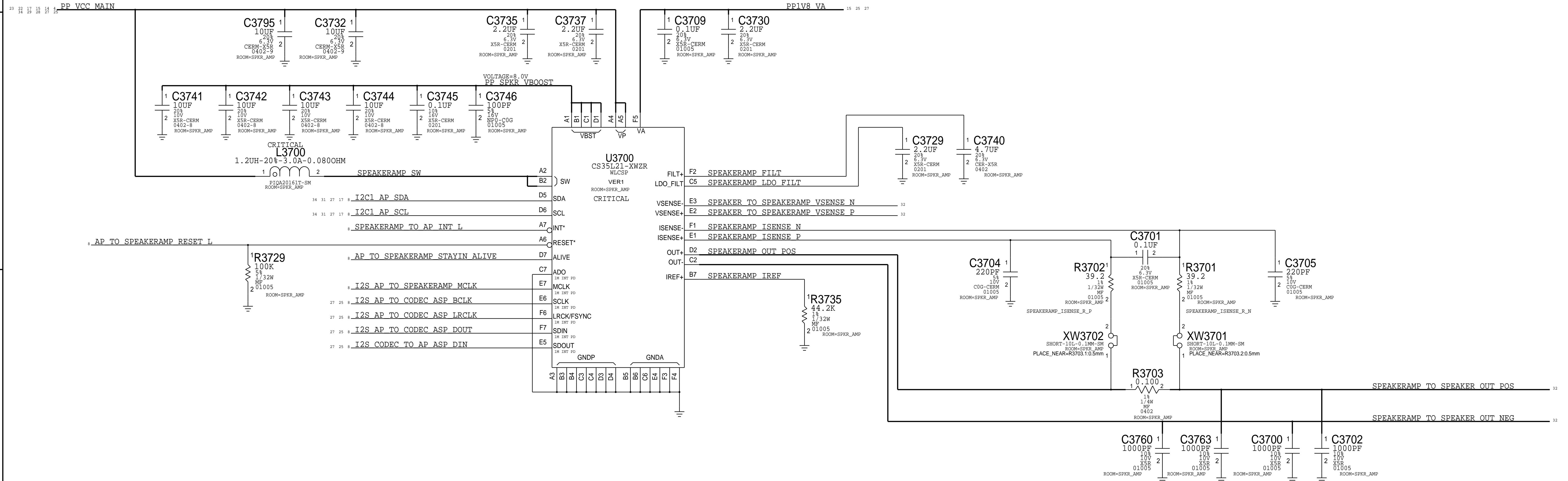


D

D

SPEAKER AMPLIFIER

APN: 338S1285
I2C ADDRESS: 1000000



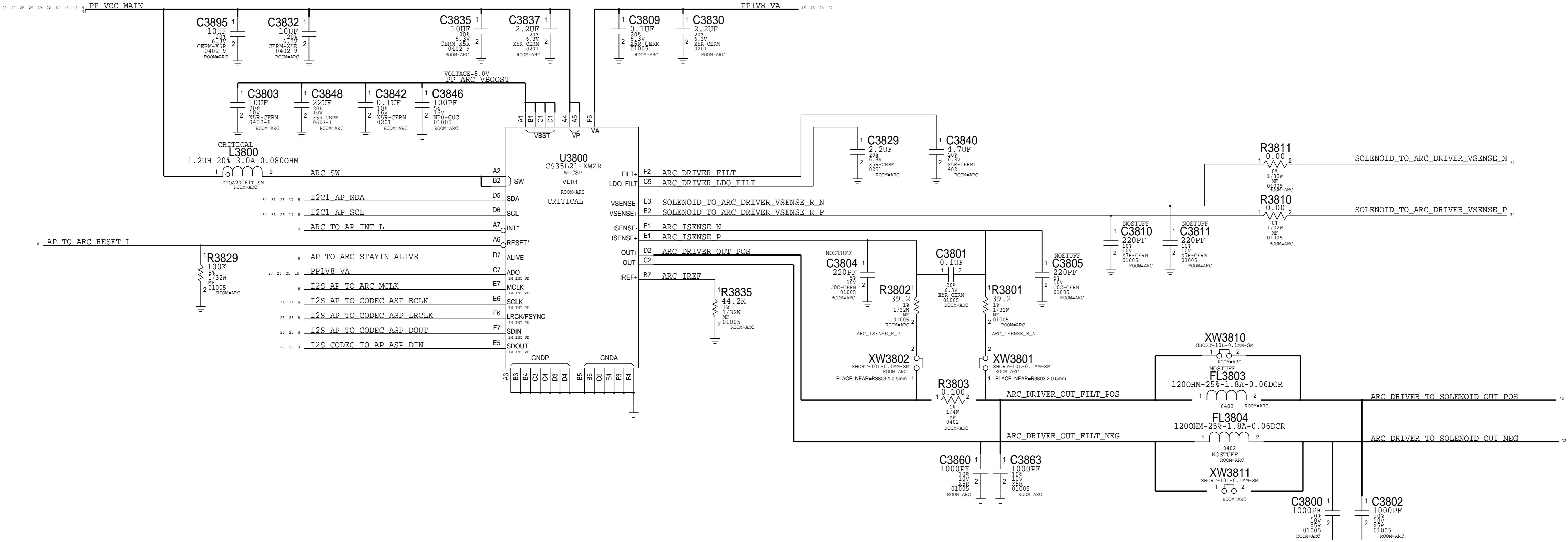
D

D

ARC DRIVER

APN: 338S1285

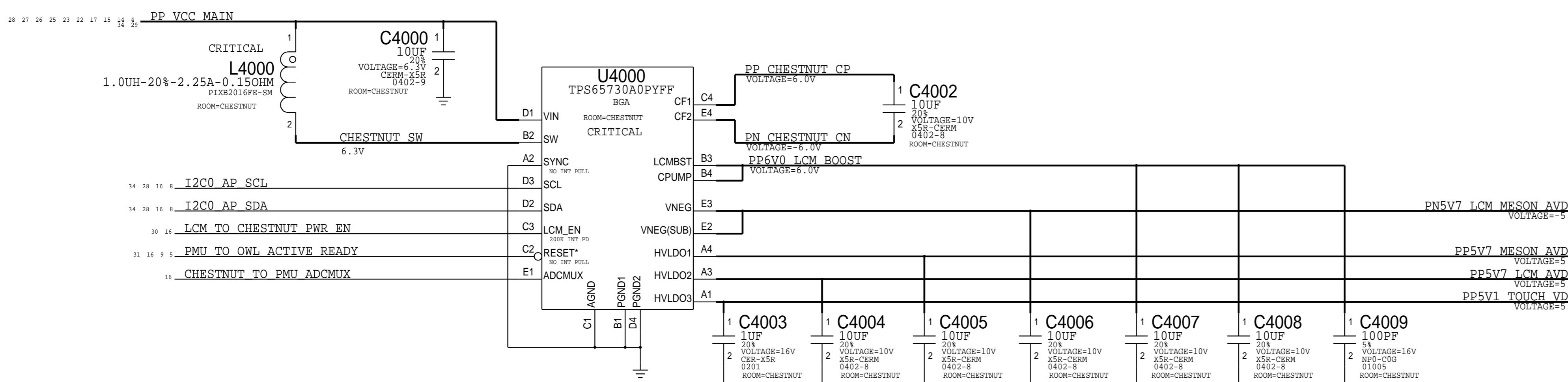
I2C ADDRESS: 1000001



DISPLAY & TOUCH - POWER SUPPLIES

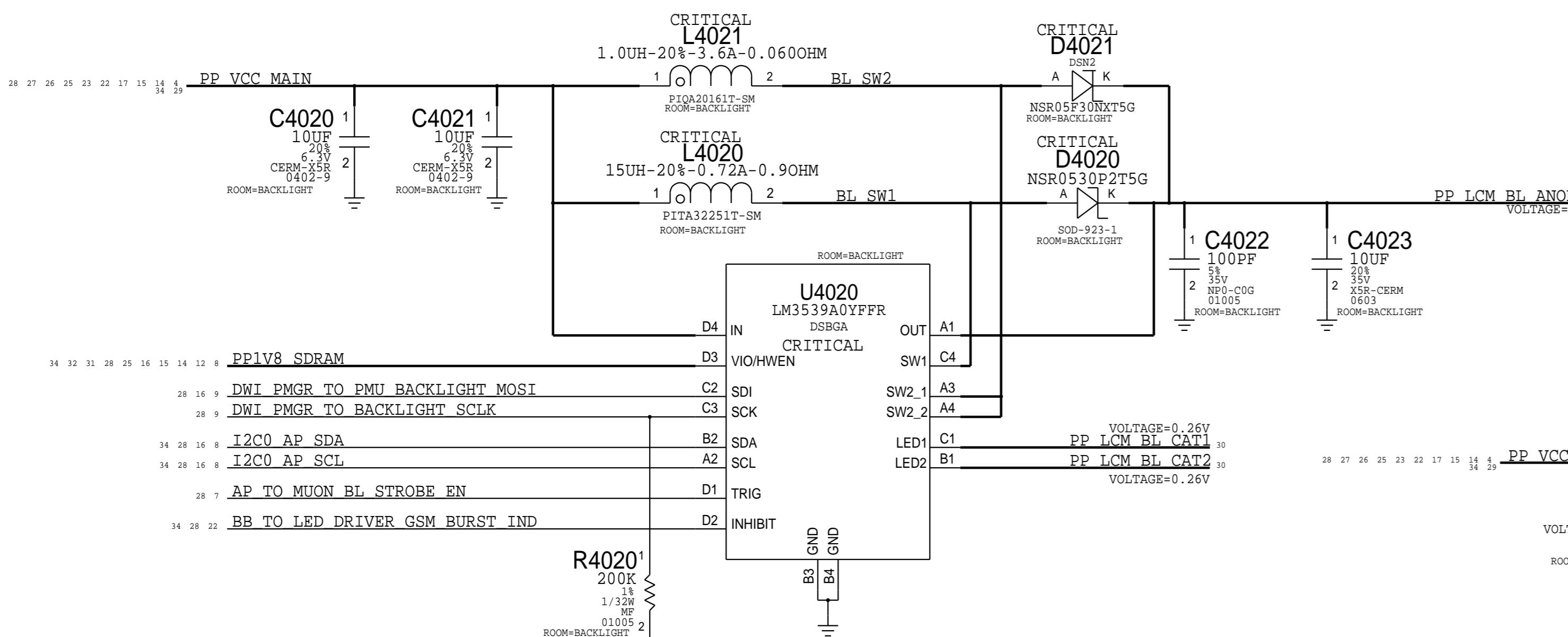
CHESTNUT DISPLAY PMU

APN:338S1172



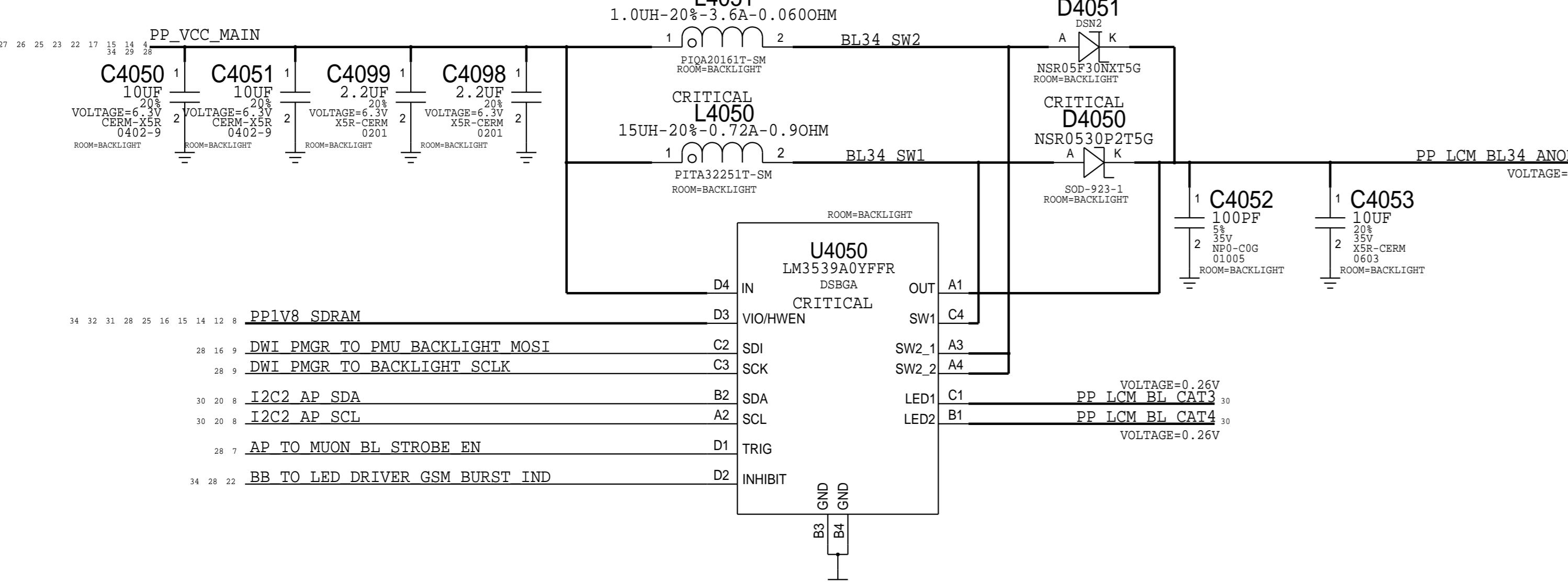
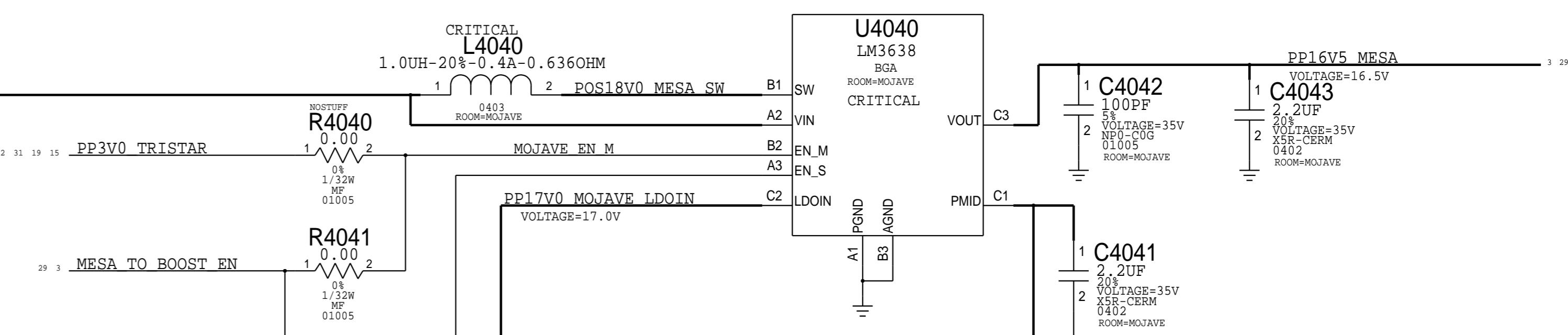
LED BACKLIGHT DRIVERS

APN:353S00407



MOJAVE MESA BOOST

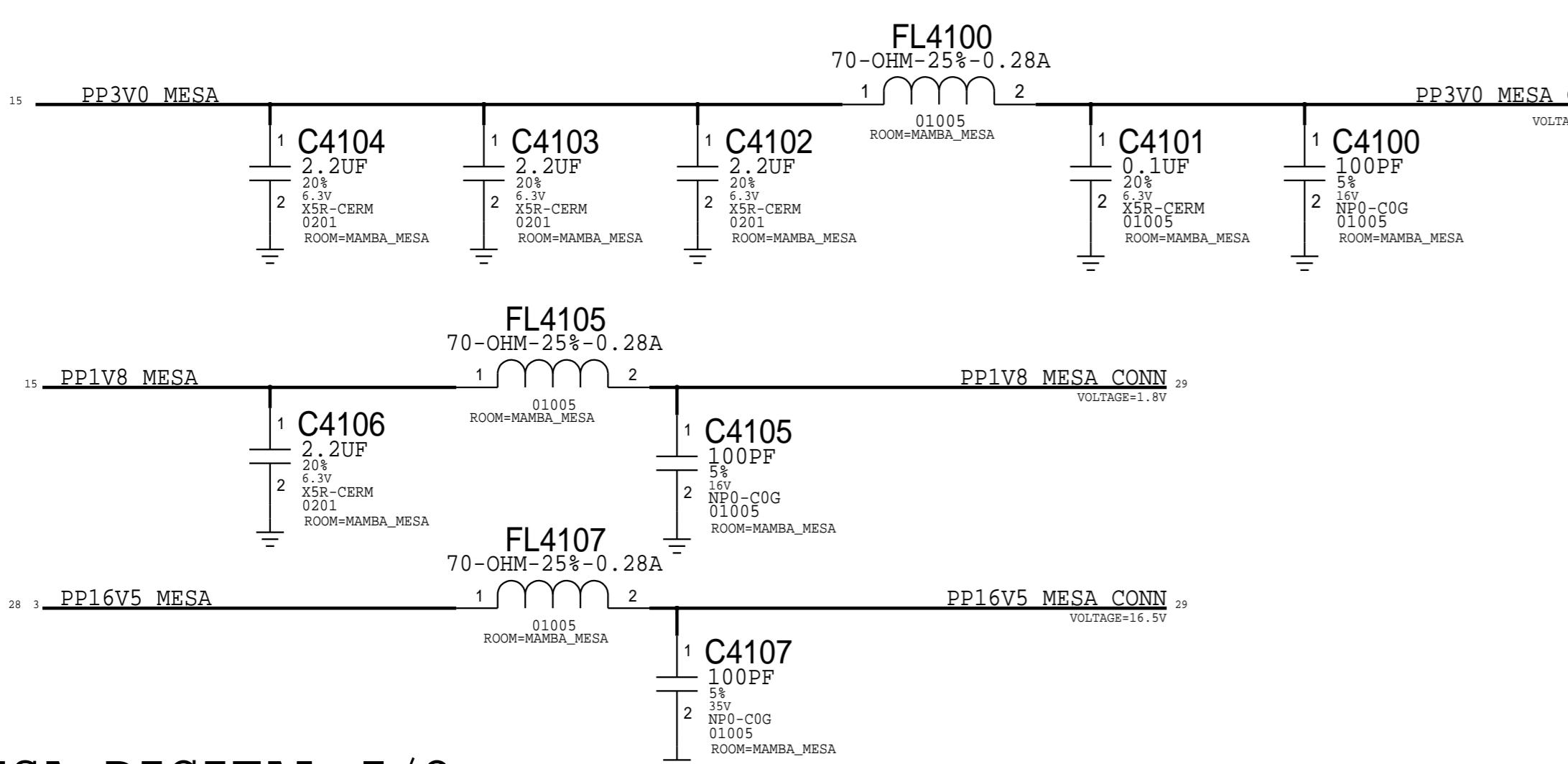
APN:353S3978



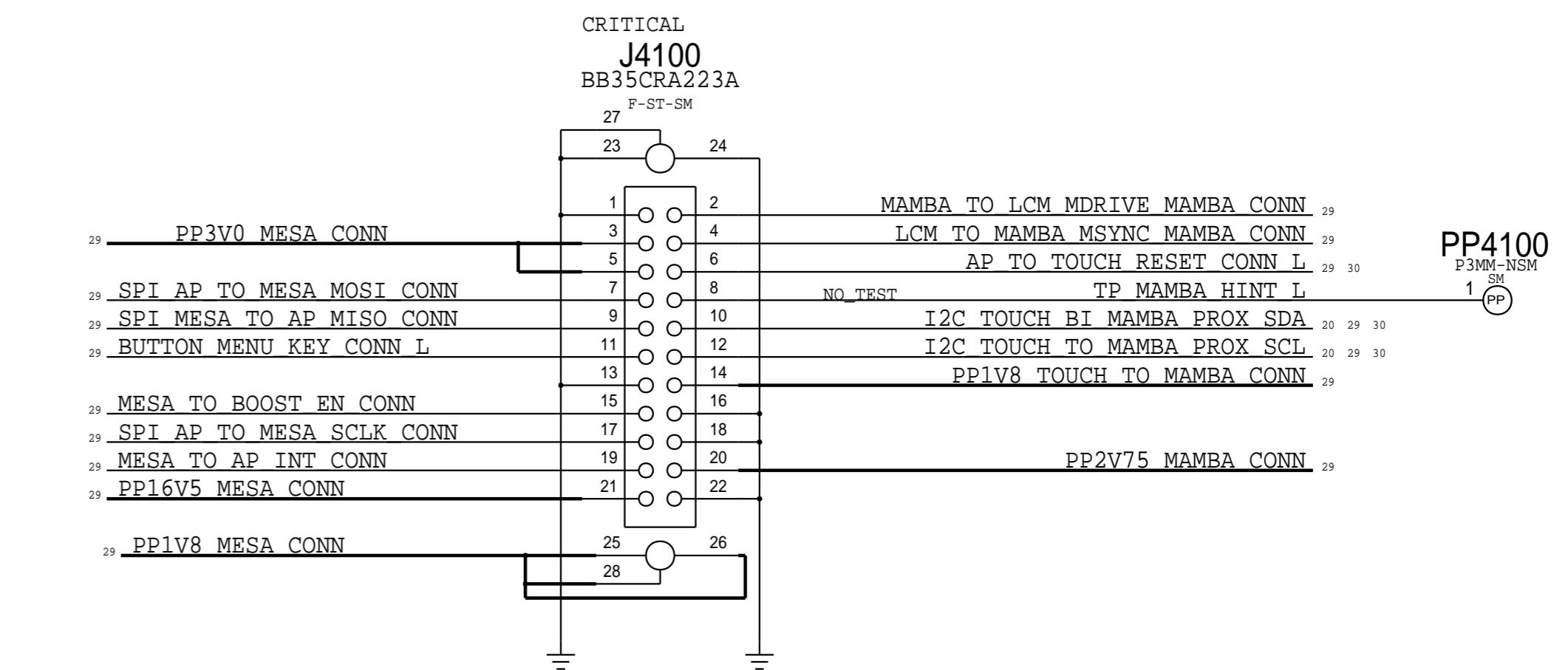
MAMBA & MESA FLEX

ORB & MESA CONNECTOR
MLB: 516S00056 (RCPT)

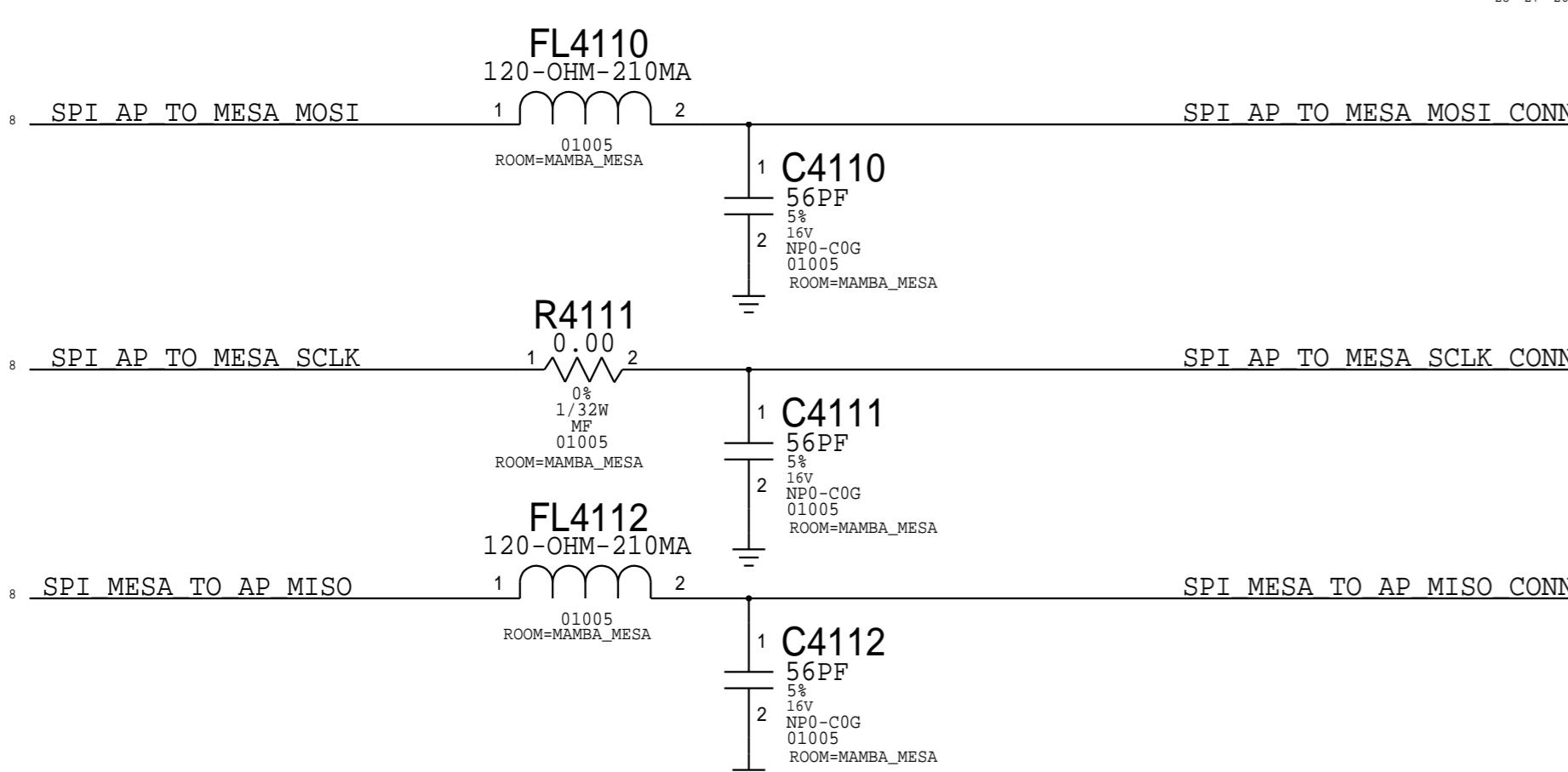
MESA POWER



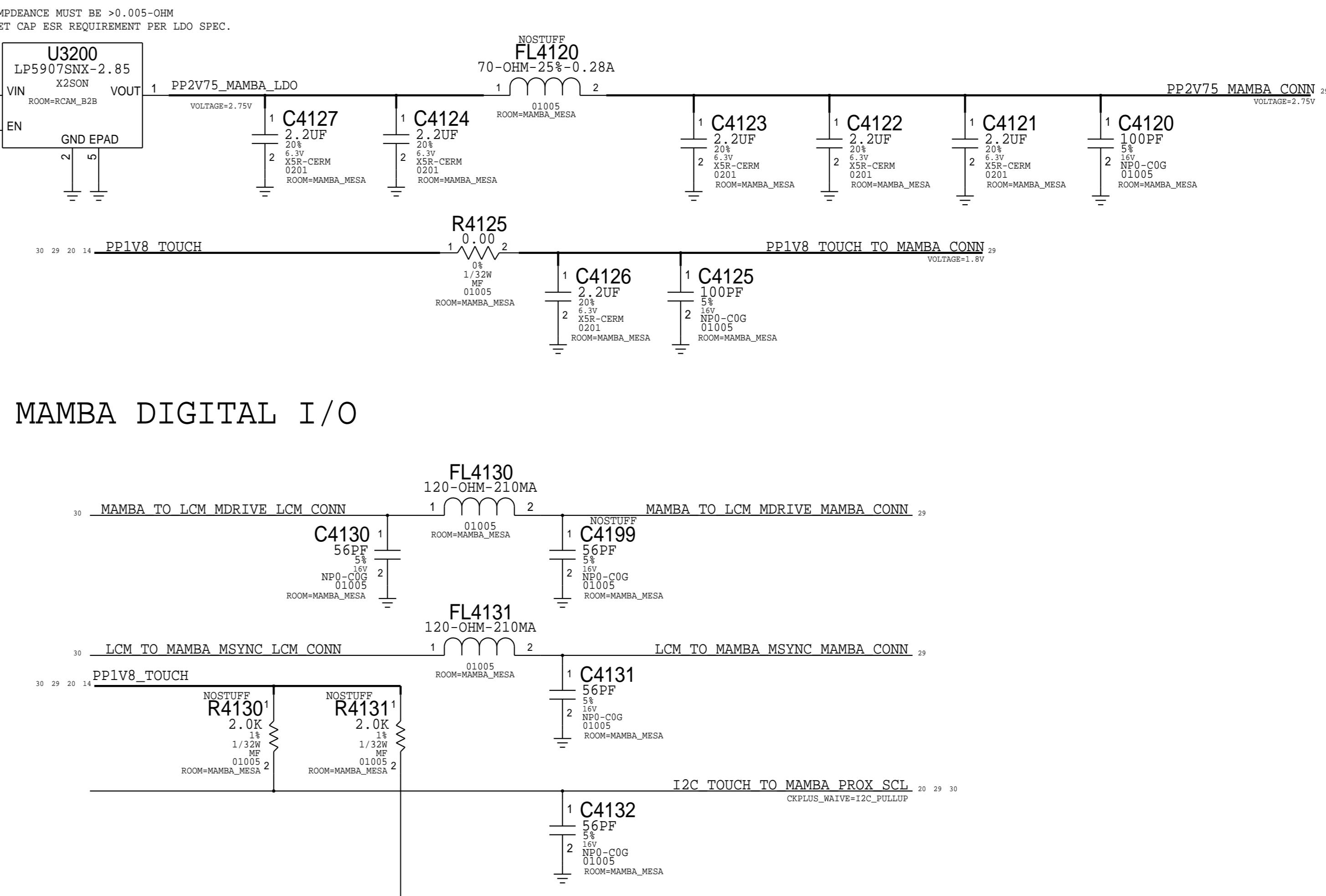
MAMBA POWER



MESA DIGITAL I/O



MAMBA DIGITAL I/O



NOTE: MAMBA I2C PULL-UPS TO PP1V8_TOUCH INSIDE KEPLER
ADDING R4130, R4131 AS OPTION FOR TWEAKING VALUE

IV ALL RIGHTS RESERVED

29 OF 60

8

7

6

5

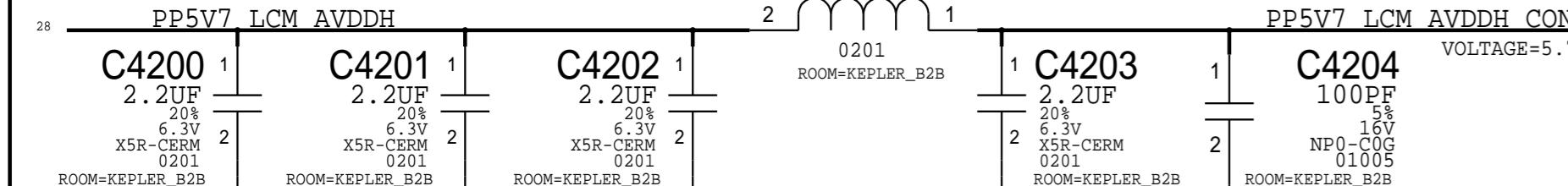
4

3

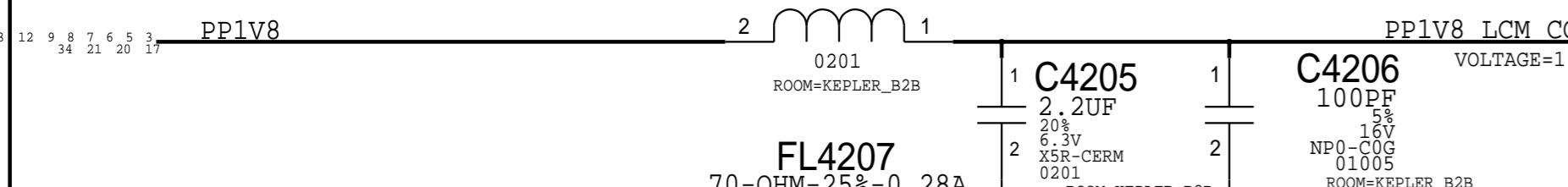
2

1

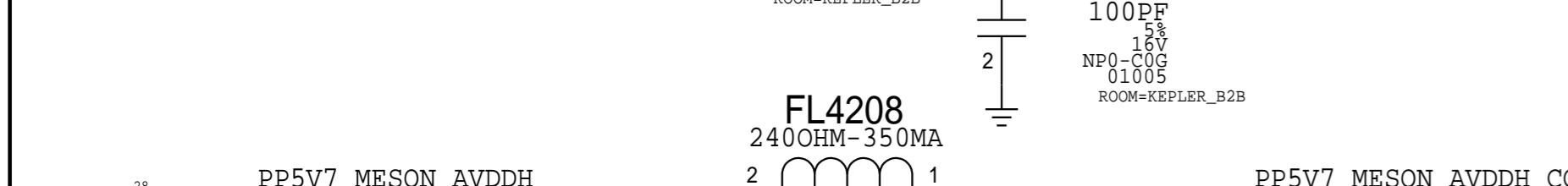
DISPLAY POWER



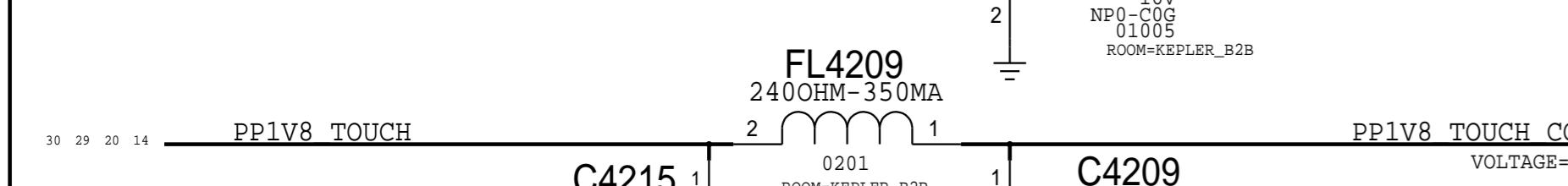
DISPLAY MIPI



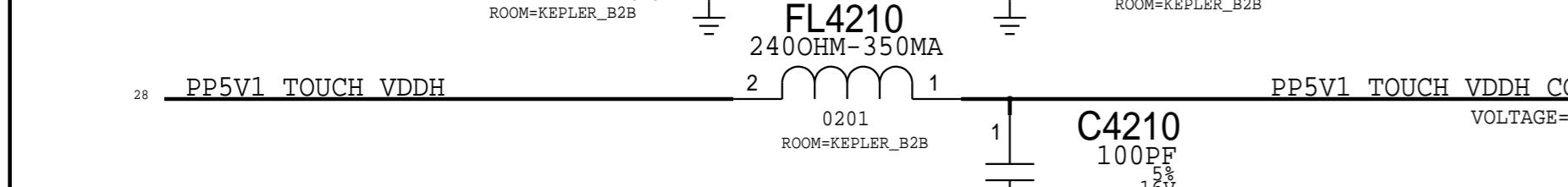
DISPLAY CONTROL SIGNALS



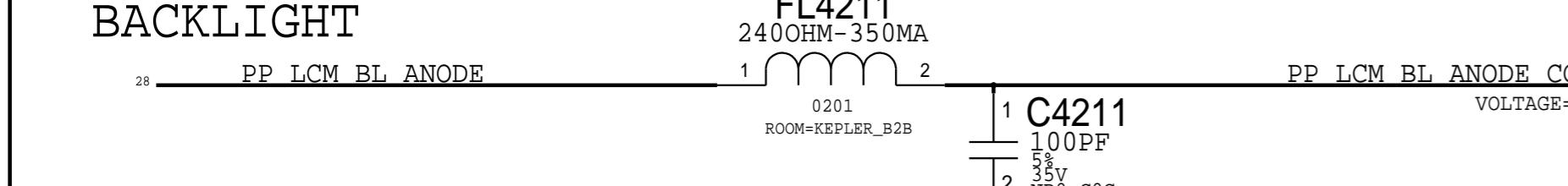
DISPLAY EEPROM I2C



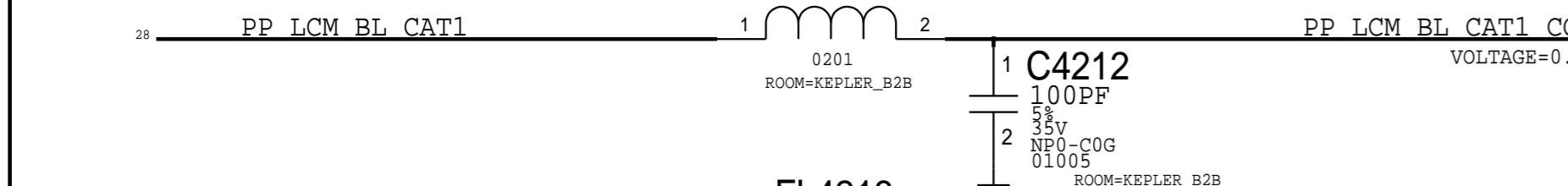
BACKLIGHT



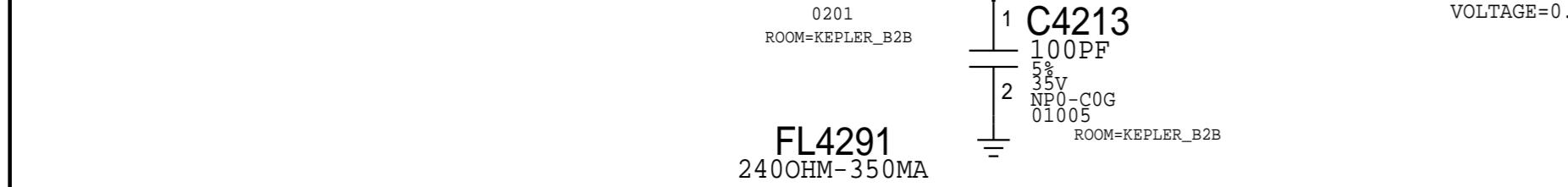
PROX/TOUCH INTERFACE



OWL/TOUCH INTERFACE



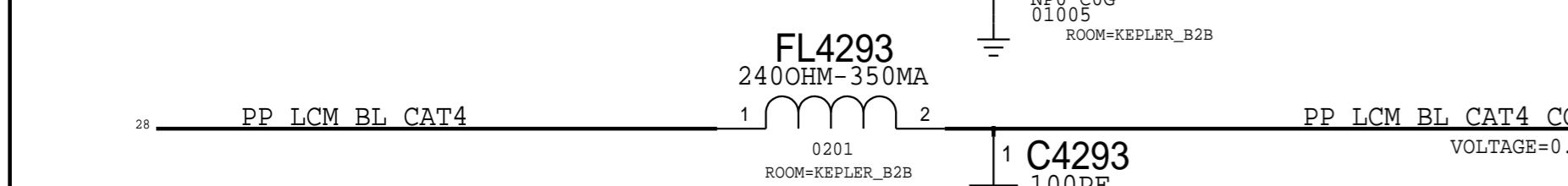
AP/TOUCH INTERFACE



TOUCH TO PROX TX EN L



TOUCH TO PROX RX EN FCAM CONN



TOUCH TO PROX RX EN LCM CONN

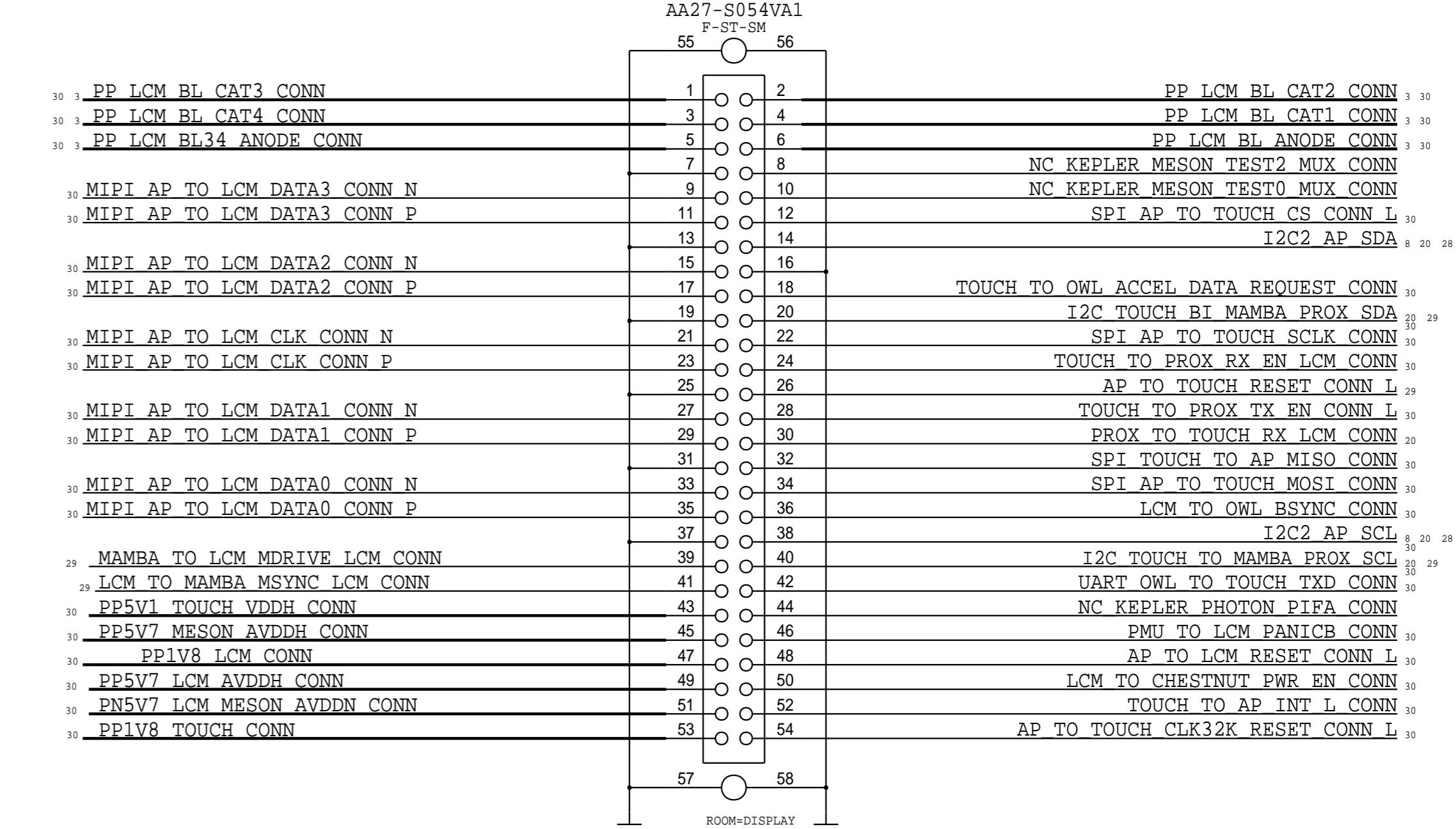


TOUCH TO PROX TX EN BUFF

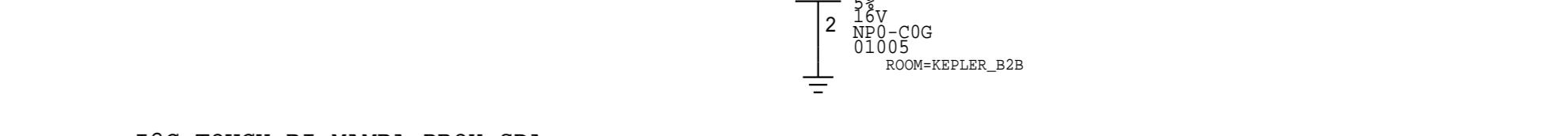
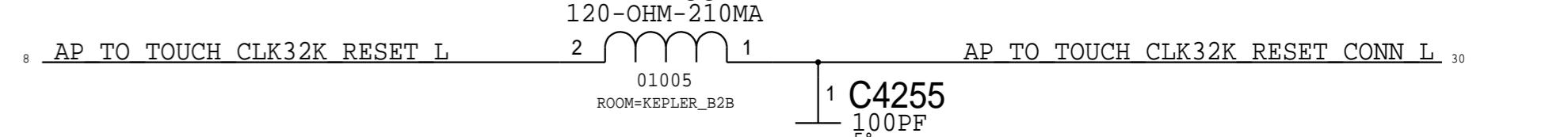
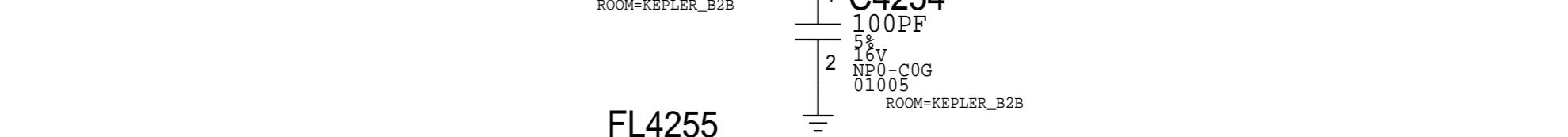
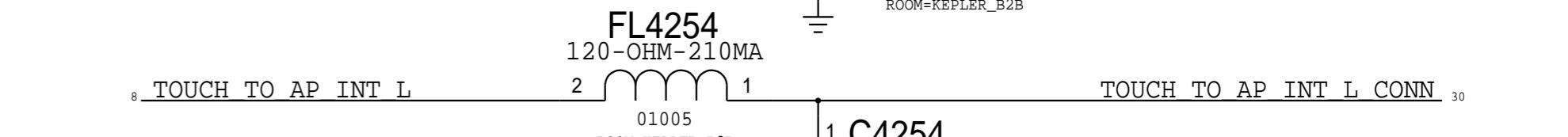
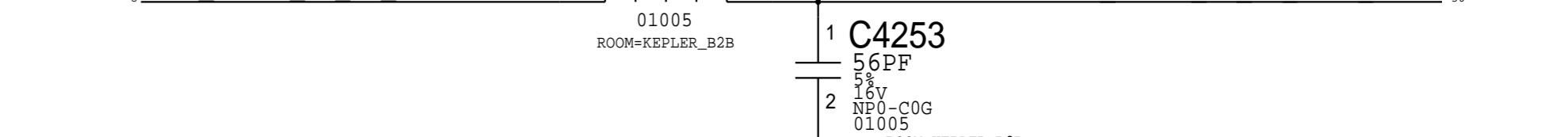
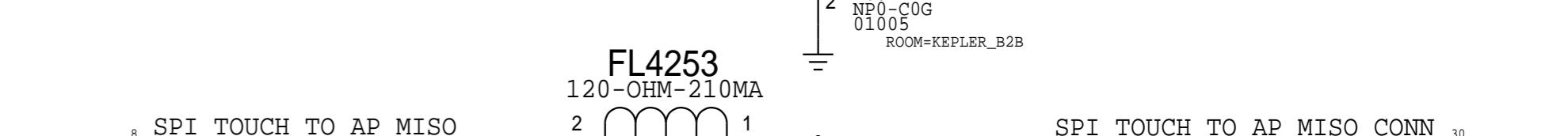
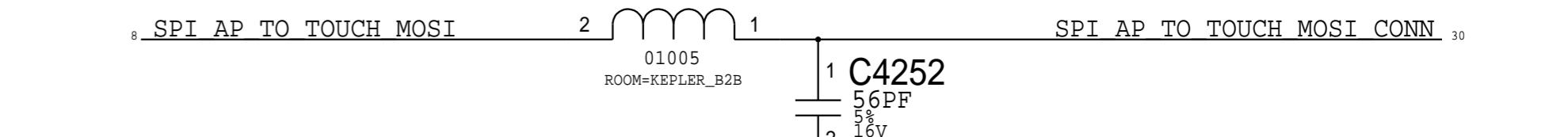
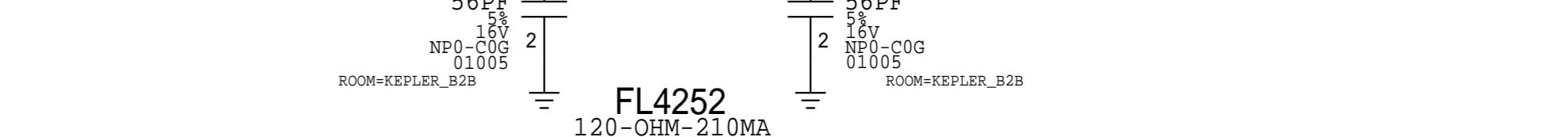
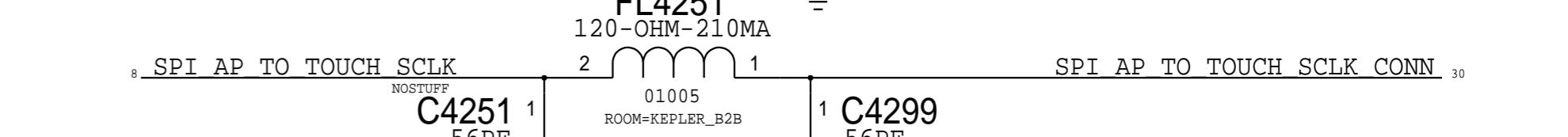
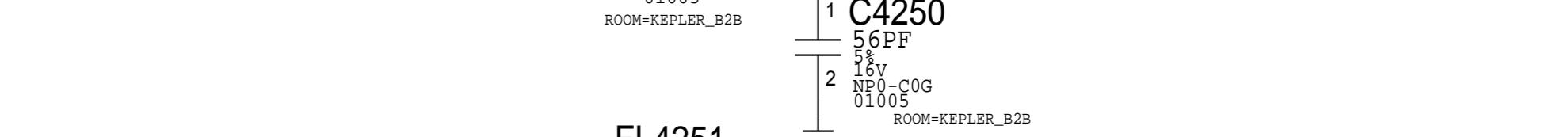
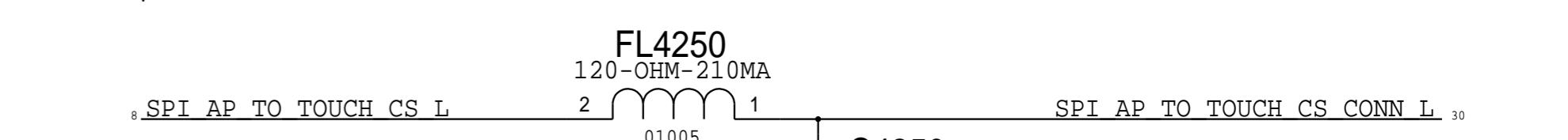


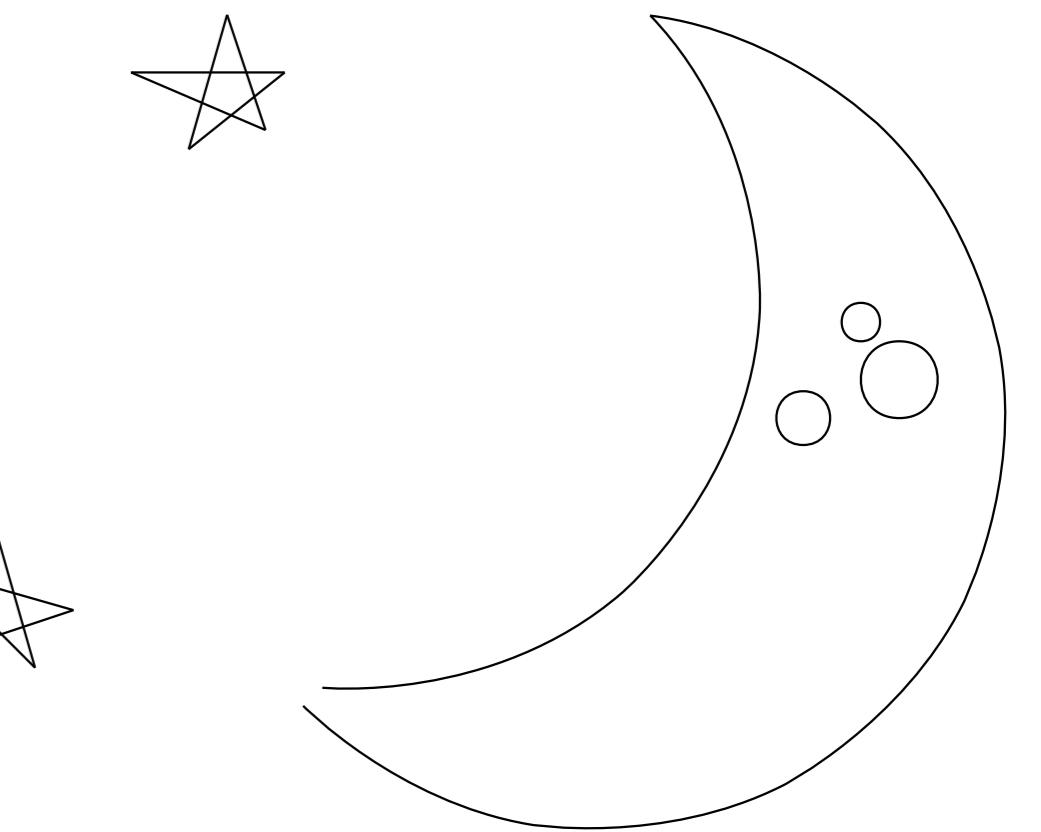
KEPLER CONNECTOR

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FLEX: 516S00050 (PLUG)



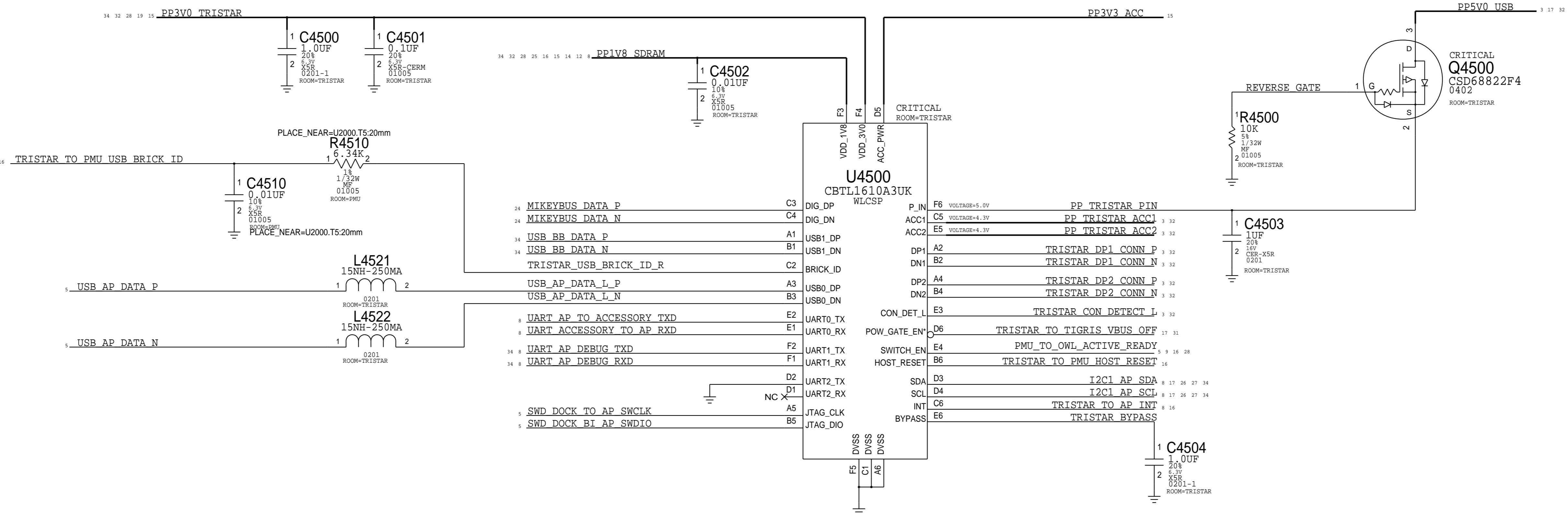
AP/TOUCH INTERFACE





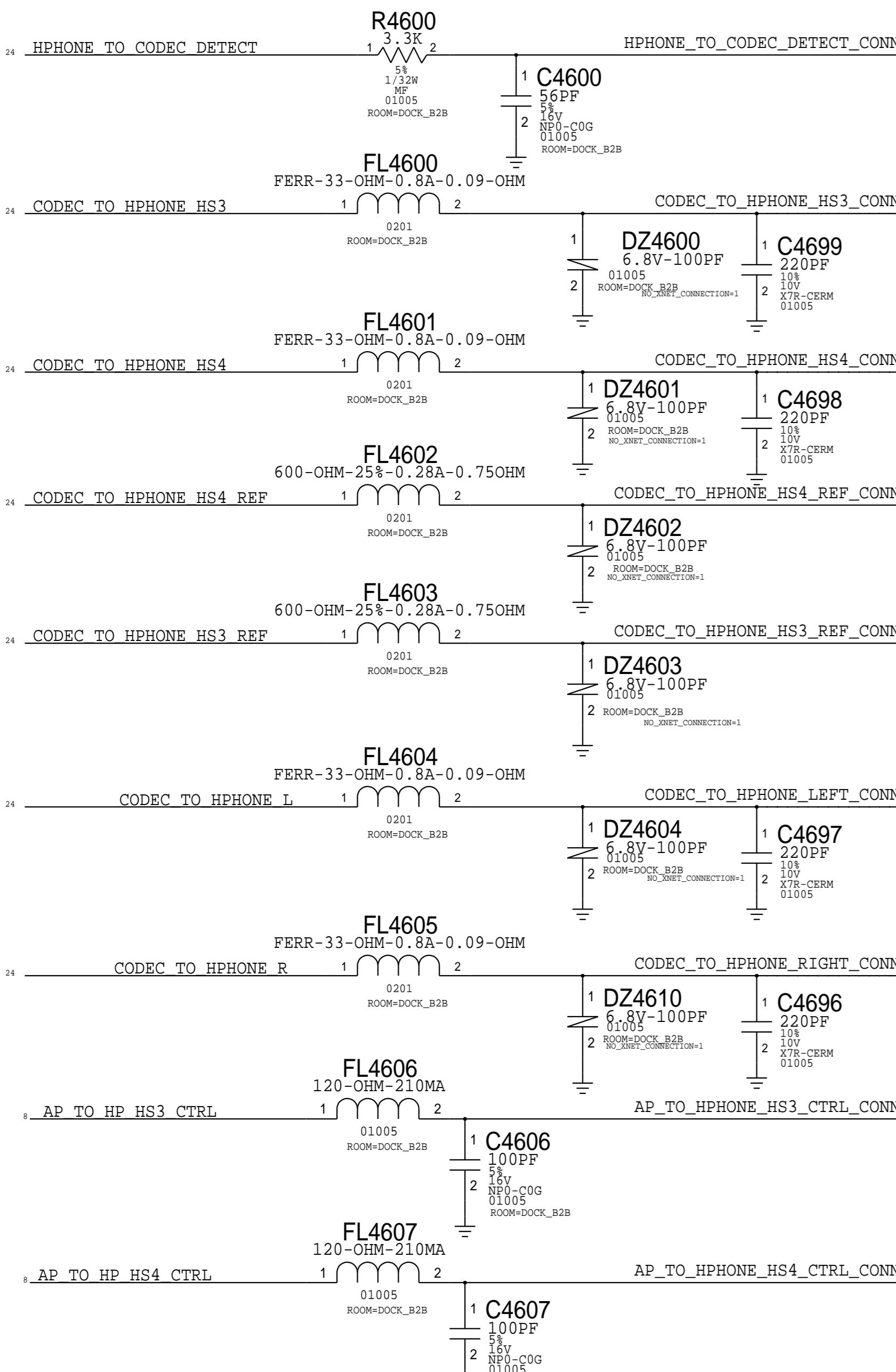
TRISTAR 2

APN:343S0695

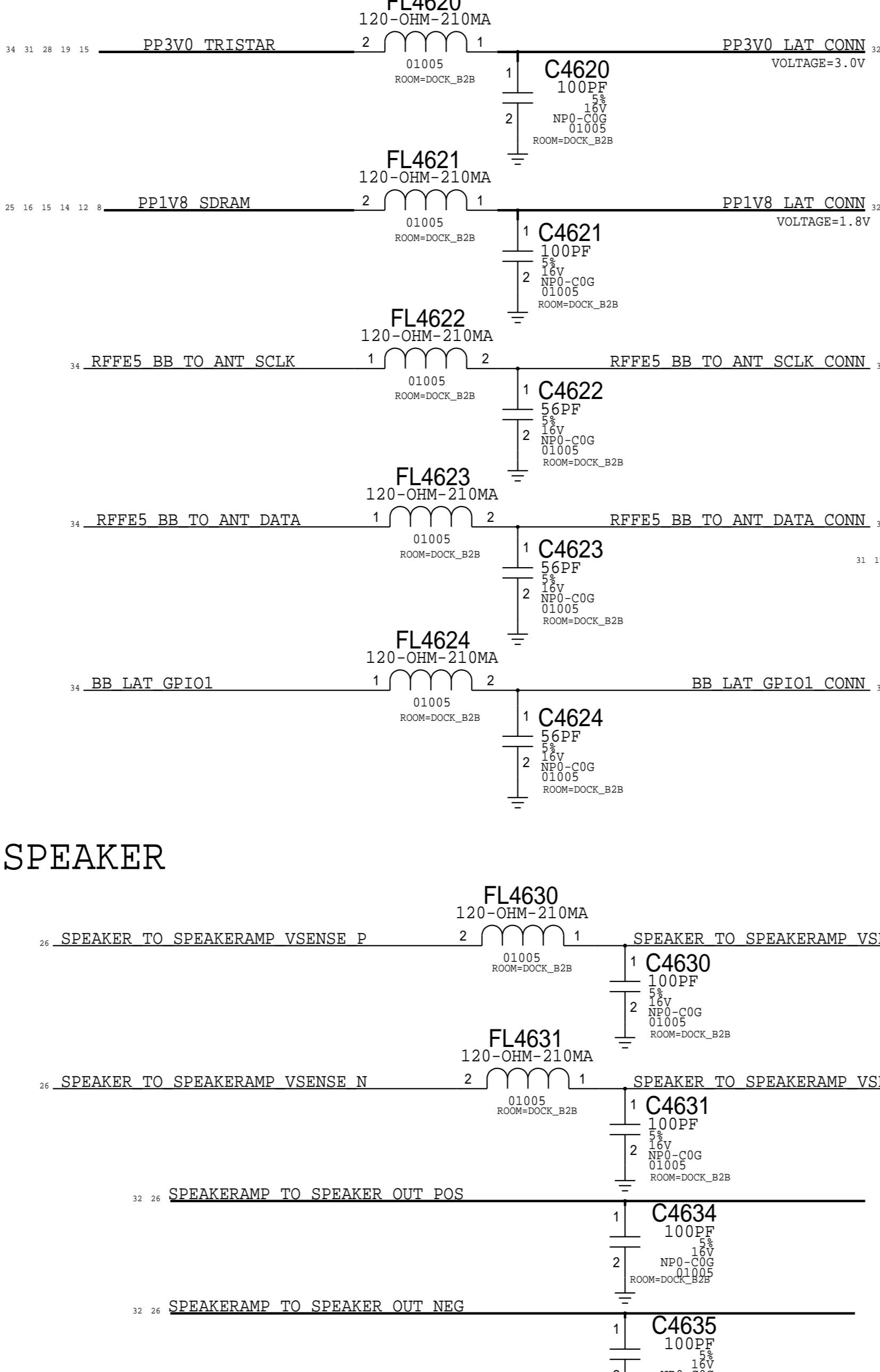


DOCK FLEX CONNECTOR

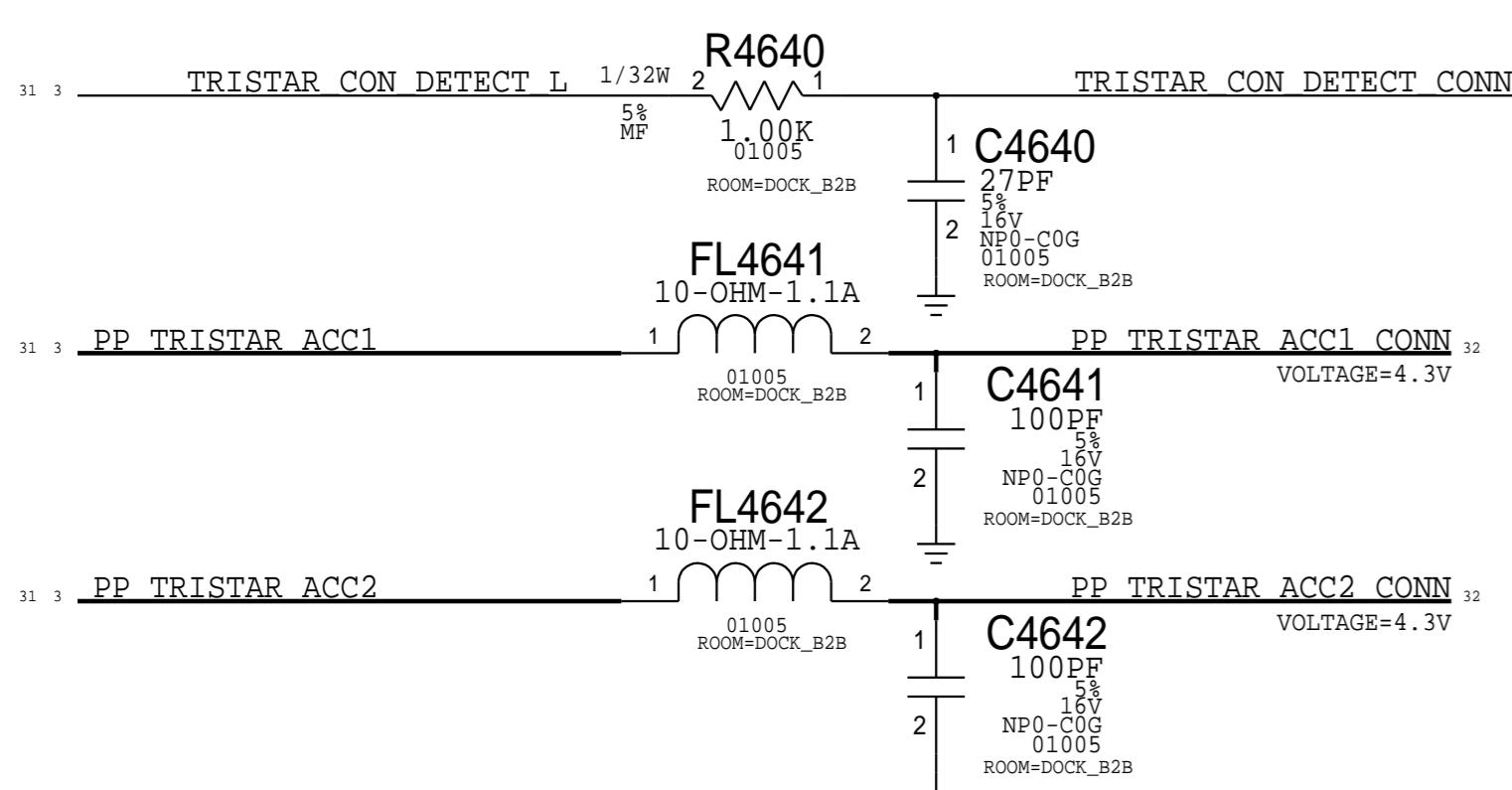
AUDIO JACK



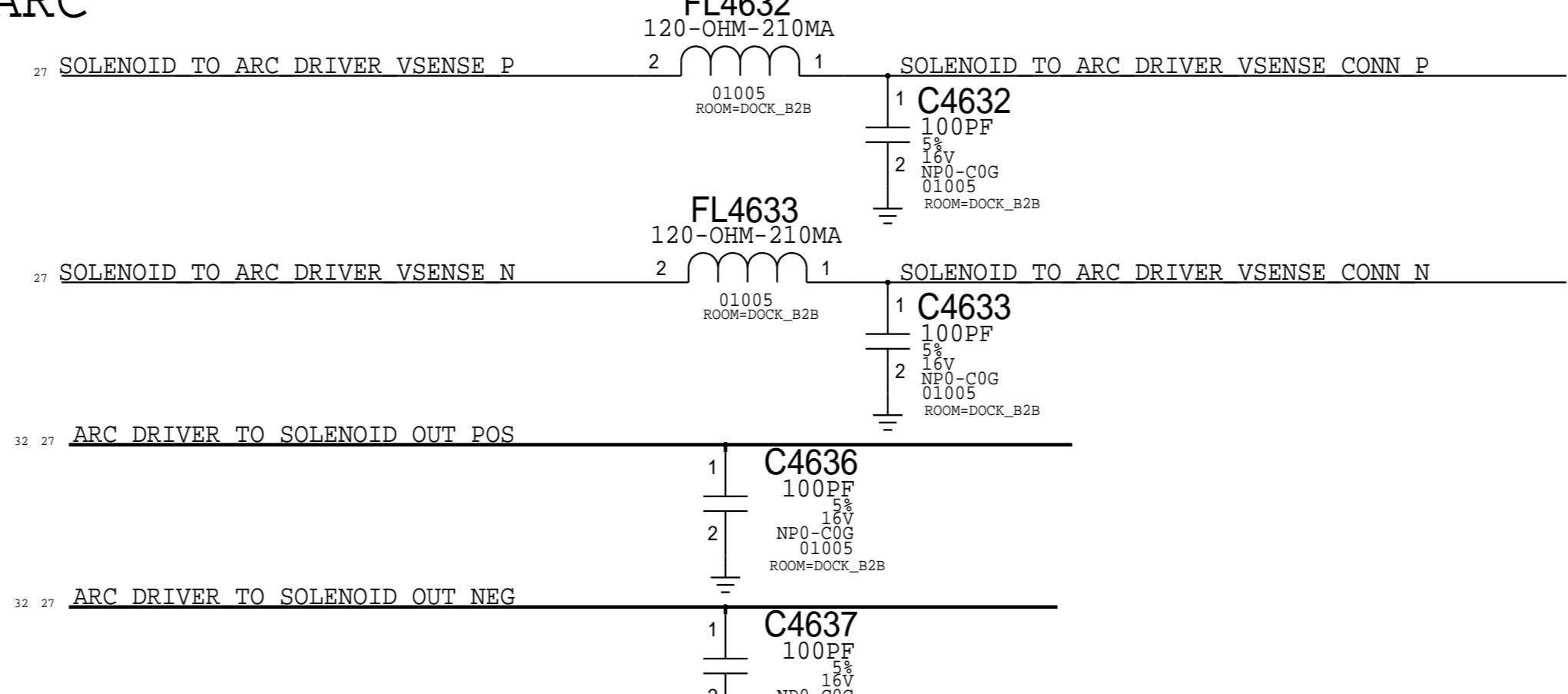
ANTENNA



TRISTRAR

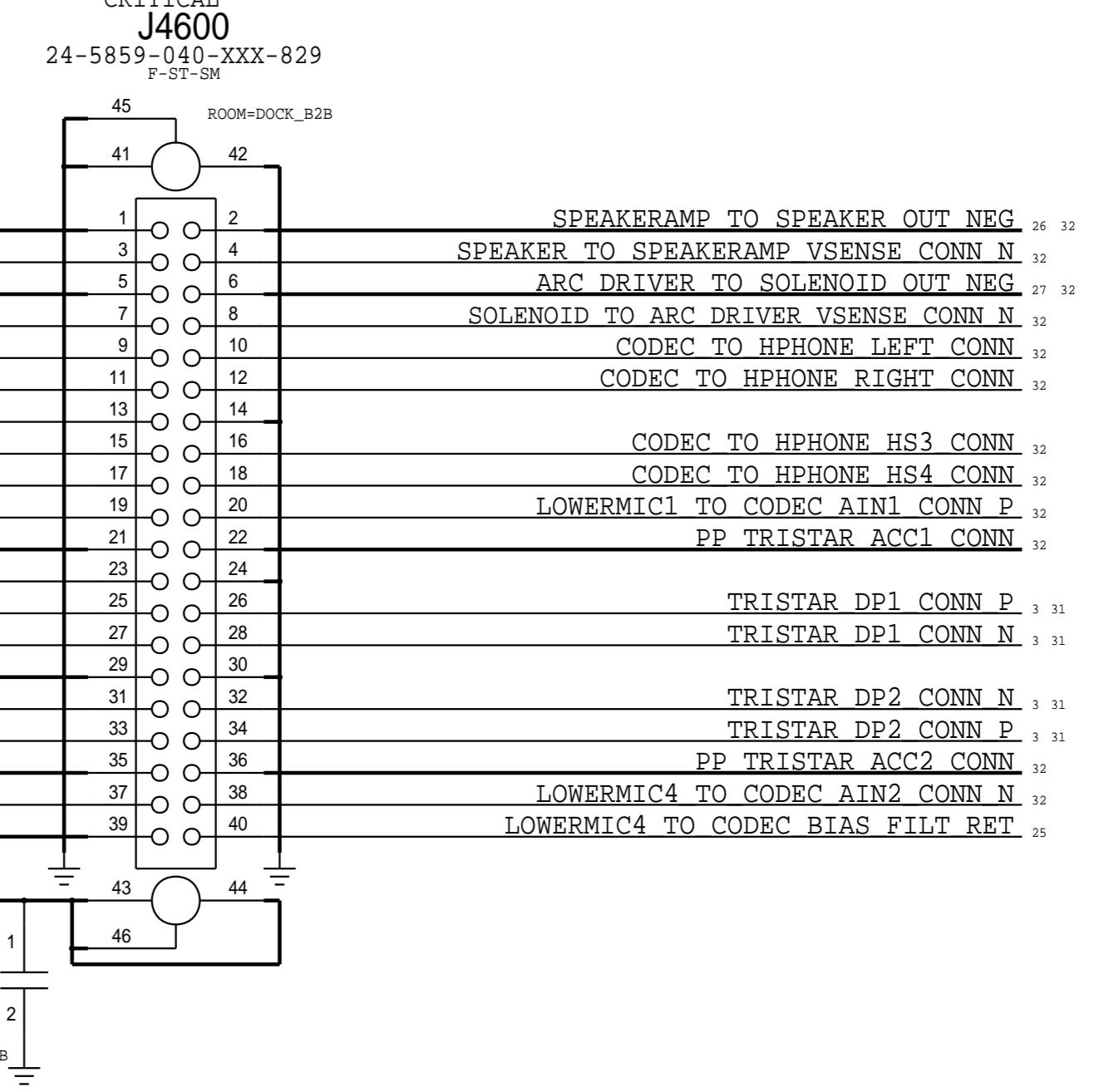


ARC

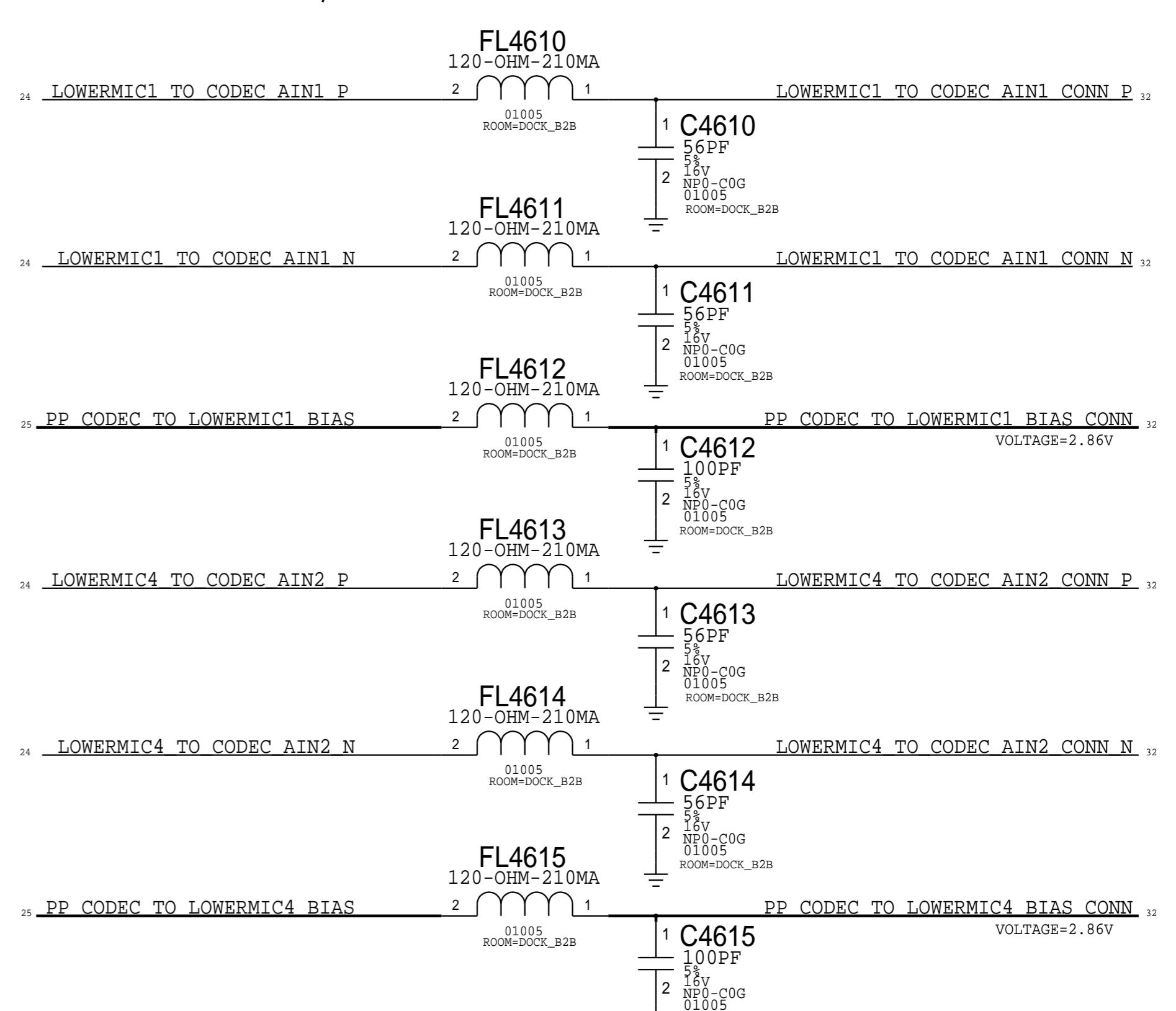


DOCK FLEX CONNECTOR

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FLEX: 516S00034 (PLUG)

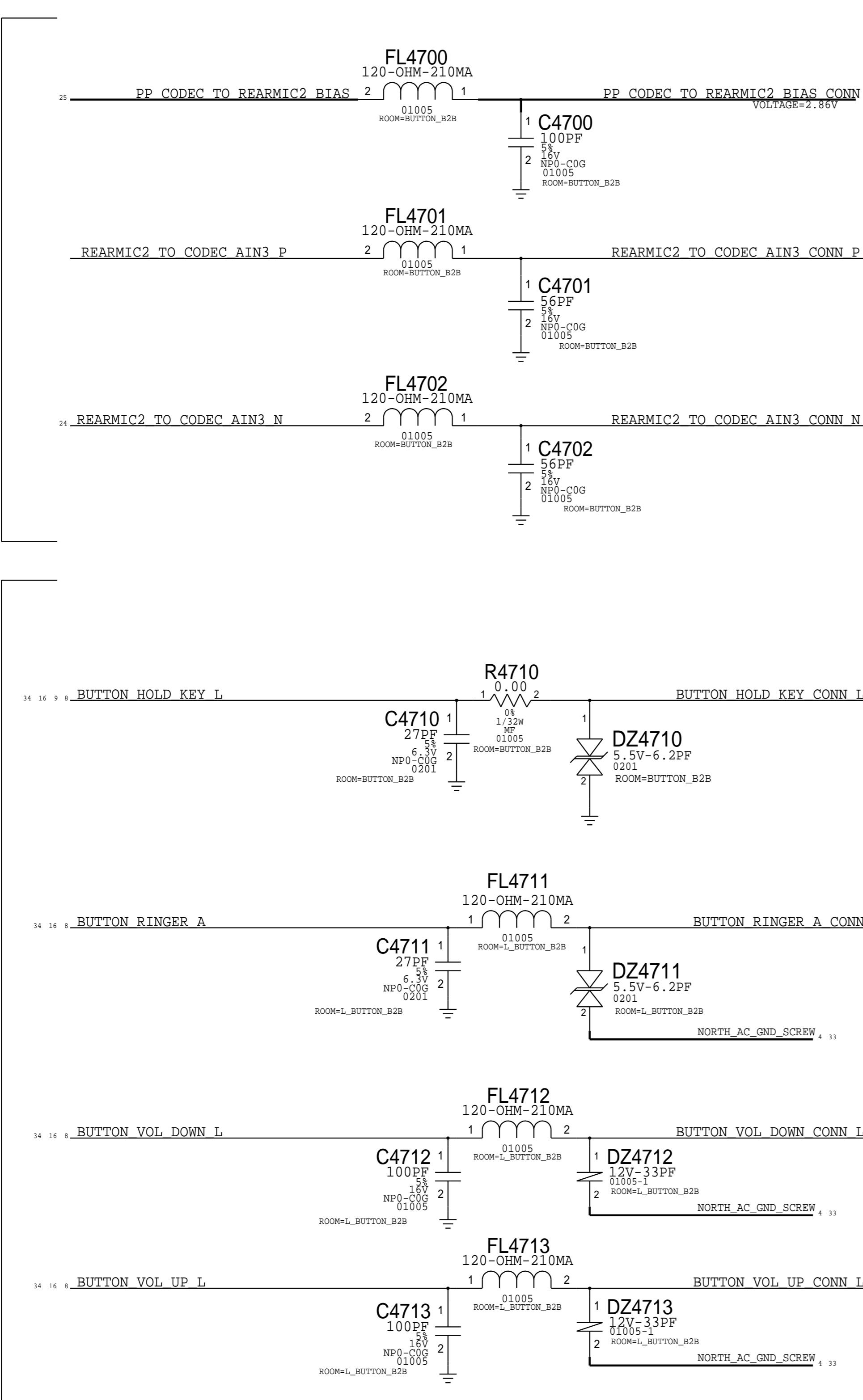


LOWER MIC1/4

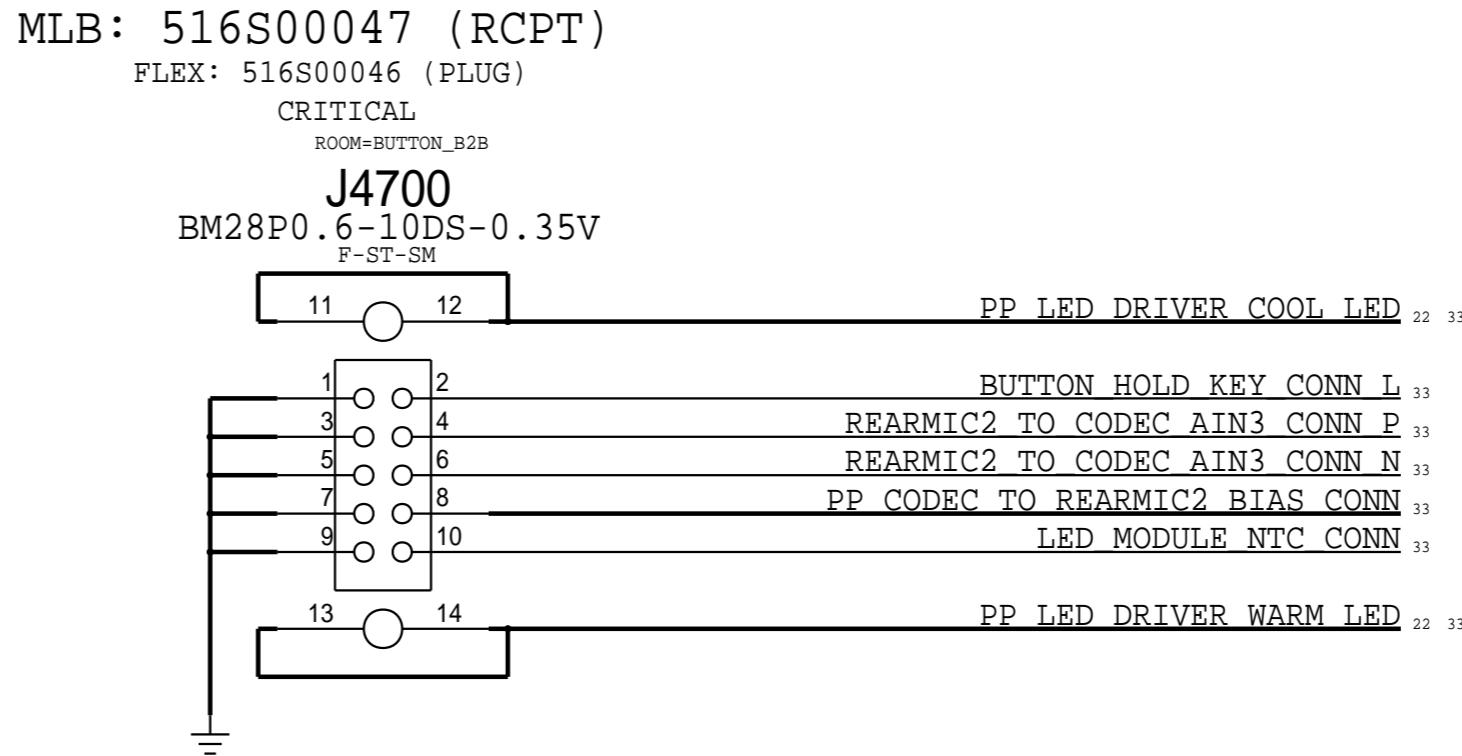


BUTTON FLEX

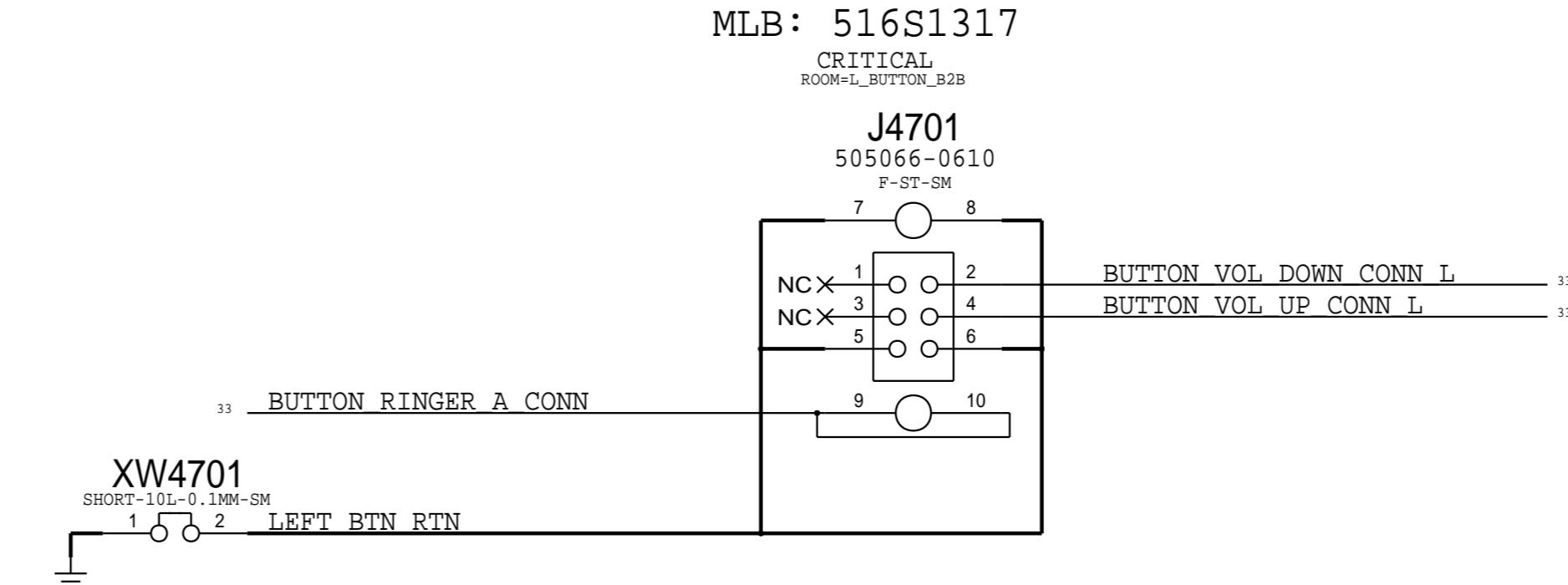
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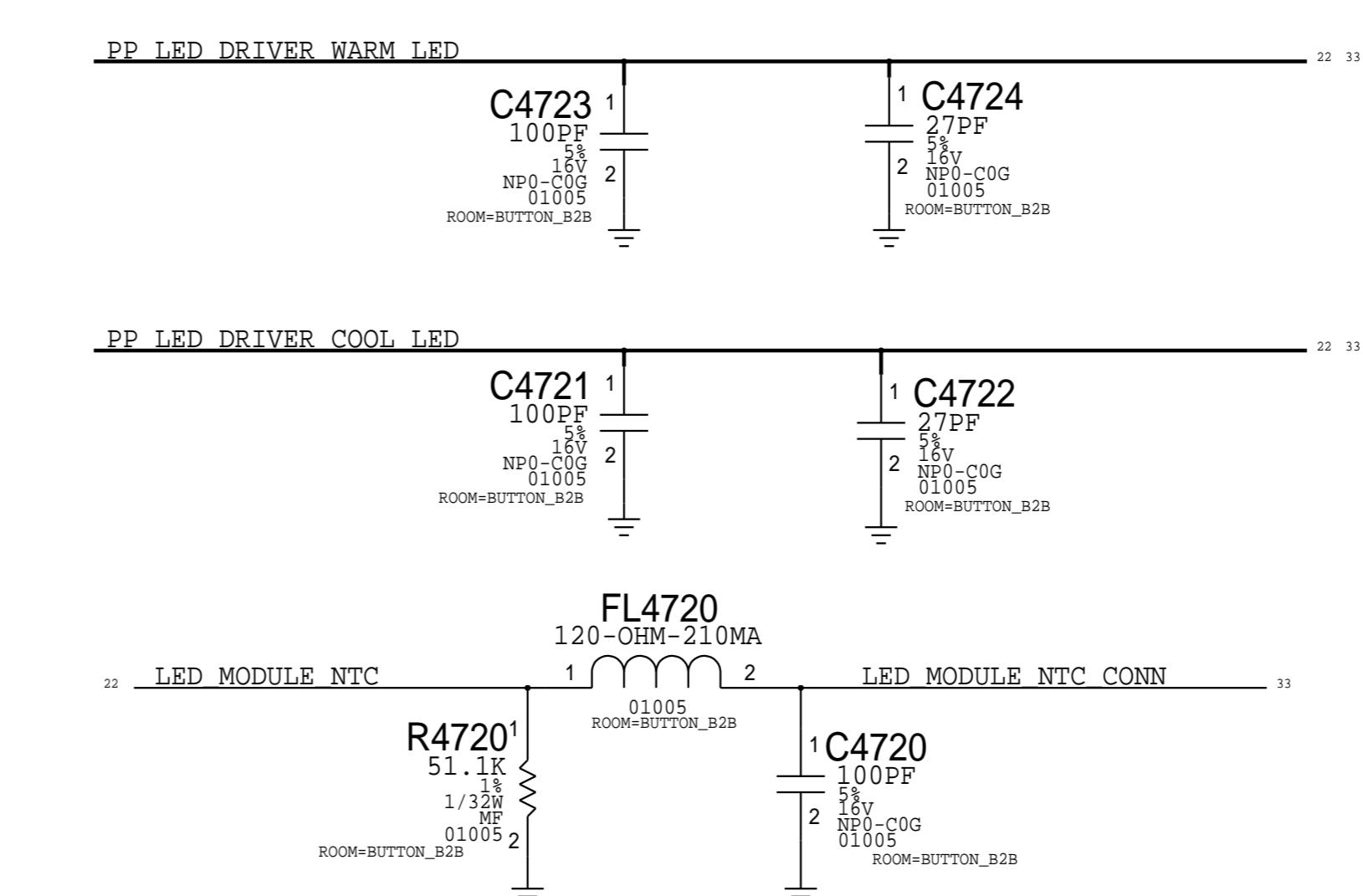
RIGHT BUTTON FLEX CONNECTOR



LEFT BUTTON FLEX CONNECTOR



STROBE:
WARM LED
COOL LED
MODULE NTC



SYNC_DATE=05/29/2018

BASEBAND, WLAN, BT & STOCKHOLM

D

D

C

C

B

B

A

A

SUBDESIGN_SUFFIX=RF									
RADIO_MLB_MIMO									
SHARED POWER									
PP_VCC_MAIN PP_VCC_MAIN PP3V0_TRISTAR PP3V0_TRISTAR PPIV8_SDRAM PPIV8_SDRAM									
BASEBAND					WLAN				
28 27 26 25 23 22 17 15 14 4 37 32 31 28 19 15 39 32 31 28 25 16 15 14 12 8	PP_VCC_MAIN PP3V0_TRISTAR PPIV8_SDRAM	PCIE_AP_TO_BB_TXD_P PCIE_AP_TO_BB_TXD_N PCIE_BB_TO_AP_RXD_P PCIE_BB_TO_AP_RXD_N PCIE_AP_TO_BB_REFCLK_P PCIE_AP_TO_BB_REFCLK_N PCIE_AP_TO_BB_RESET_L PCIE_BB_BI_AP_CLKREQ_L BB_TO_PMU_PCIE_HOST_WAKE_L AP_TO_BB_PCIE_DEV_WAKE I2S_AP_TO_BB_LRCLK I2S_AP_TO_BB_BCLK I2S_AP_TO_BB_DOUT I2S_BB_TO_AP_DIN AP_TO_BB_RADIO_ON_L PMU_TO_BB_PMIC_RESET_L AP_TO_BB_RESET_L BB_TO_AP_RESET_DETECT_L BB_TO_LED_DRIVER_GSM_BURST_IND AP_TO_BB_MESA_ON_L BB_TO_AP_GPS_TIME_MARK AP_TO_BB_COREDUMP BB_IPC_GPIO LCM_TO_OWL_BSYNC UART_OWL_TO_BB_TXD UART_BB_TO_OWL_RXD USB_BB_DATA_P USB_BB_DATA_N PMU_TO_BB_USB_VBUS_DETECT SWD_AP_PERIPHERAL_SWCLK SWD_AP_BT_BB_SWDIO RFFE5_BB_TO_ANT_SCLK RFFE5_BB_TO_ANT_DATA BB_LAT_GPIO1 BB_TO_PMU_AMUX_LDO11_SIM1 BB_TO_PMU_AMUX_SMPS1 BB_TO_PMU_AMUX_SMPS3 BB_TO_PMU_AMUX_SMPS4	PCIE_AP_TO_BB_TX_P PCIE_AP_TO_BB_TX_N PCIE_BB_TO_AP_RX_P PCIE_BB_TO_AP_RX_N PCIE_AP_TO_BB_REFCLK_P PCIE_AP_TO_BB_REFCLK_N PCIE_AP_TO_BB_PERST_L PCIE_AP_TO_BB_CLKREQ_L PCIE_BB_TO_PMU_HOST_WAKE_L PCIE_AP_TO_BB_DEV_WAKE I2S_AP_TO_BB_WS I2S_AP_TO_BB_CLK I2S_AP_TO_BB_TX I2S_BB_TO_AP_TX AP_TO_BBPBMU_RADIO_ON_L PMU_TO_BBPBMU_RESET_L AP_TO_BB_RST_L BB_TO_AP_RESET_DET_L BB_TO_AP_GSM_TXBURST_IND AP_TO_BB_MESA_ON_L BB_TO_AP_GPS_TIME_MARK AP_TO_BB_COREDUMP_TRIG AP_TO_BB_IPC_GPIO TOUCH_TO_BBPBMU_FORCE_PWM UART0_OWL_TO_BB_TX UART0_BB_TO_OWL_TX USB_BB_P USB_BB_N USB_BB_VBUS_DETECT SWD_CLK_BB_JTAG_TCK SWD_IO_BB_JTAG_TMS 75_RFFE5_SDCLK_BB 75_RFFE5_SDATA_BB RFFE_BUFFER_LAT_GPIO1 BB_TO_PMU_AMUX_LDO11_SIM1 BB_TO_PMU_AMUX_SMPS1 BB_TO_PMU_AMUX_SMPS3 BB_TO_PMU_AMUX_SMPS4	PCIE_AP_TO_WLAN_TX_P PCIE_AP_TO_WLAN_TX_N PCIE_WLAN_TO_AP_RX_P PCIE_WLAN_TO_AP_RX_N PCIE_AP_TO_WLAN_REFCLK_P PCIE_AP_TO_WLAN_REFCLK_N PCIE_AP_TO_WLAN_PERST_L PCIE_AP_TO_WLAN_DEV_WAKE PCIE_AP_TO_WLAN_CLKREQ_L I2S_AP_TO_BB_WS I2S_AP_TO_BB_CLK I2S_AP_TO_BB_TX I2S_BB_TO_AP_TX AP_TO_BBPBMU_RADIO_ON_L PMU_TO_BBPBMU_RESET_L AP_TO_BB_RST_L BB_TO_AP_RESET_DET_L BB_TO_AP_GSM_TXBURST_IND AP_TO_BB_MESA_ON_L BB_TO_AP_GPS_TIME_MARK AP_TO_BB_COREDUMP_TRIG AP_TO_BB_IPC_GPIO TOUCH_TO_BBPBMU_FORCE_PWM UART0_OWL_TO_BB_TX UART0_BB_TO_OWL_TX USB_BB_P USB_BB_N USB_BB_VBUS_DETECT SWD_CLK_BB_JTAG_TCK SWD_IO_BB_JTAG_TMS 75_RFFE5_SDCLK_BB 75_RFFE5_SDATA_BB RFFE_BUFFER_LAT_GPIO1 BB_TO_PMU_AMUX_LDO11_SIM1 BB_TO_PMU_AMUX_SMPS1 BB_TO_PMU_AMUX_SMPS3 BB_TO_PMU_AMUX_SMPS4	PCIE_AP_TO_WLAN_TXD_P PCIE_AP_TO_WLAN_TXD_N PCIE_WLAN_TO_AP_RXD_P PCIE_WLAN_TO_AP_RXD_N PCIE_AP_TO_WLAN_REFCLK_P PCIE_AP_TO_WLAN_REFCLK_N PCIE_AP_TO_WLAN_RESET_L PCIE_AP_TO_WLAN_DEV_WAKE PCIE_AP_TO_WLAN_CLKREQ_L UART_AP_TO_WLAN_TXD UART_AP_TO_WLAN_RTS_L UART_WLAN_TO_AP_RXD UART_WLAN_TO_AP_CTS_L PMU_TO_WLAN_CLK32K PMU_TO_WLAN_REG_ON WLAN_TO_PMU_HOST_WAKE OWL_TO_WLAN_CONTEXT_A OWL_TO_WLAN_CONTEXT_B	I2S_AP_TO_BT_LRCLK I2S_AP_TO_BT_BCLK I2S_AP_TO_BT_DOUT I2S_BT_TO_AP_DOUT UART_AP_TO_BT_TXD UART_AP_TO_BT_RTS_L UART_BT_TO_AP_RXD UART_BT_TO_AP_CTS_L PMU_TO_BT_REG_ON BT_TO_PMU_HOST_WAKE AP_TO_BT_WAKE	UART3_AP_TO_STOCKHOLM_TXD UART3_AP_TO_STOCKHOLM_RTS_L UART3_STOCKHOLM_TO_AP_RXD UART3_STOCKHOLM_TO_AP_RTS_L PMU_TO_STOCKHOLM_EN STOCKHOLM_TO_PMU_HOST_WAKE AP_TO_STOCKHOLM_DEV_WAKE AP_TO_STOCKHOLM_FW_DLWD_REQ	UART_AP_TO_STOCKHOLM_TXD UART_AP_TO_STOCKHOLM_RTS_L UART_STOCKHOLM_TO_AP_RXD UART_STOCKHOLM_TO_AP_CTS_L PMU_TO_STOCKHOLM_EN STOCKHOLM_TO_PMU_HOST_WAKE AP_TO_STOCKHOLM_DEV_WAKE AP_TO_STOCKHOLM_FW_DLWD_REQUEST	AP_TO_STOCKHOLM_FW_DLWD_REQUEST
ANT					AP DEBUG				
60 4 AP_TO_STOCKHOLM_ANT	PP1V8 DFU_STATUS FORCE_DFU	PMU_TO_SYSTEM_COLD_RESET_L I2C0_AP_SCL I2C0_AP_SDA I2C1_AP_SCL I2C1_AP_SDA BUTTON_HOLD_KEY_L BUTTON_MENU_KEY_L BUTTON_RINGER_A BUTTON_VOL_DOWN_L BUTTON_VOL_UP_L NC_PMU_GPIO20 NC_PMU_GPIO21 NC_OWL_FUNC2 NC_AP_RESERVED2 UART_AP_DEBUG_RXD UART_AP_DEBUG_TXD NC_PMU_AMUX_AY NC_PMU_AMUX_BY	PMU_TO_SYSTEM_COLD_RESET_L I2C0_AP_SCL I2C0_AP_SDA I2C1_AP_SCL I2C1_AP_SDA BUTTON_HOLD_KEY_L BUTTON_MENU_KEY_L BUTTON_RINGER_A BUTTON_VOL_DOWN_L BUTTON_VOL_UP_L PMU_GPIO20 PMU_GPIO21 OWL_FUNC2 AP_RESERVED2 AP_RESERVED1 AP_RESERVED0 PMU_AMUX_AY PMU_AMUX_BY	PMU_TO_STOCKHOLM_EN STOCKHOLM_TO_PMU_HOST_WAKE AP_TO_STOCKHOLM_DEV_WAKE AP_TO_STOCKHOLM_FW_DLWD_REQ	UART_AP_TO_STOCKHOLM_TXD UART_AP_TO_STOCKHOLM_RTS_L UART_STOCKHOLM_TO_AP_RXD UART_STOCKHOLM_TO_AP_CTS_L PMU_TO_STOCKHOLM_EN STOCKHOLM_TO_PMU_HOST_WAKE AP_TO_STOCKHOLM_DEV_WAKE AP_TO_STOCKHOLM_FW_DLWD_REQUEST	AP_TO_STOCKHOLM_FW_DLWD_REQUEST			

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
4	0003980769	ENGINEERING RELEASED	2015-03-27

N66 RADIO_MLB_MIMO - EVT_MD

MARCH 26, 2015

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15	41	CELLULAR TRANSCEIVER: TX PORTS
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18	44	CELLULAR FRONT END: HB PAD
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22	48	CELLULAR FRONT END: DIVERSITY
23	49	SIM
24	50	WIFI/BT: WIFI/BT MODULE
25	51	STOCKHOLM

HB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00376	1	IC, PWR AMP,HB_PAD,TQS	UHBPA_RF	ROW
353S4494	1	IC, PWR AMP,HB_PAD,AVAGO	UHBPA_RF	RF2
353S00376	1	IC, PWR AMP,HB_PAD,TQS	UHBPA_RF	RFC

LB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00461	1	IC, PWR AMP,LB_PAD,SKWS	ULBPA_RF	ROW
353S00056	1	IC, PWR AMP,LB_PAD,MURATA	ULBPA_RF	RF2
353S00461	1	IC, PWR AMP,LB_PAD,SKWS	ULBPA_RF	RFC

VINYL

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
337S00125	1	IC, VINYL	U5101_RF	ROW
337S00125	1	IC, VINYL	U5101_RF	RF2

VINYL RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
117S0161	1	0 OHM, RESISTOR	R3402_RF	RFC

ROW HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	ROW
152S2007	1	8.2NH, INDUCTOR	L4401_RF	ROW
131S0426	1	22PF, CAPACITOR	C4405_RF	ROW
152S2042	1	1.8NH, INDUCTOR	C4406_RF	ROW
131S0425	1	0.5PF, CAPACITOR	L4407_RF	ROW
152S2041	1	10.0NH, INDUCTOR	L4403_RF	ROW
131S00071	1	33PF, CAPACITOR	C4407_RF	ROW
152S000143	1	15NH, INDUCTOR	L4404_RF	ROW
131S00071	1	33PF, CAPACITOR	C4408_RF	ROW
117S0108	1	51 OHM, RESISTOR	L4410_RF	ROW
131S0599	1	1.5PF, CAPACITOR	C3921_RF	ROW
152S00052	1	3.4NH, INDUCTOR	L3910_RF	ROW
117S0201	1	0 OHM, RESISTOR	L3911_RF	ROW
152S2039	1	3.8NH, INDUCTOR	L3919_RF	ROW
131S0414	1	5.0PF, CAPACITOR	C4410_RF	ROW

RF2 HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1990	1	3.0NH, INDUCTOR	L4105_RF	RF2
131S0377	1	1.2PF, CAPACITOR	C4108_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4401_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4405_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4406_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4407_RF	RF2
131S00001	1	0.1PF, CAPACITOR	L4403_RF	RF2
152S2051	1	1.3NH, INDUCTOR	C4407_RF	RF2
152S2051	1	1.3NH, INDUCTOR	C4408_RF	RF2
131S0805	1	100PF, CAPACITOR	C4409_RF	RF2
131S0431	1	0.2PF, CAPACITOR	L4410_RF	RF2
131S0381	1	1.6PF, CAPACITOR	C3921_RF	RF2
152S00027	1	3.7NH, INDUCTOR	L3910_RF	RF2
117S0201	1	0 OHM, RESISTOR	L3911_RF	RF2
152S2045	1	3.0NH, INDUCTOR	L3919_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3912_RF	RF2
131S0599	1	1.5PF, CAPACITOR	C3922_RF	RF2
131S0630	1	27PF, CAPACITOR	C3911_RF	RF2
131S0414	1	5.0PF, CAPACITOR	C4410_RF	RF2

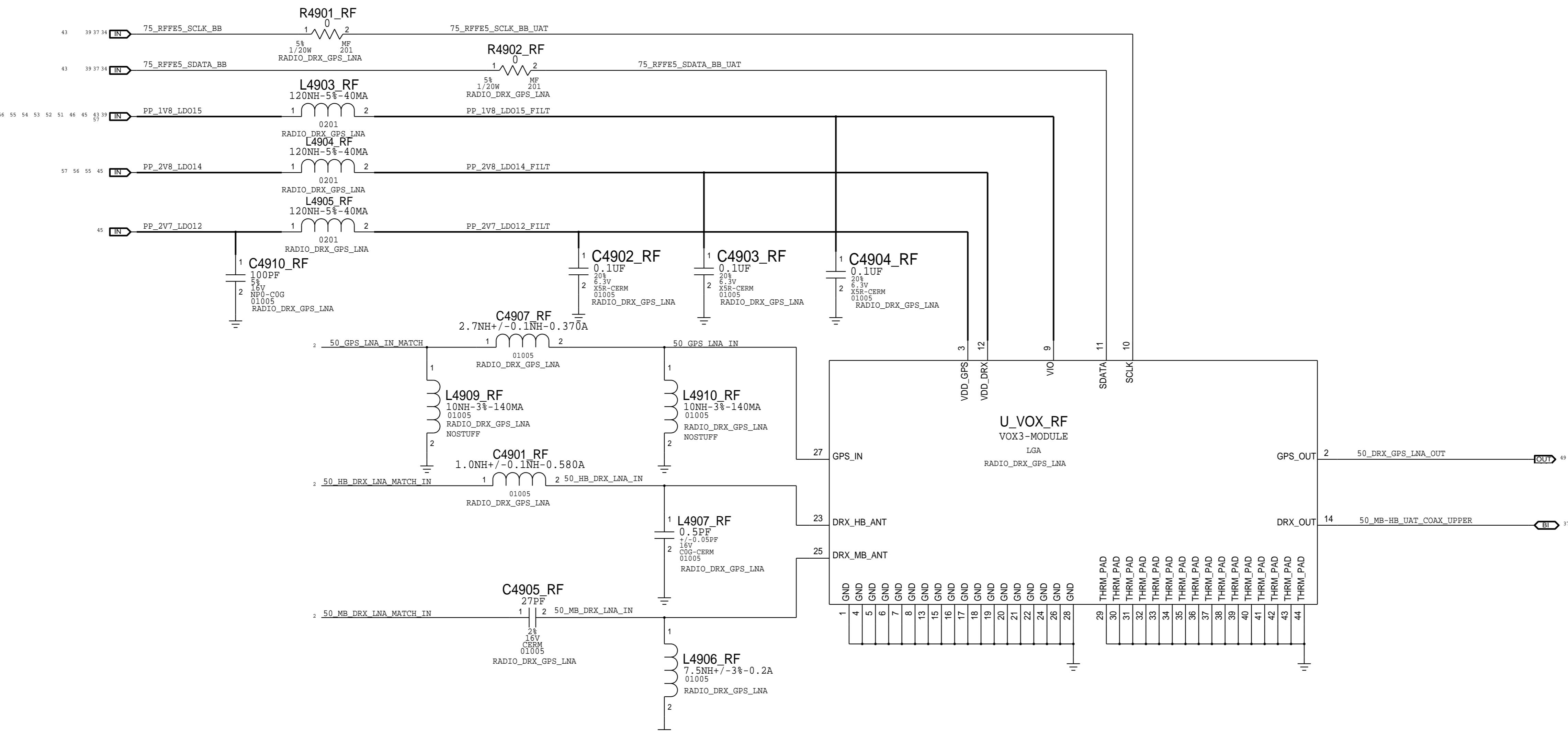
RF2 LB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
131S0551	1	1.2PF, CAPACITOR	L4203_RF	RF2
152S2004	1	3.3NH, INDUCTOR	C4205_RF	RF2
131S0551	1	1.2PF, CAPACITOR	L4204_RF	RF2
152S2020	1	3.6NH, INDUCTOR	C4206_RF	RF2
131S0551	1	1.2PF, CAPACITOR	L4205_RF	RF2
152S00202	1	4.0NH, INDUCTOR	C4207_RF	RF2
131S0336	1	1.3PF, CAPACITOR	L4206_RF	RF2
152S2022	1	4.3NH, INDUCTOR	C4208_RF	RF2
131S0555	1	1.0PF, CAPACITOR	L4207_RF	RF2
152S00052	1	3.4NH, INDUCTOR	C4209_RF	RF2
131S0551	1	1.2PF, CAPACITOR	L4209_RF	RF2
152S00158	1	4.1NH, INDUCTOR	C4211_RF	RF2
131S00070	1	1.3PF, CAPACITOR	L4210_RF	RF2
152S00180	1	4.2NH, INDUCTOR	C4212_RF	RF2
131S0560	1	1.1PF, CAPACITOR	L4211_RF	RF2
152S00027	1	3.7NH, INDUCTOR	C4213_RF	RF2
152S00202	1	4.0NH, INDUCTOR	L4201_RF	RF2
152S2045	1	3.0NH, INDUCTOR	R4201_RF	RF2
152S2056	1	5.6NH, INDUCTOR	L4202_RF	RF2
152S1996	1	15NH, INDUCTOR	C3913_RF	RF2
131S00074	1	30PF, CAPACITOR	L3902_RF	RF2
152S1995	1	12NH, INDUCTOR	C3902_RF	RF2

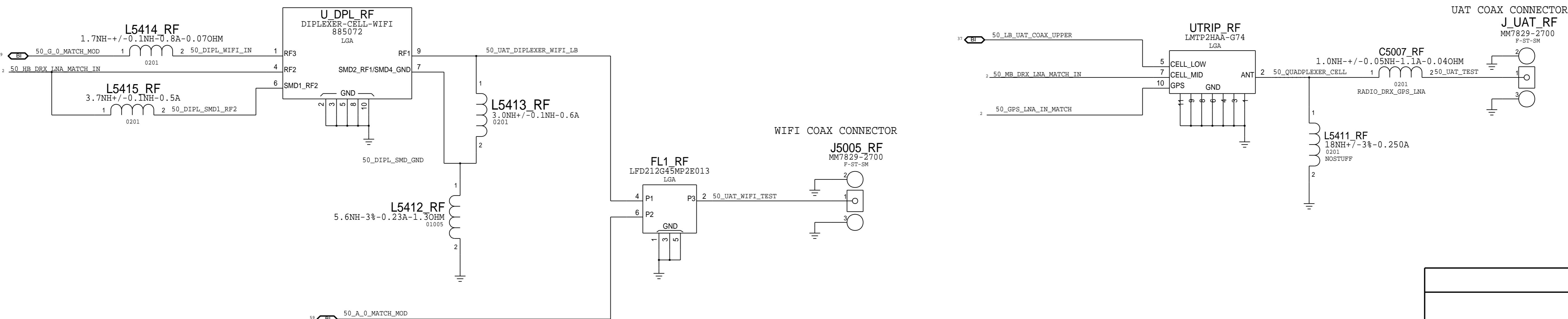
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N66-SPECIFIC RADIO PAGE 2

DIVERSITY LNA



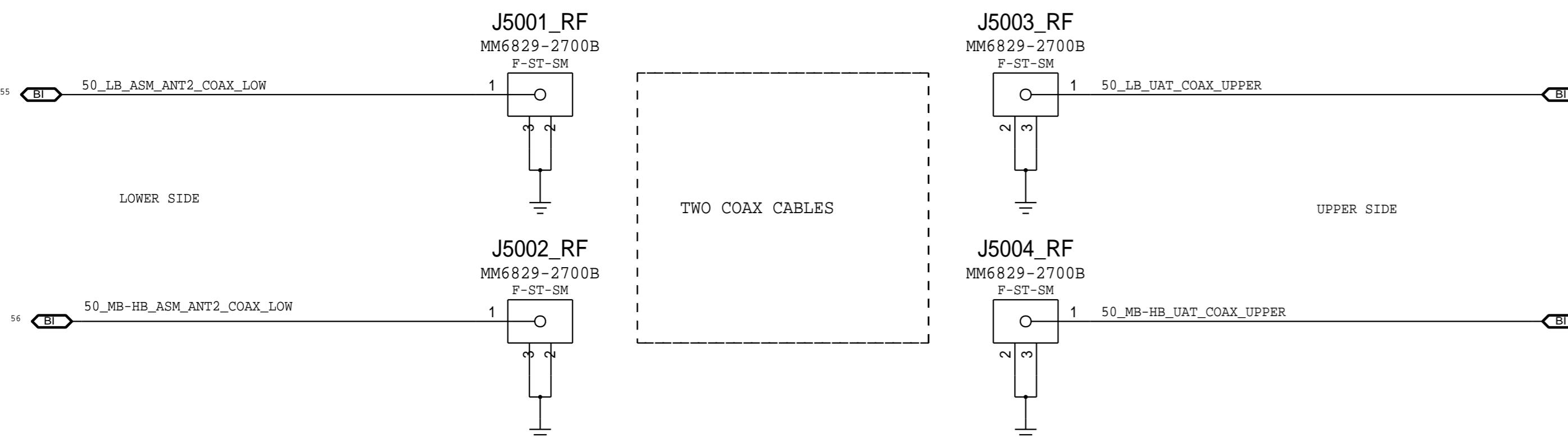
UAT ANT FEED



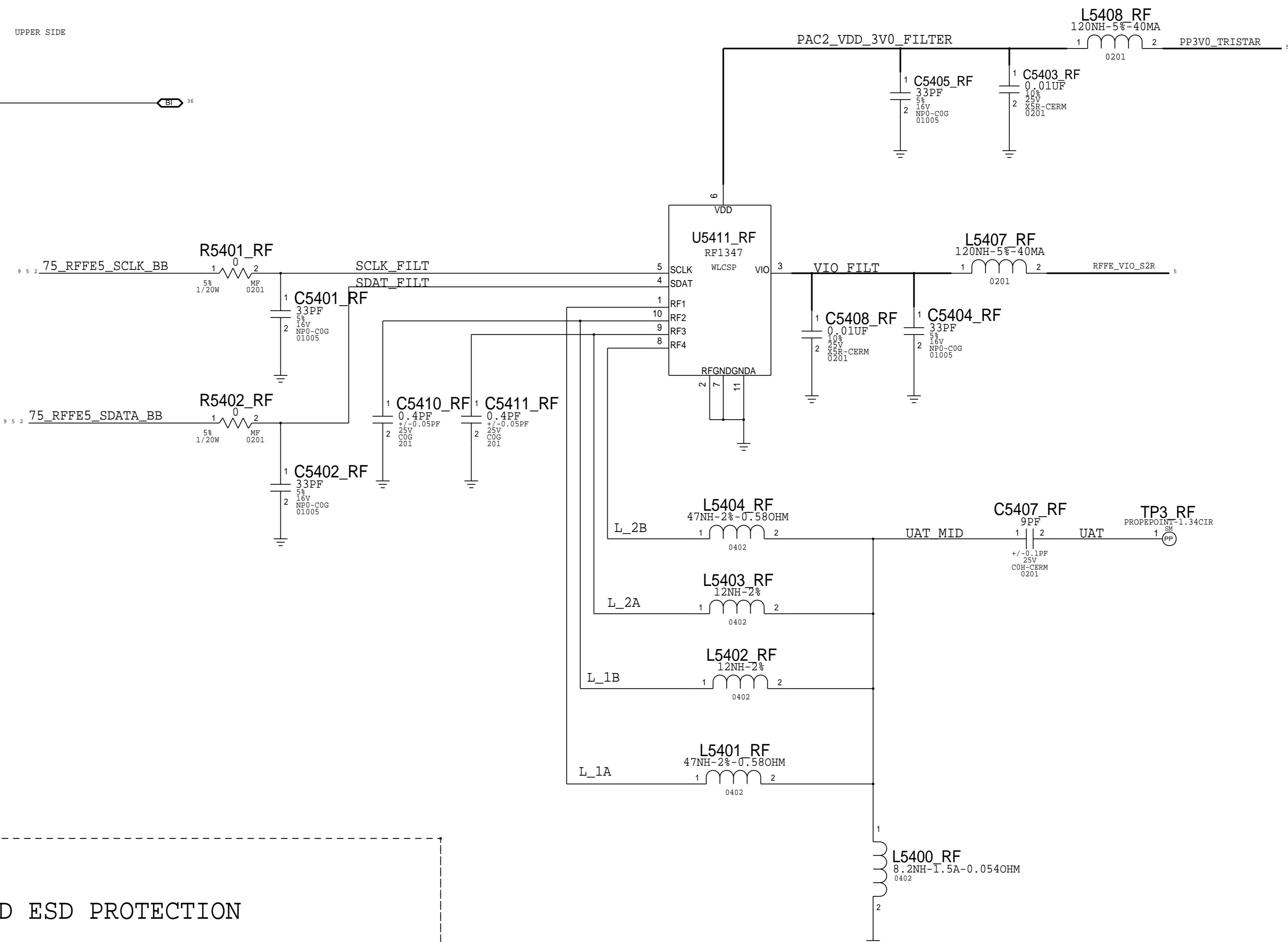
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N66-SPECIFIC RADIO PAGE 3

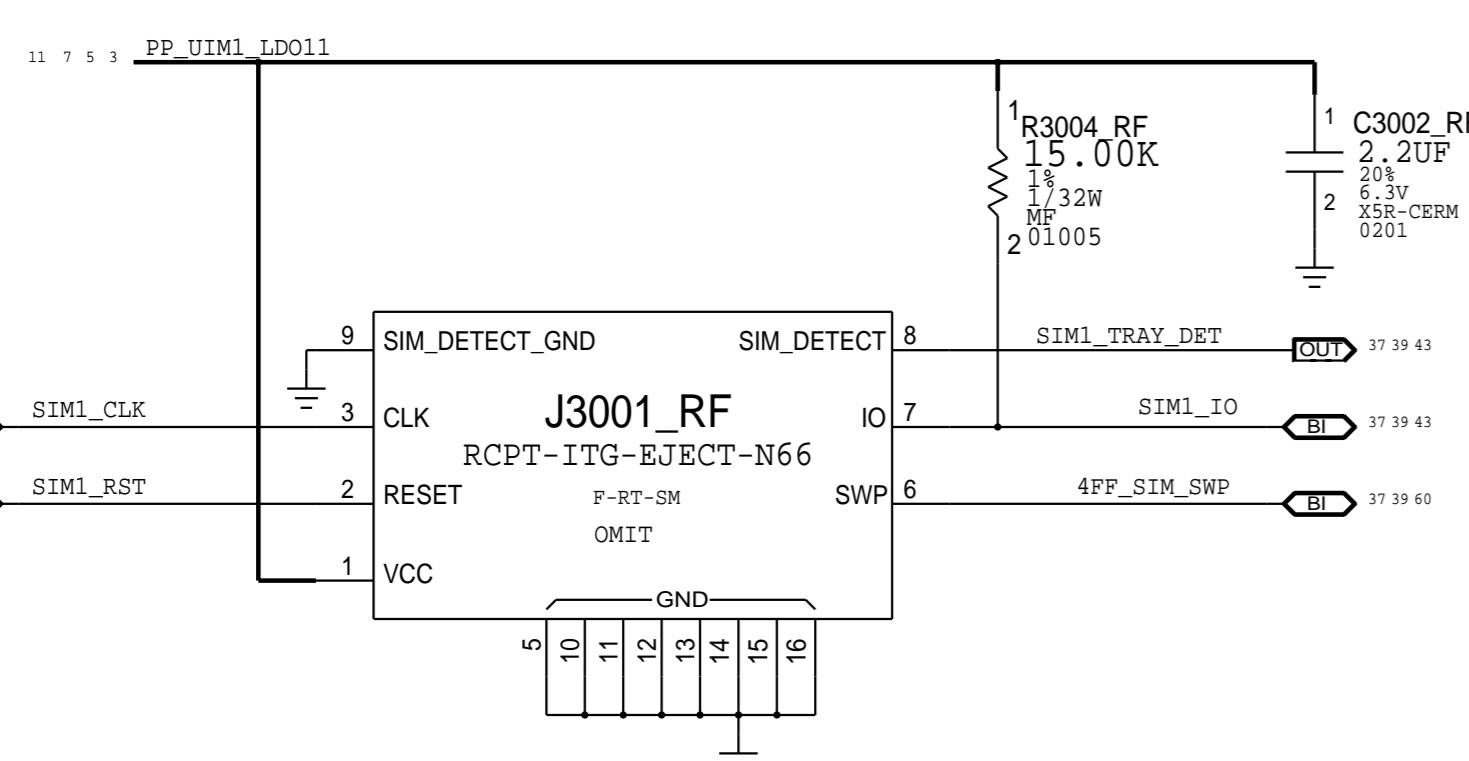
ANTENNA FEEDS AND CONNECTORS



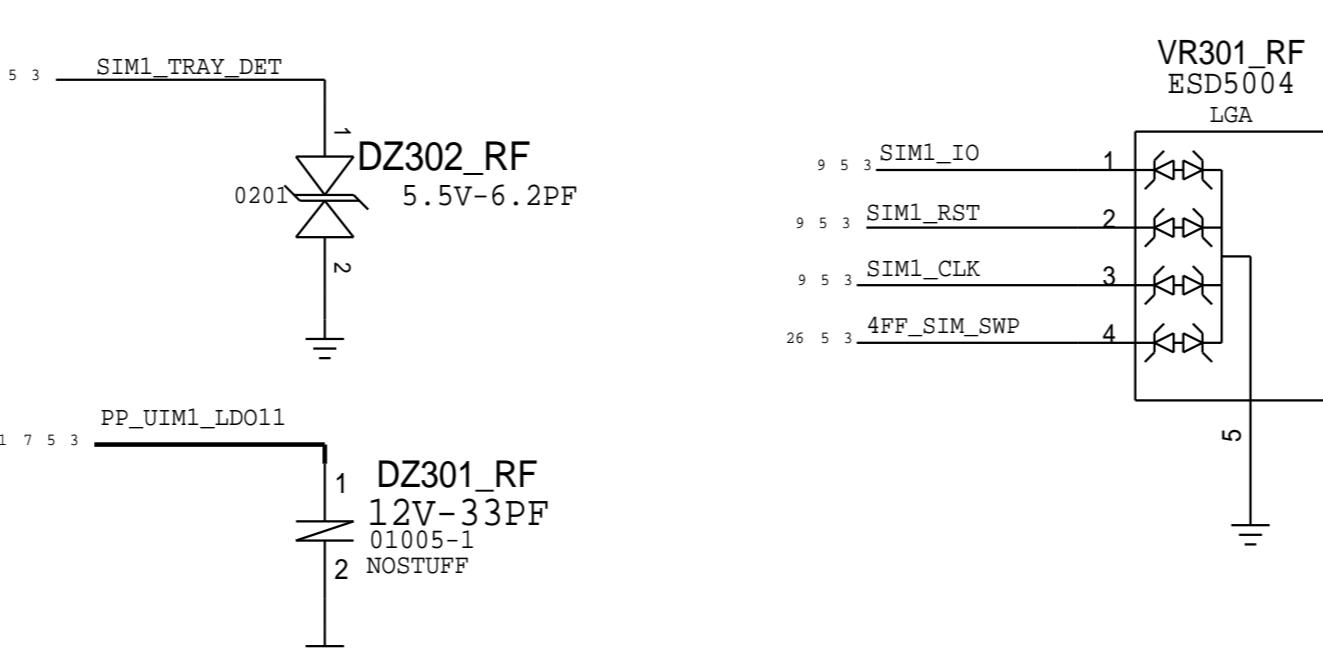
UAT TUNER



SIM CARD CONNECTOR



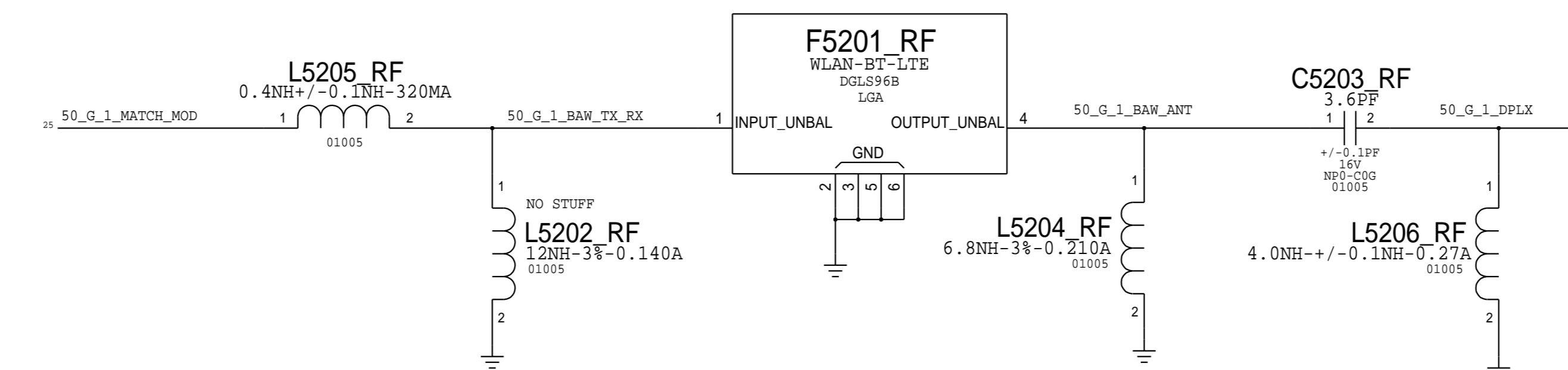
SIM CARD ESD PROTECTION



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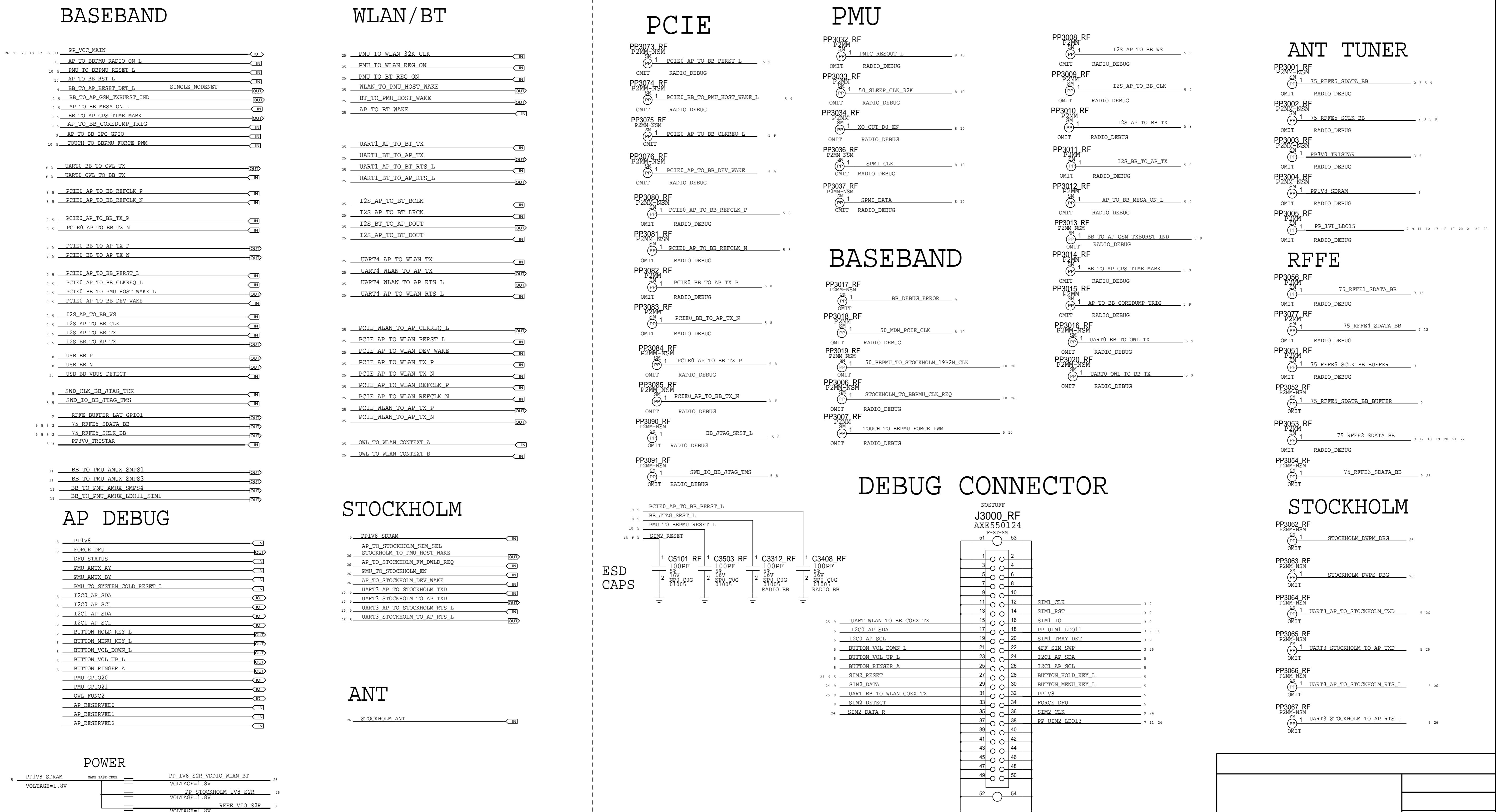
N66-SPECIFIC RADIO PAGE 4

WLAN LAT 2.4GHZ BAW BPF



AP TO BB/WLAN/BT/SH CONNECTIONS

MLB PROBE POINTS

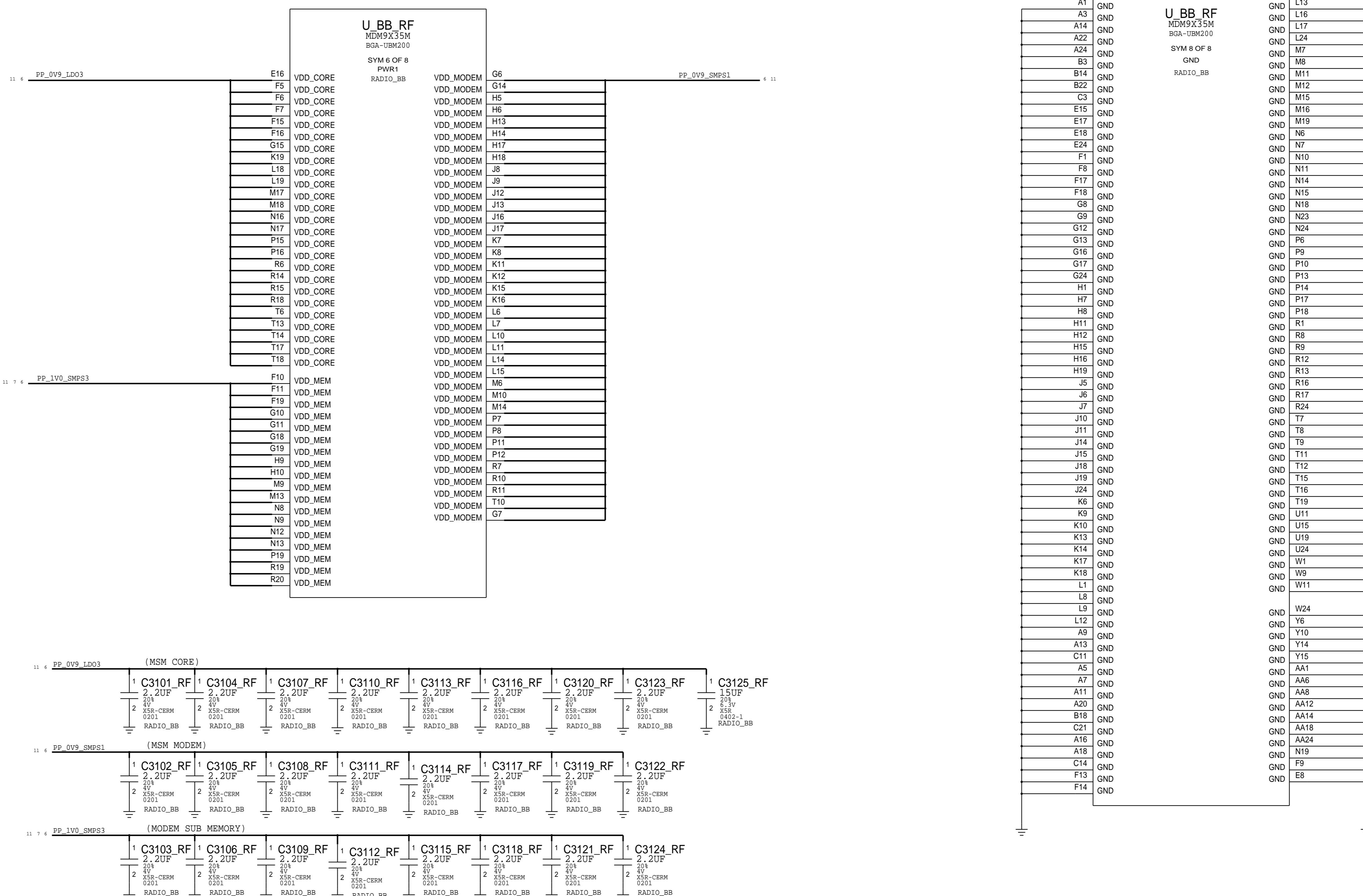


CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

BASEBAND : POWER 1

D

D



C

C

B

B

A

A

BASEBAND: POWER 2

D

D

C

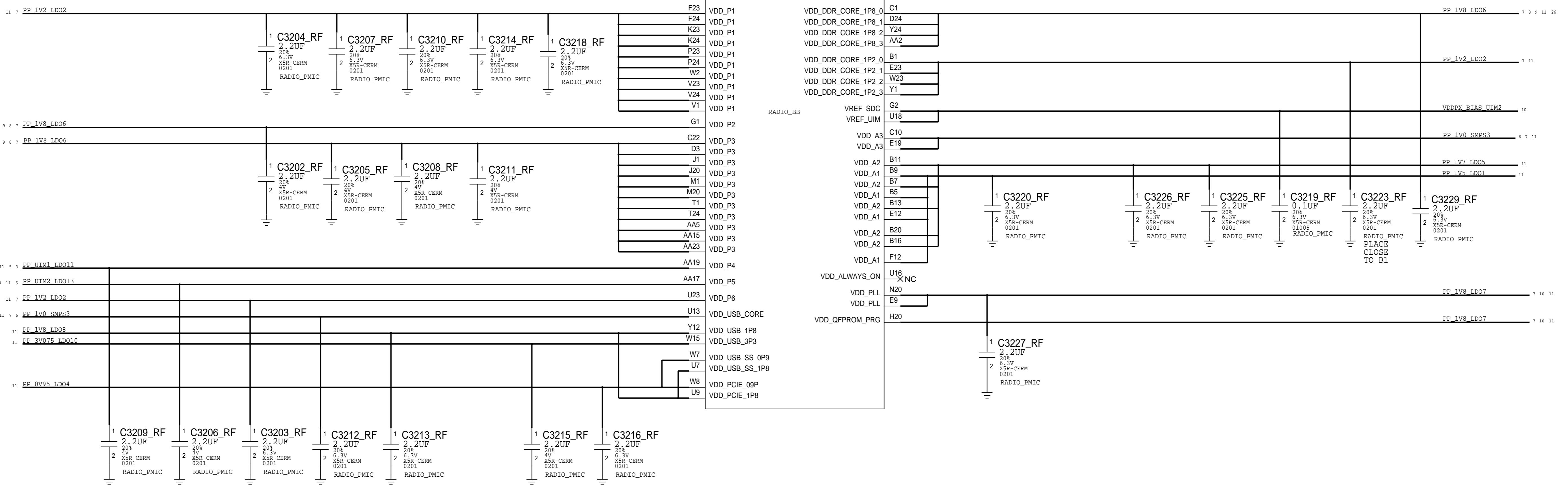
C

B

B

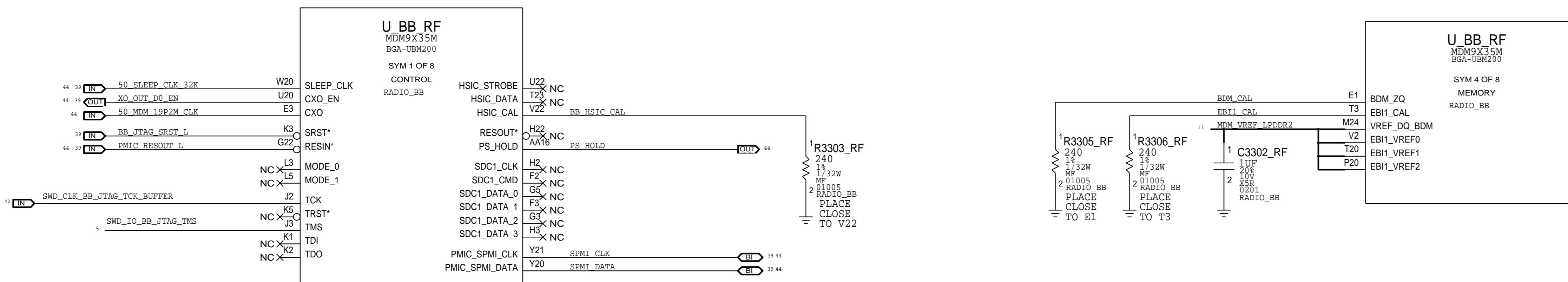
A

A

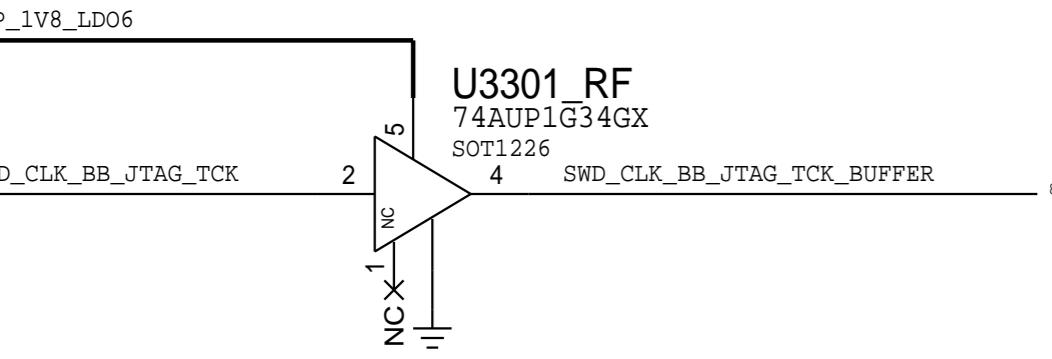


BASEBAND: CONTROL AND INTERFACES

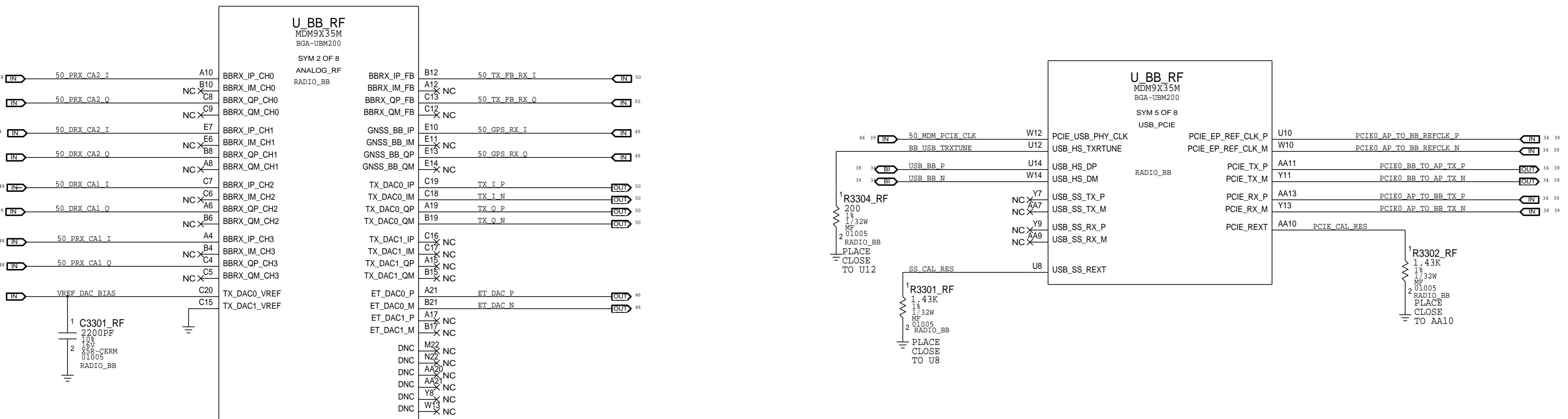
D



C

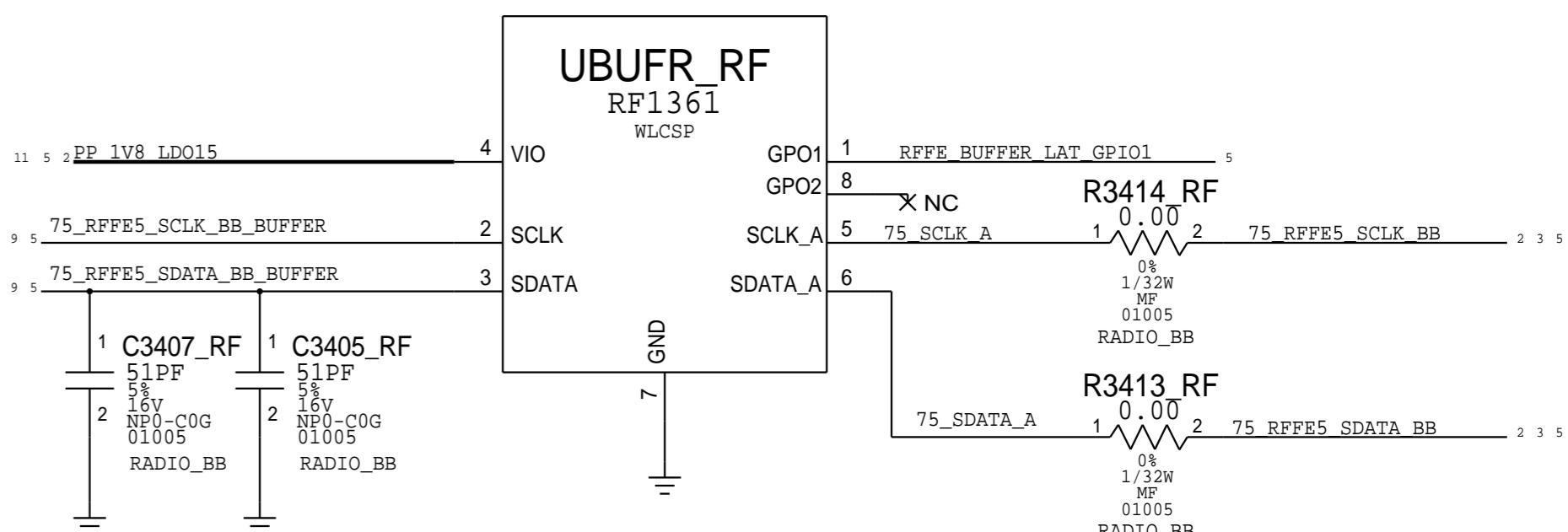
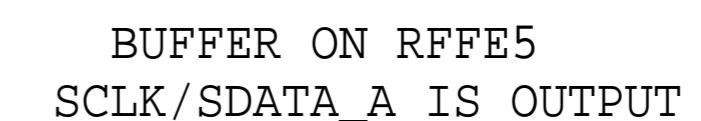
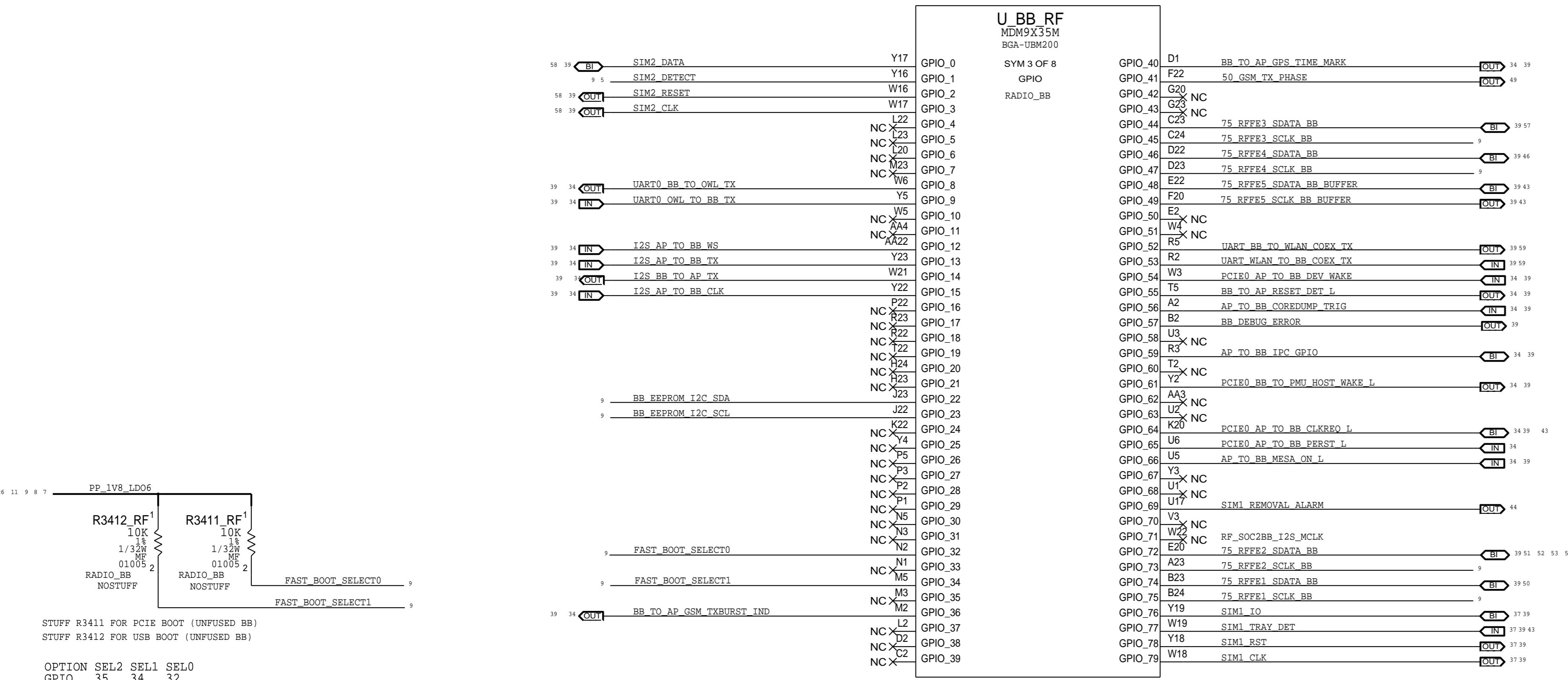


B



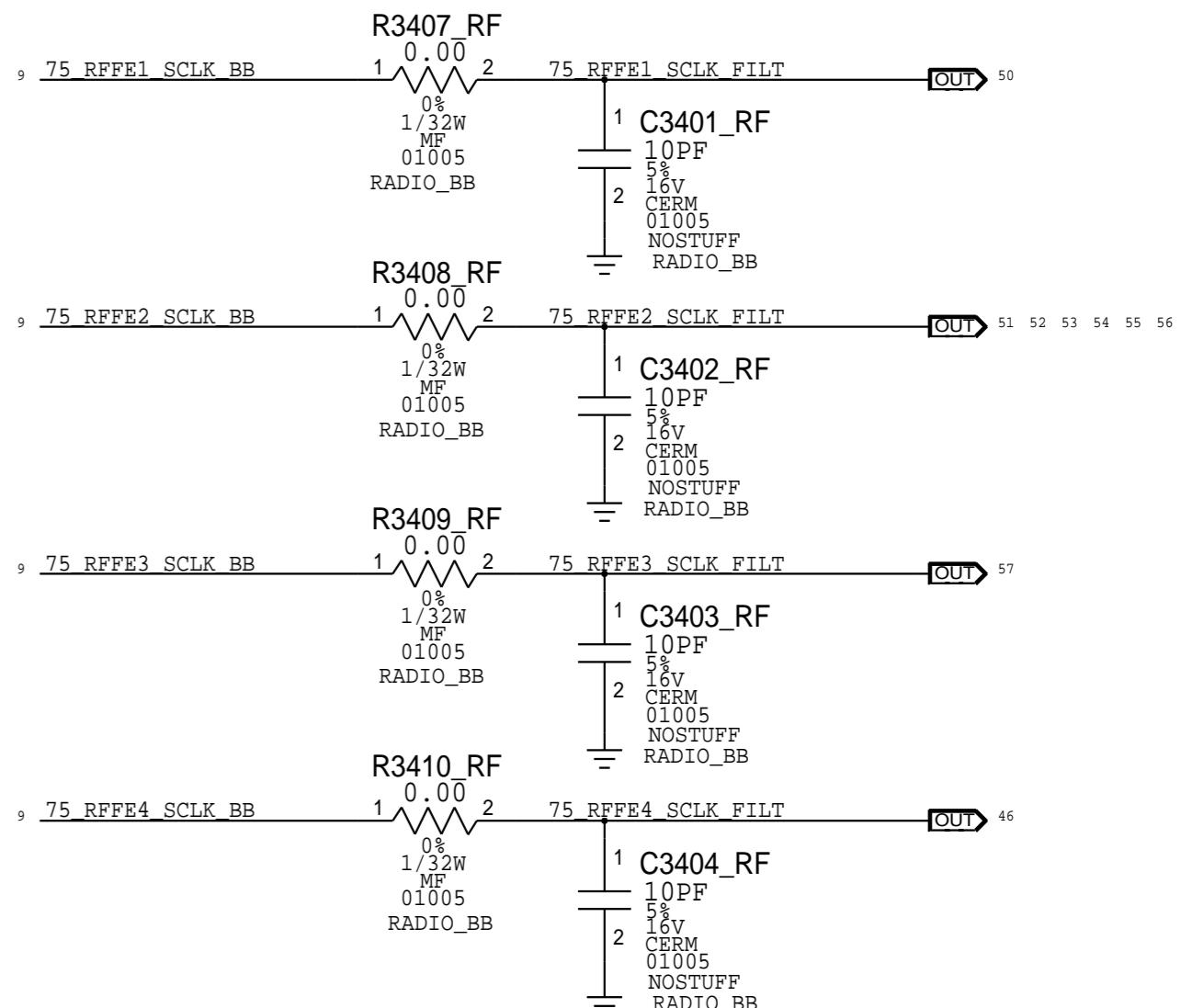
A

BASEBAND : GPIOS



PLACE C3405_RF CLOSE TO BUFFER
PLACE C3407_RF CLOSE TO MDM

RFFE CLOCK FILTERS



RFFE USAGE TABLE

RFFE1 WTR

RFFE2 LB/MB/HB PAD, 2G PA, LB/MB/HB ASM

RFFE3 DIV ASM

RFFE4_QPOE

RFFF5 DTV LNA, ANT TUNERS

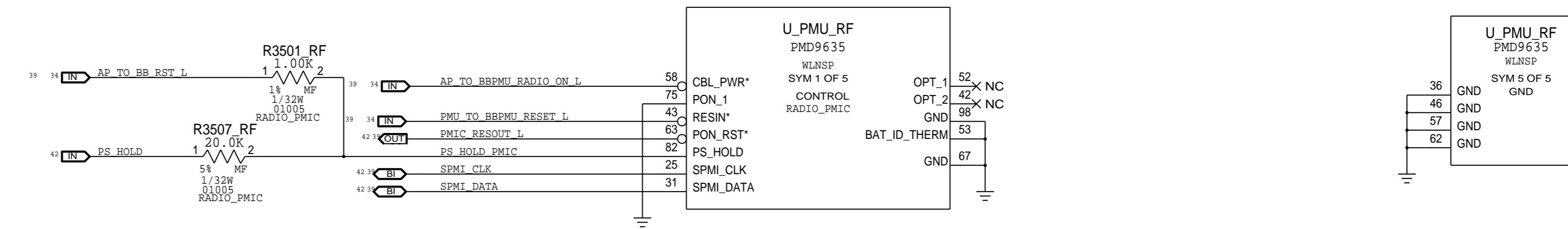
ACTE BUILT-HDS TO BB PATT

CONFIDENTIAL AND PROPRIETARY APPLE SYSTEM DESIGN. FOR REFERENCE PURPOSE ONLY - NOT A CHANGE REQUEST

PMU: CONTROL AND CLOCKS

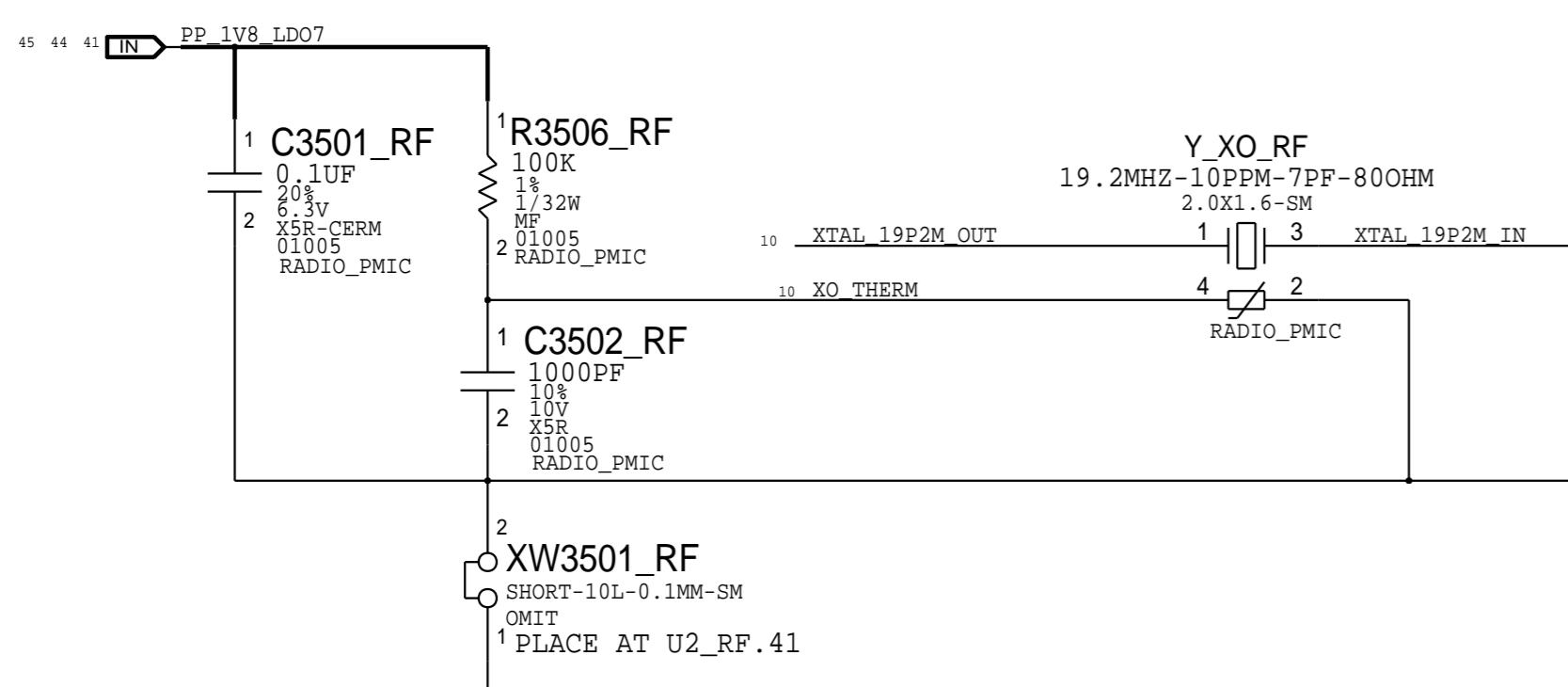
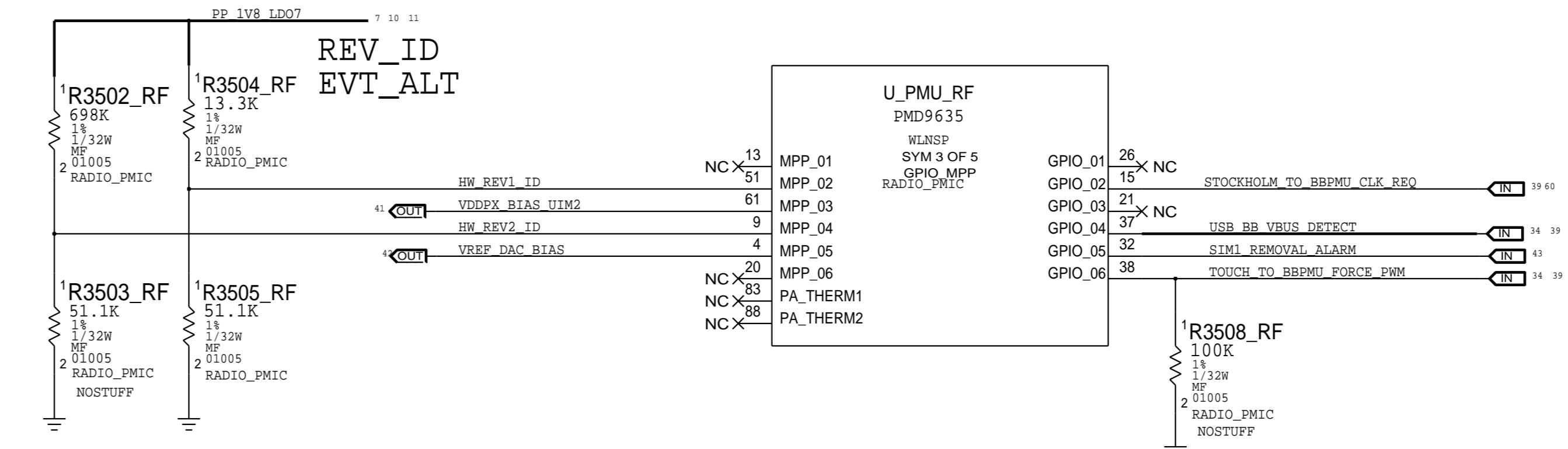
HW_REV2_ID	R3502	R3503	CONFIG
1.80V	698K	-	MLB
0.12V	698K	51.1K	SELF GEN

RESET AND CONTROL: PMU

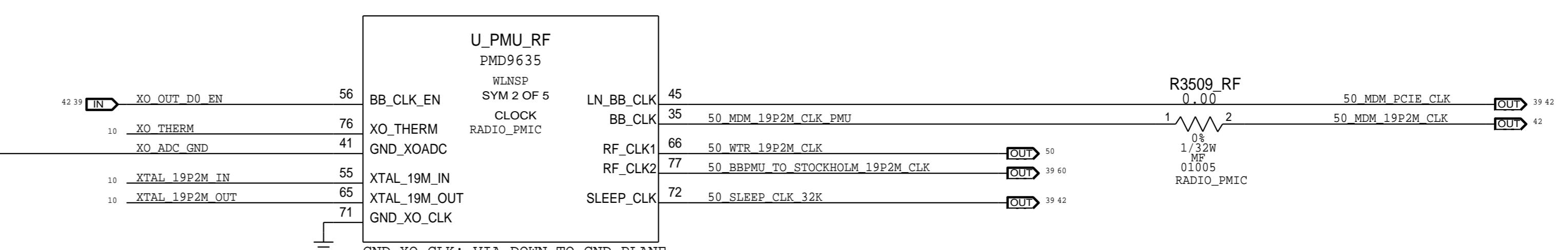


HW_REV_ID	R3504	R3505	REVISION
0.10V	887K	51.1K	DEV1
0.30V	255K	51.1K	DEV2
0.50V	124K	51.1K	DEV3
0.70V	82.5K	51.1K	DEV4/PROTOMLB1
0.90V	51.1K	51.1K	PROTOMLB2
1.10V	31.6K	51.1K	DEV5/PROTO1
1.20V	50K	100K	PROTO2
1.31V	39K	105K	EVT
1.43V	13.3K	51.1K	EVT_ALT
1.55V	8.25K	51.1K	CARRIER BUILD
1.63V	5.23K	51.1K	DVT
1.80V	10K	-	PVT

MPPS AND GPIOS: PMU

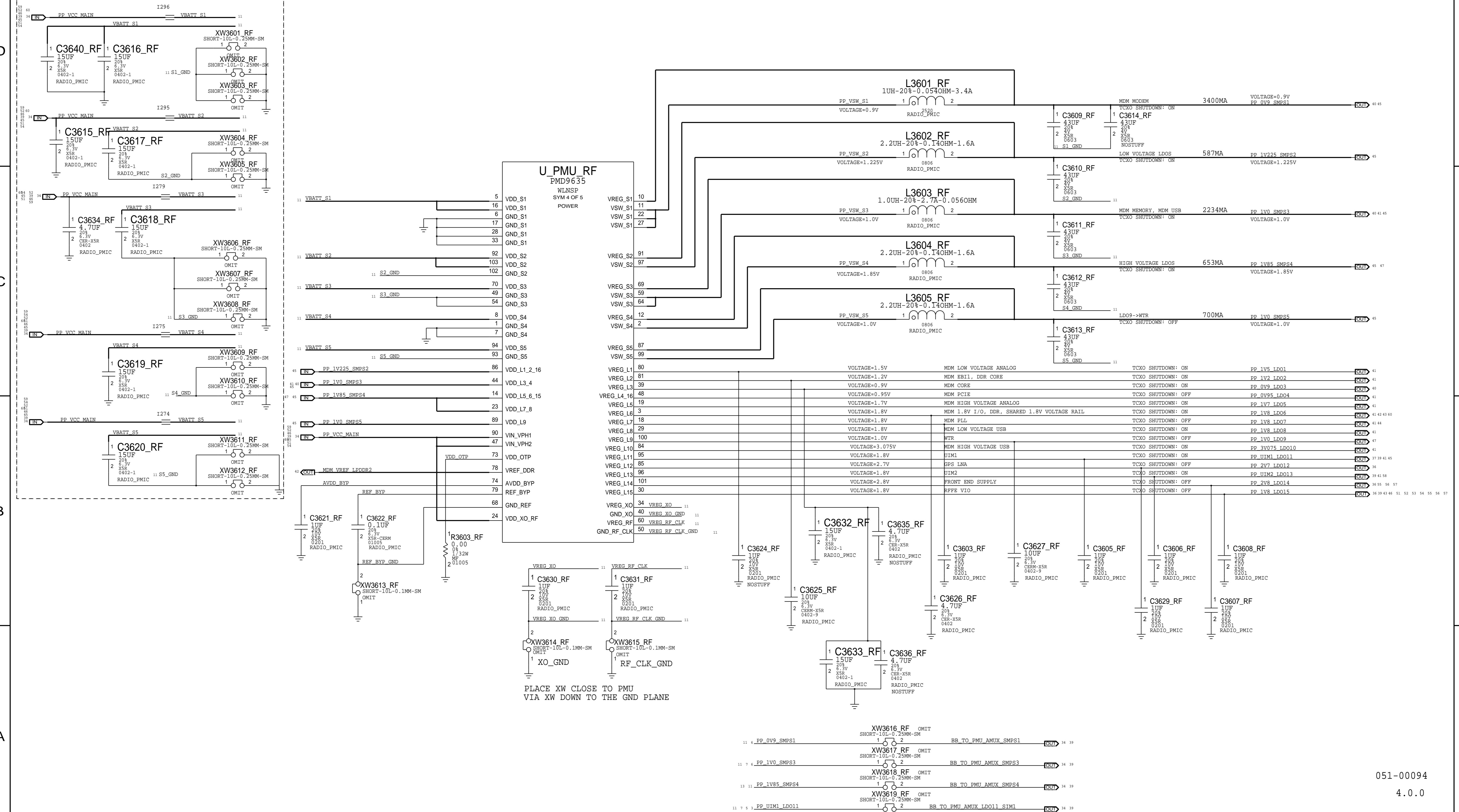


XTAL AND CLOCK: PMU

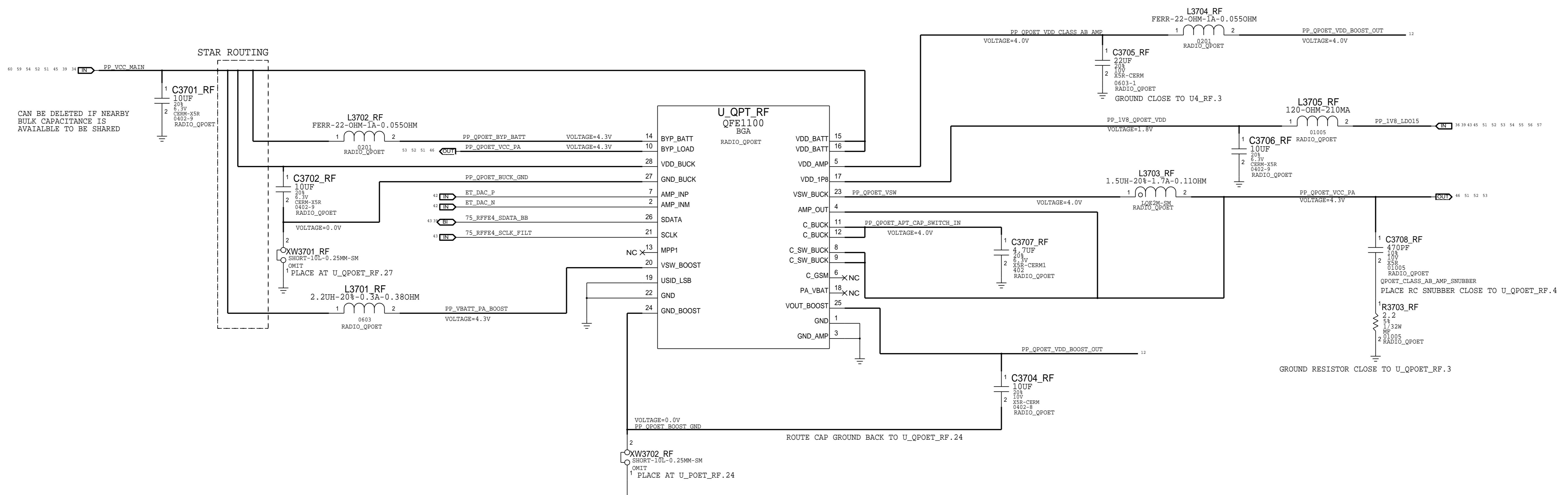


PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS



PMU: ET MODULATOR



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TRANSCEIVER: POWER

D

D

C

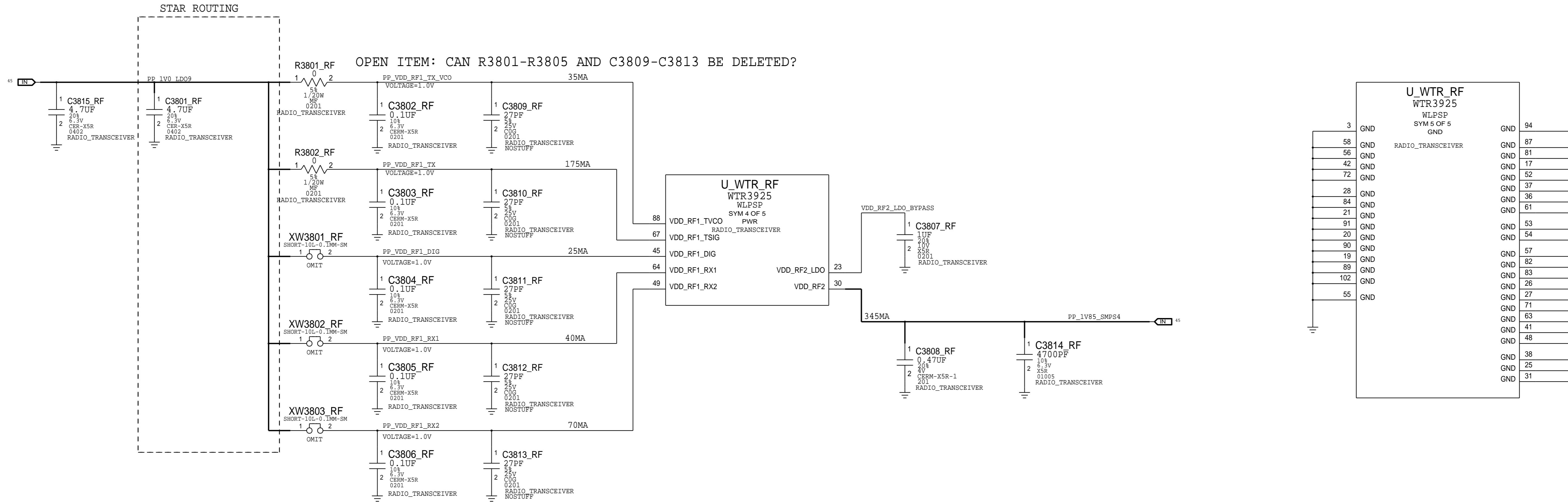
C

B

B

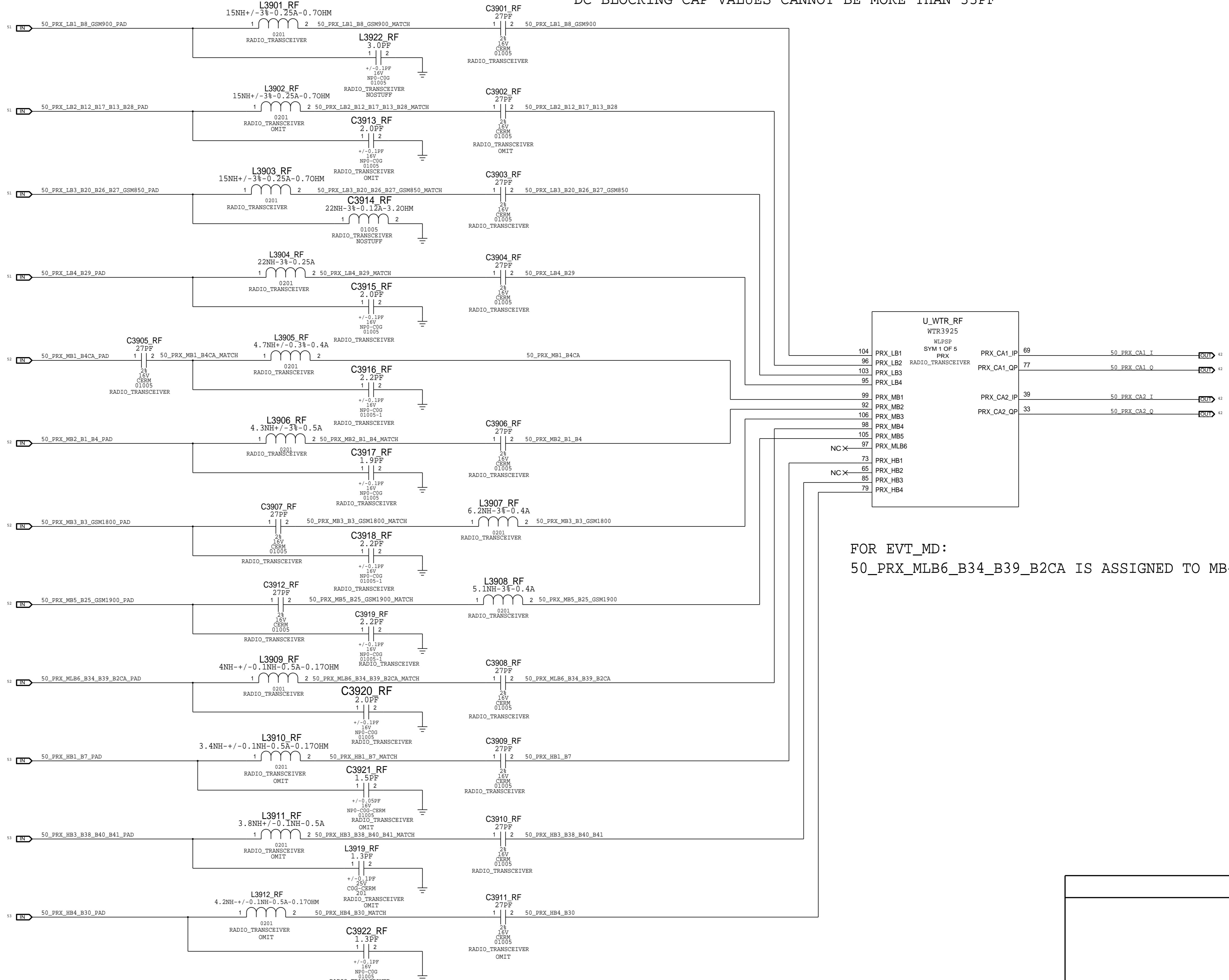
A

A



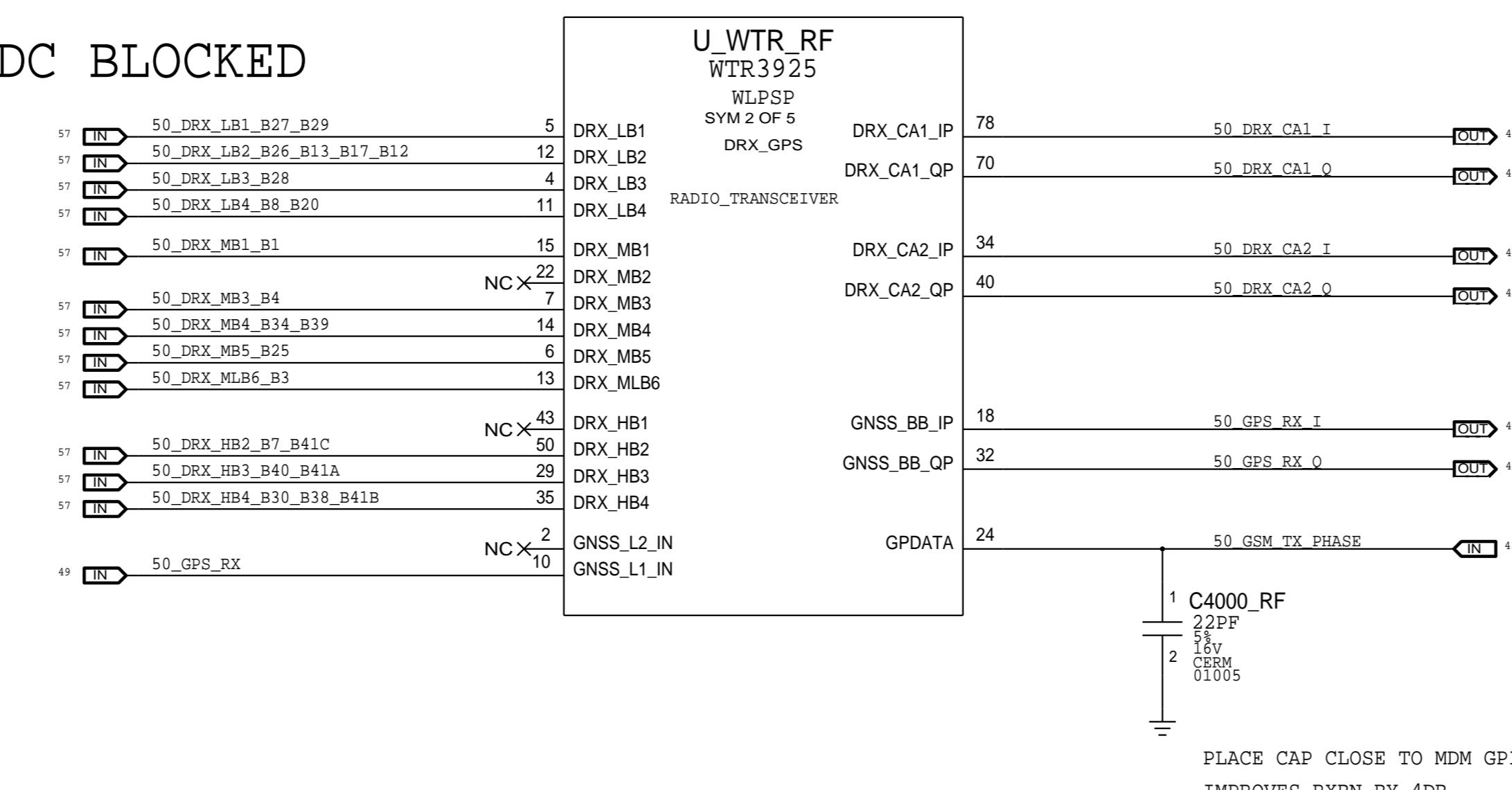
TRANSCEIVER: PRX PORTS

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF



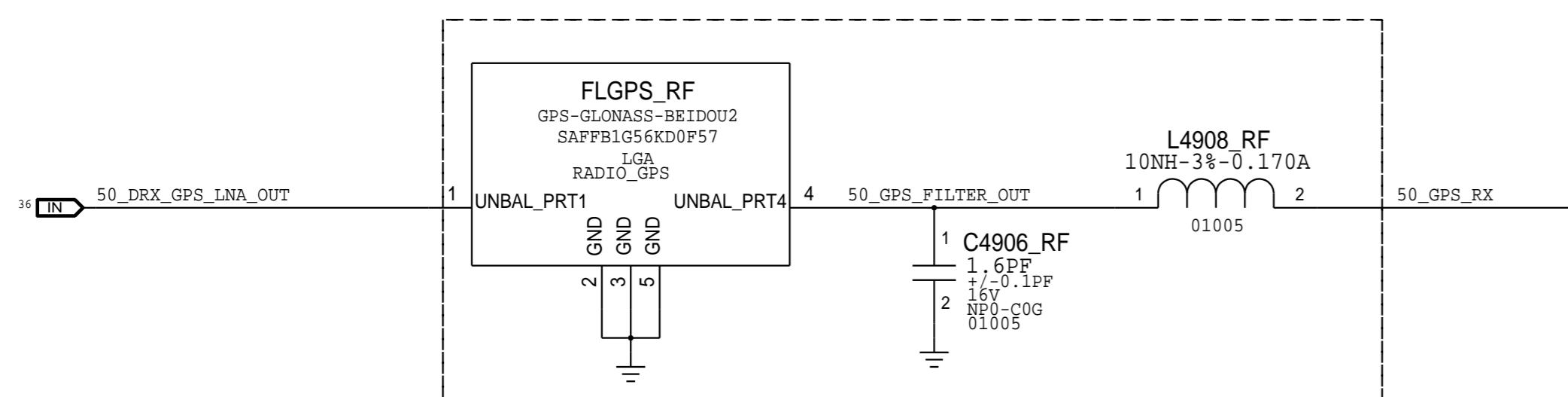
TRANSCEIVER: DRX/GPS PORTS

DRX MODULE PORTS ARE DC BLOCKED



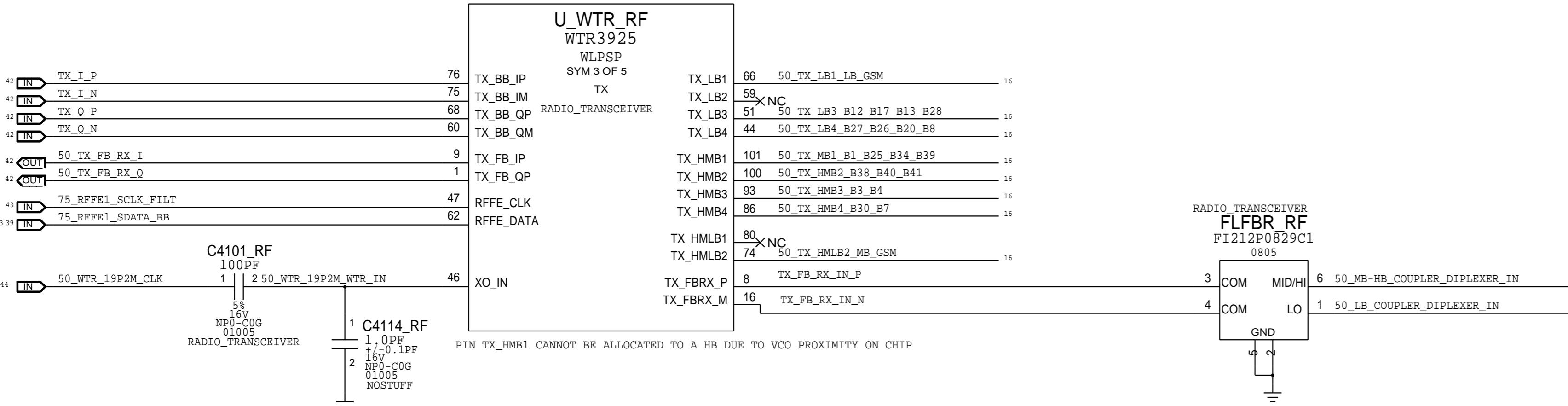
GPS FILTER

PLACE NEAR U_WTR



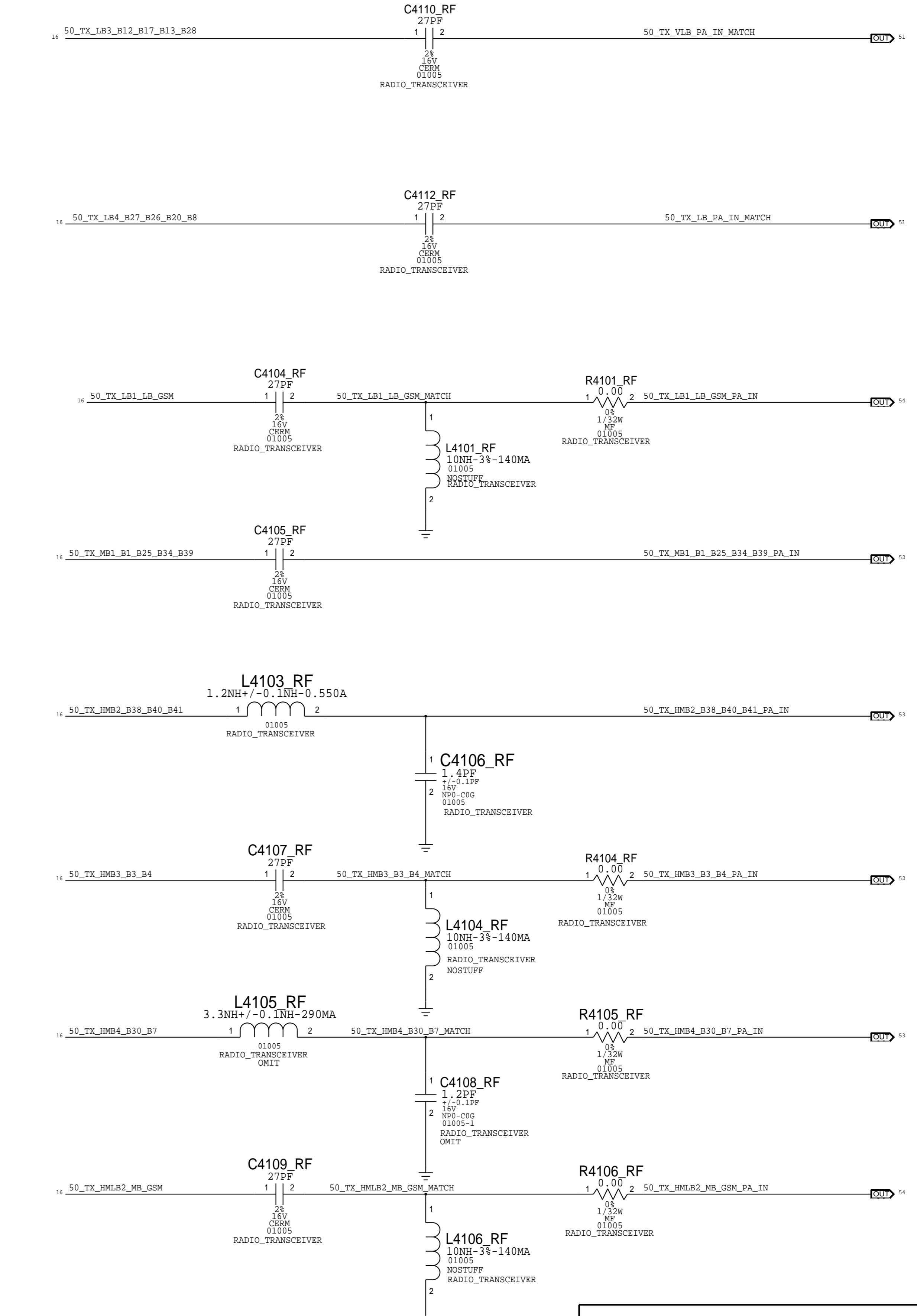
TRANSCEIVER: TX PORTS

D



B12/13 TX INTERSTAGE FILTER REMOVED

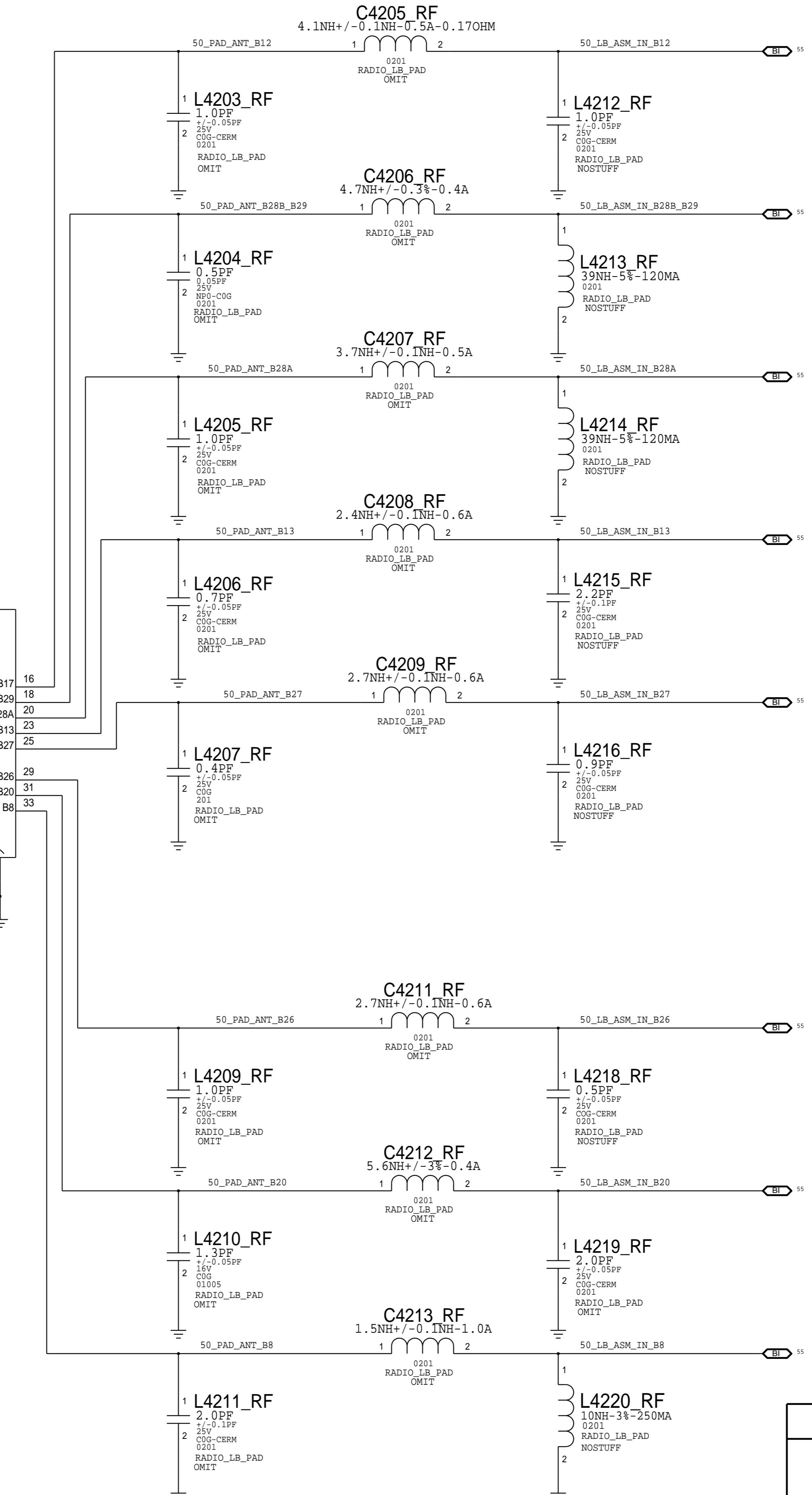
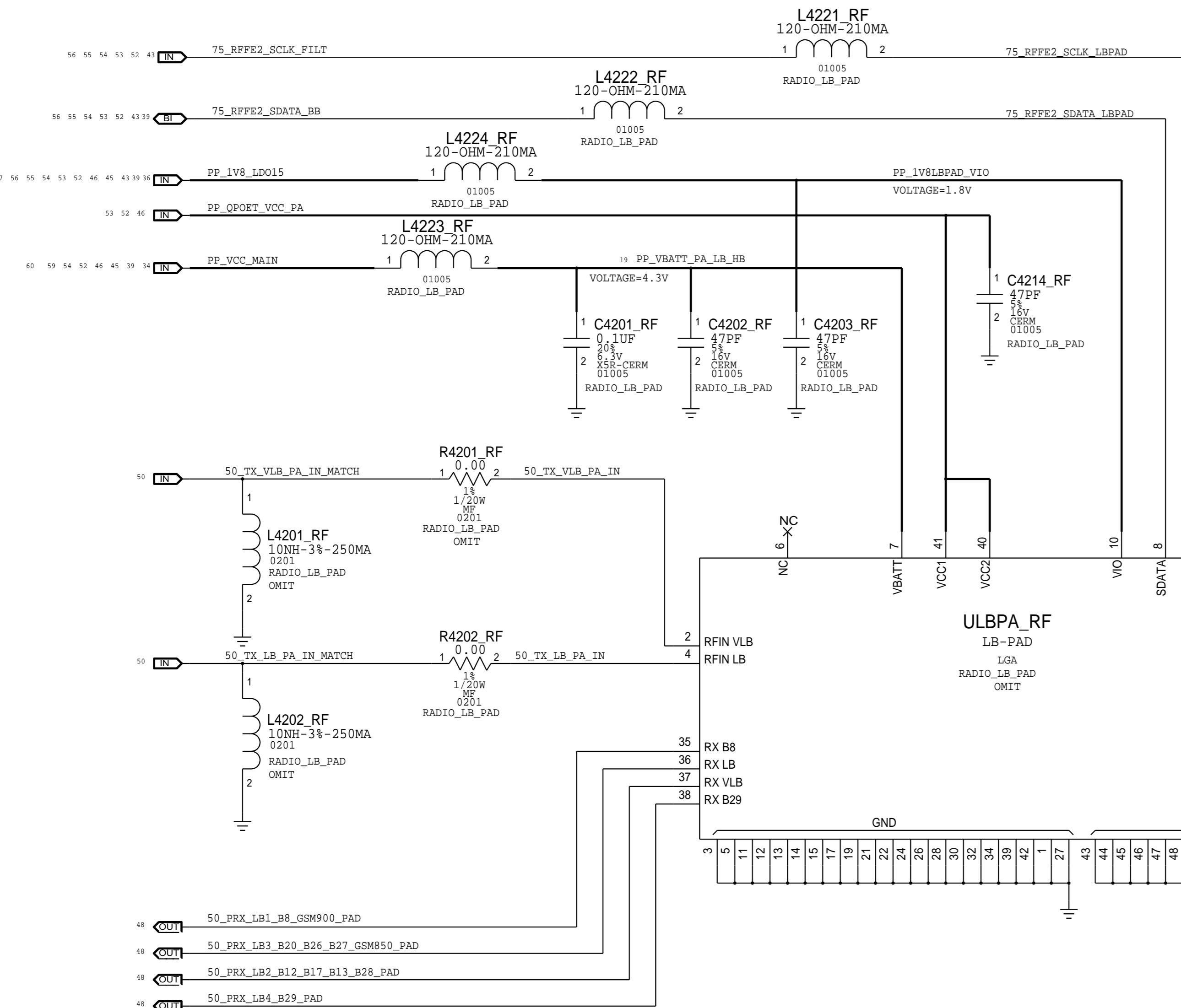
B



A

LOW BAND PA+DUPLEXERS

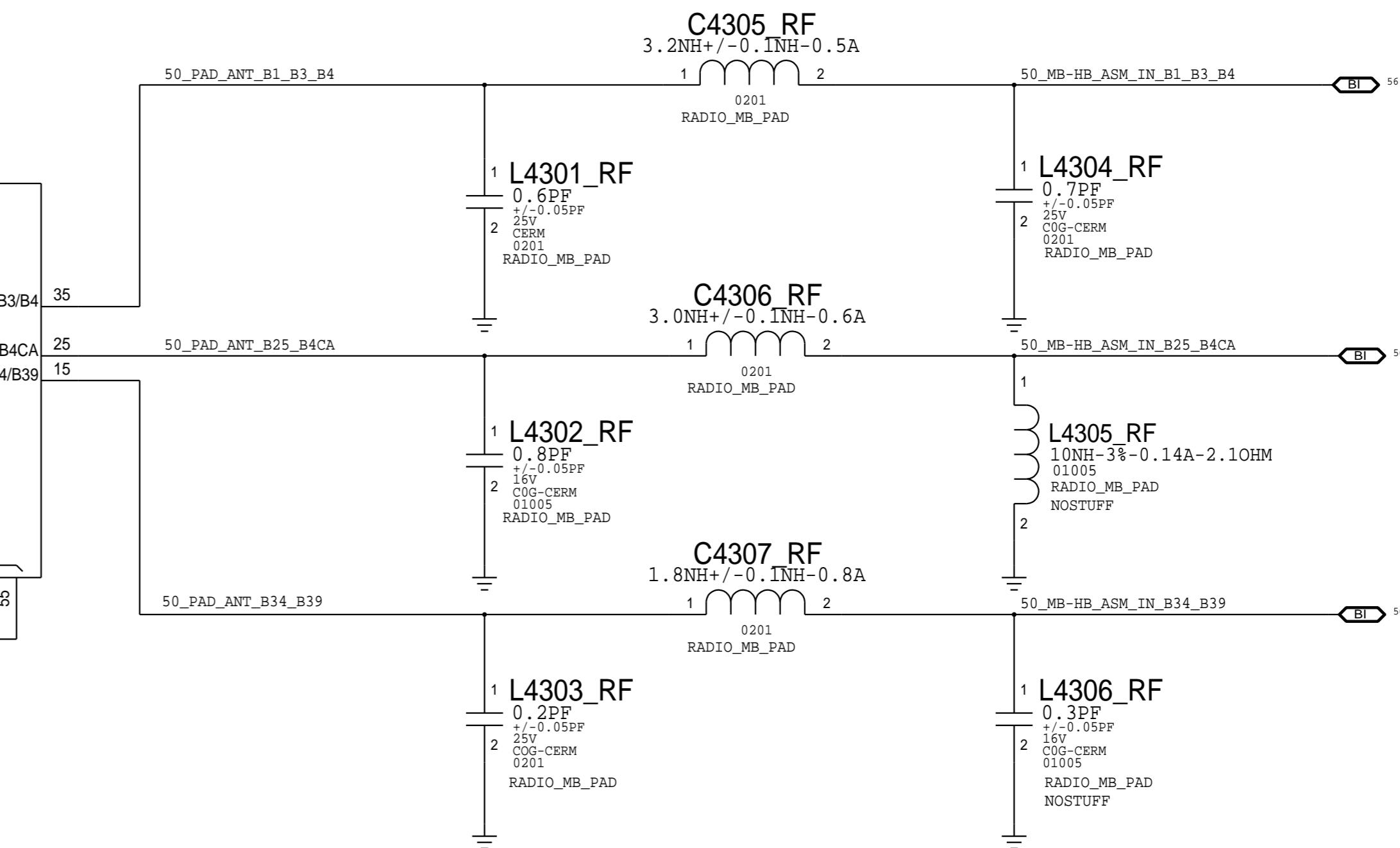
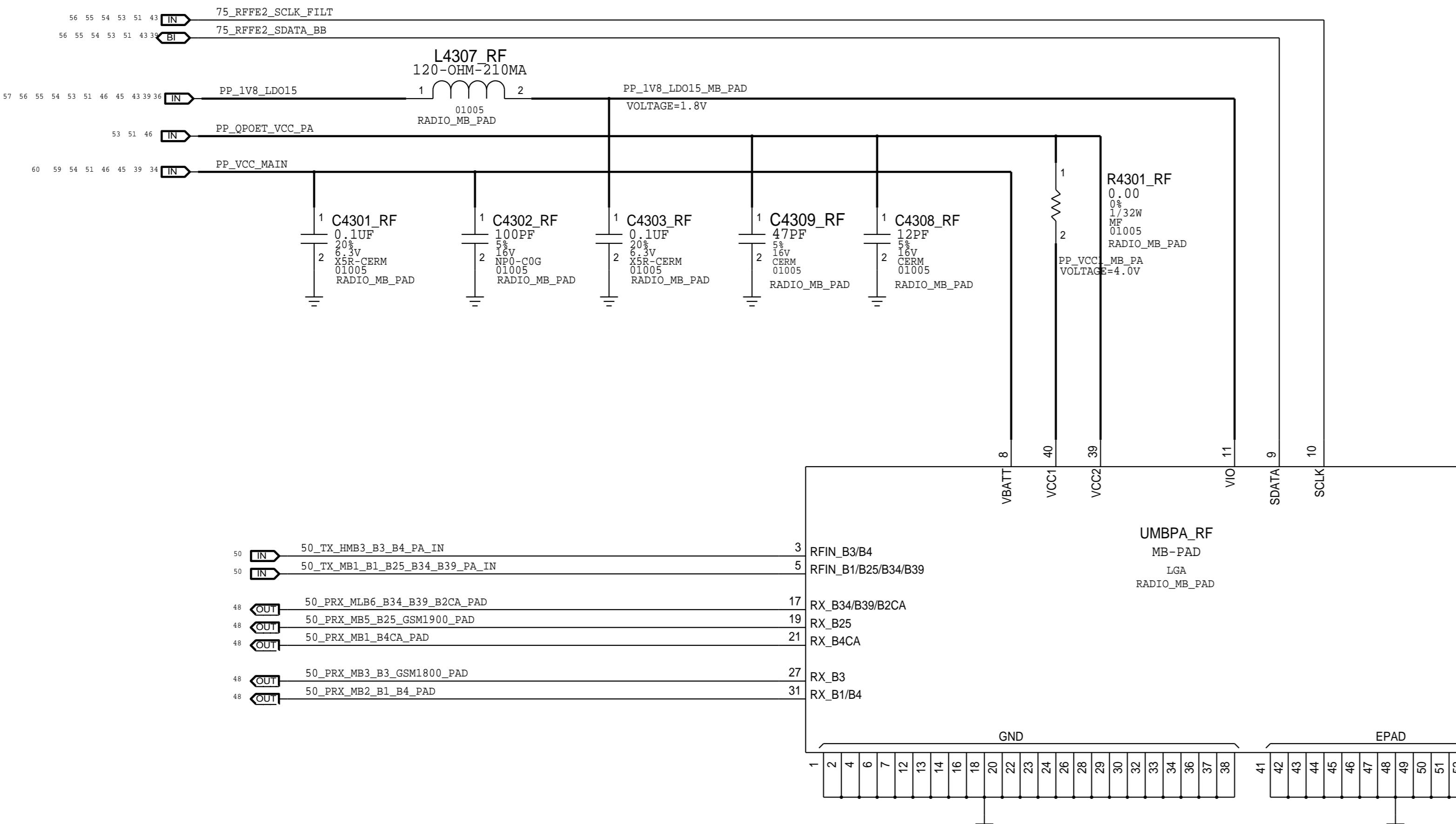
D



MID BAND PA+DUPLEXERS

D

D



B

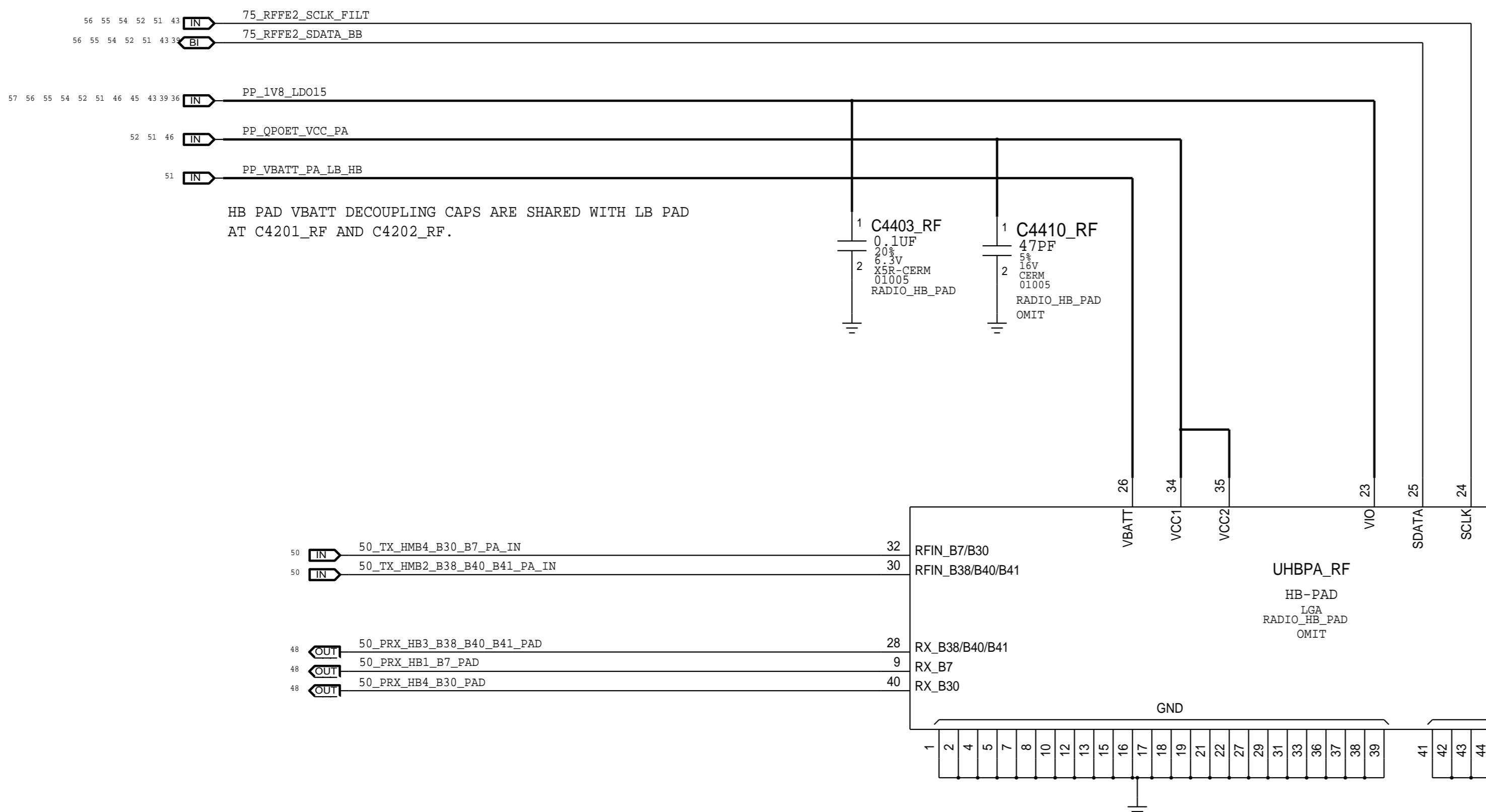
B

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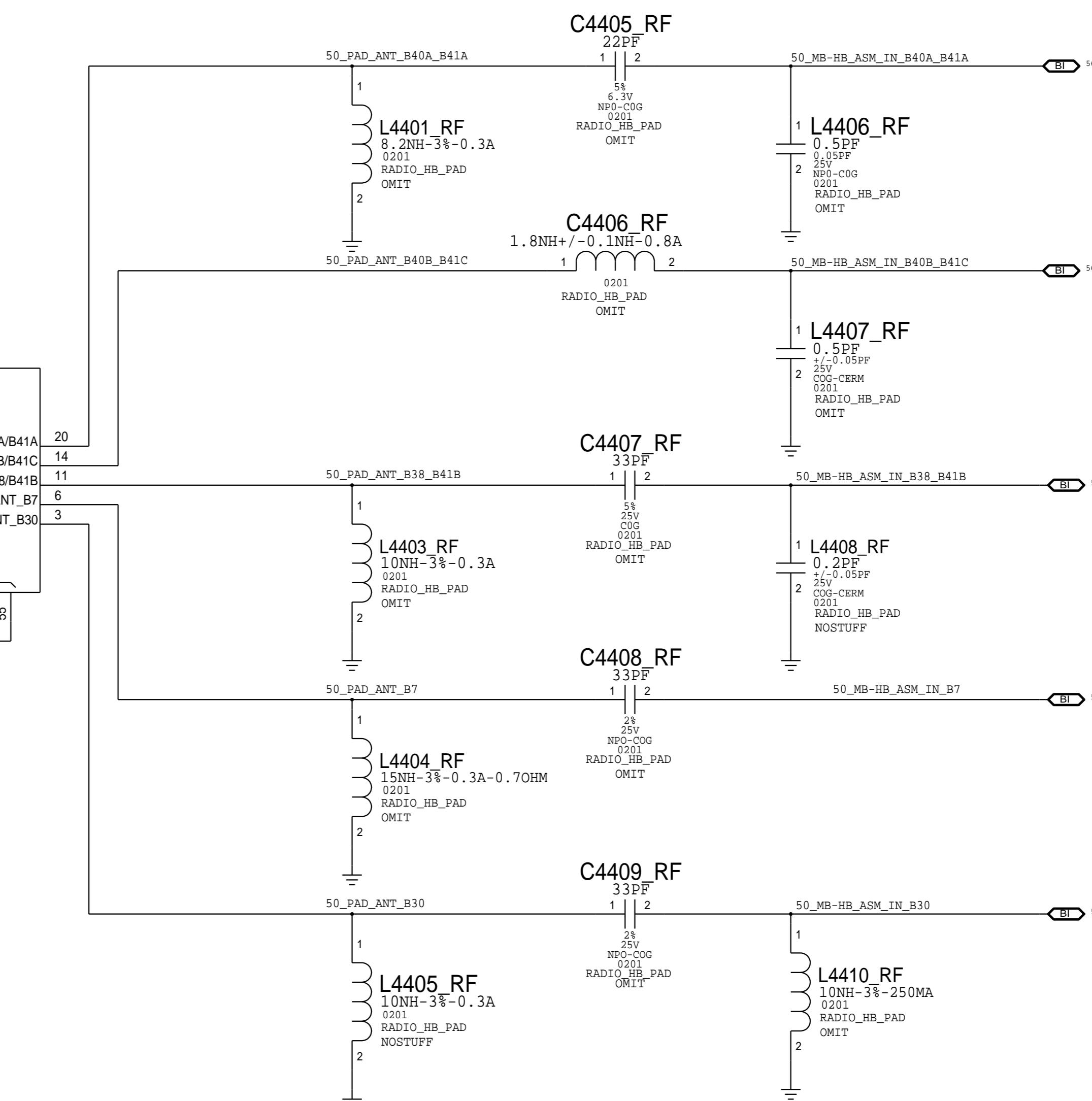
A

HIGH BAND PA+DUPLEXERS

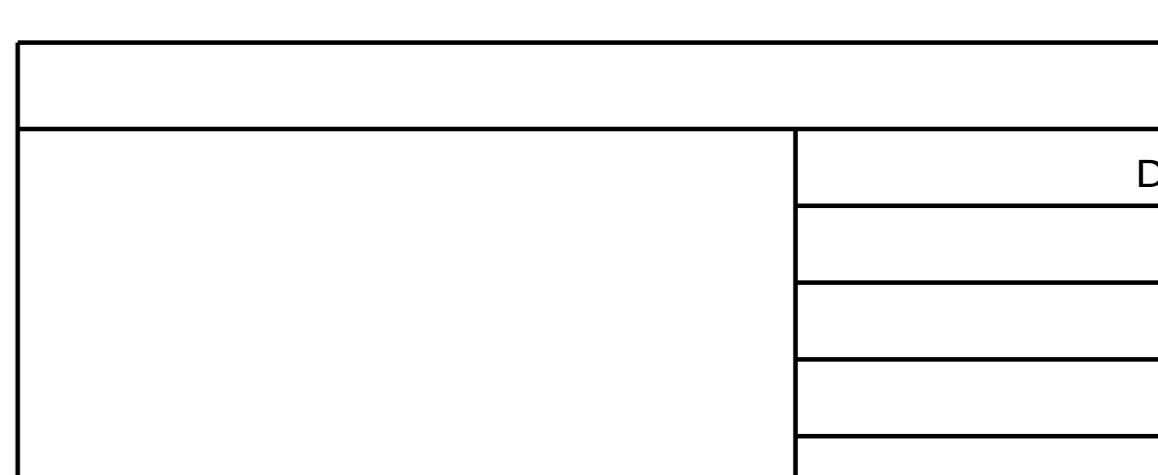
D



C



B



A

D

C

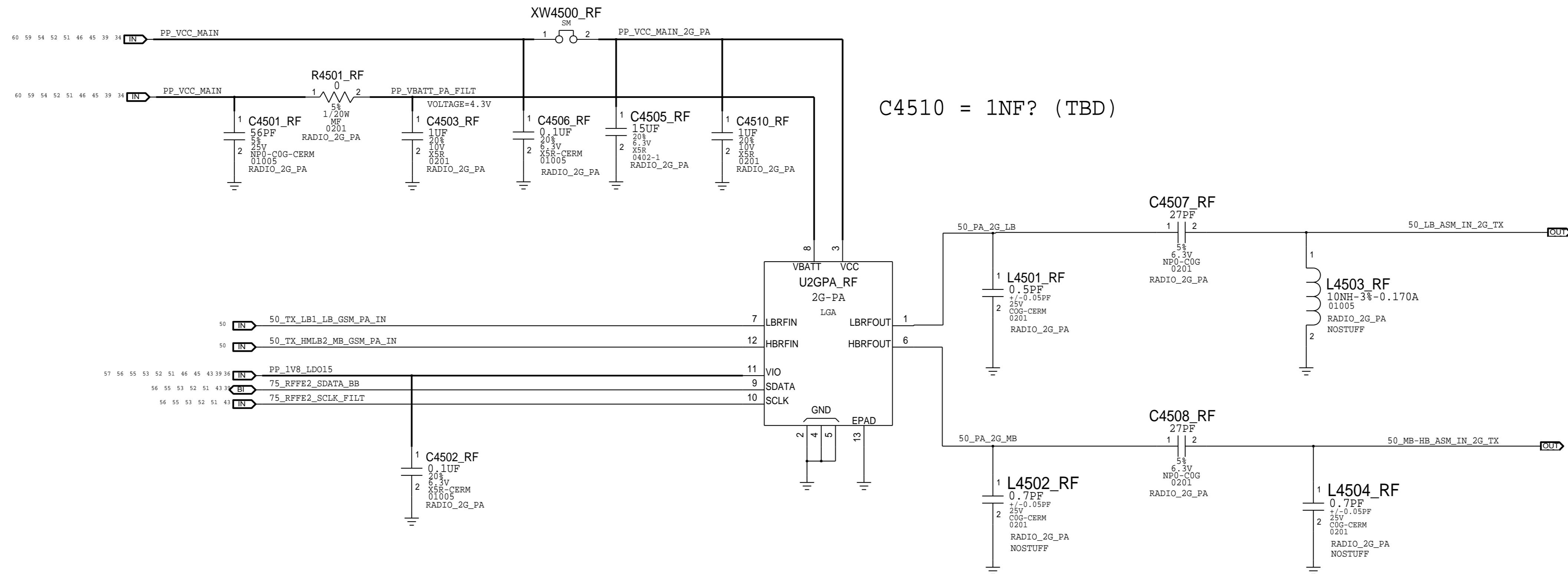
B

A

2G PA

D

D



C4510 = 1NF? (TBD)

LOW BAND ANTENNA SWITCH MODULE

D

D

C

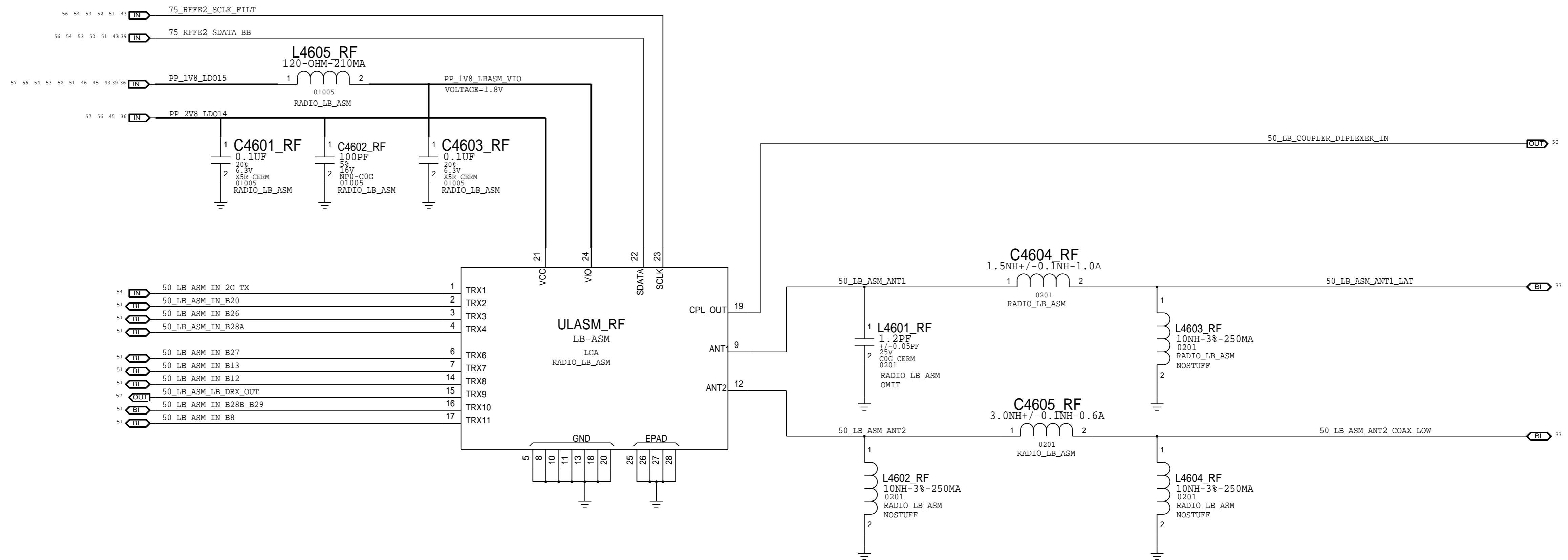
C

B

B

A

A



MID-HIGH BAND ANTENNA SWITCH MODULE

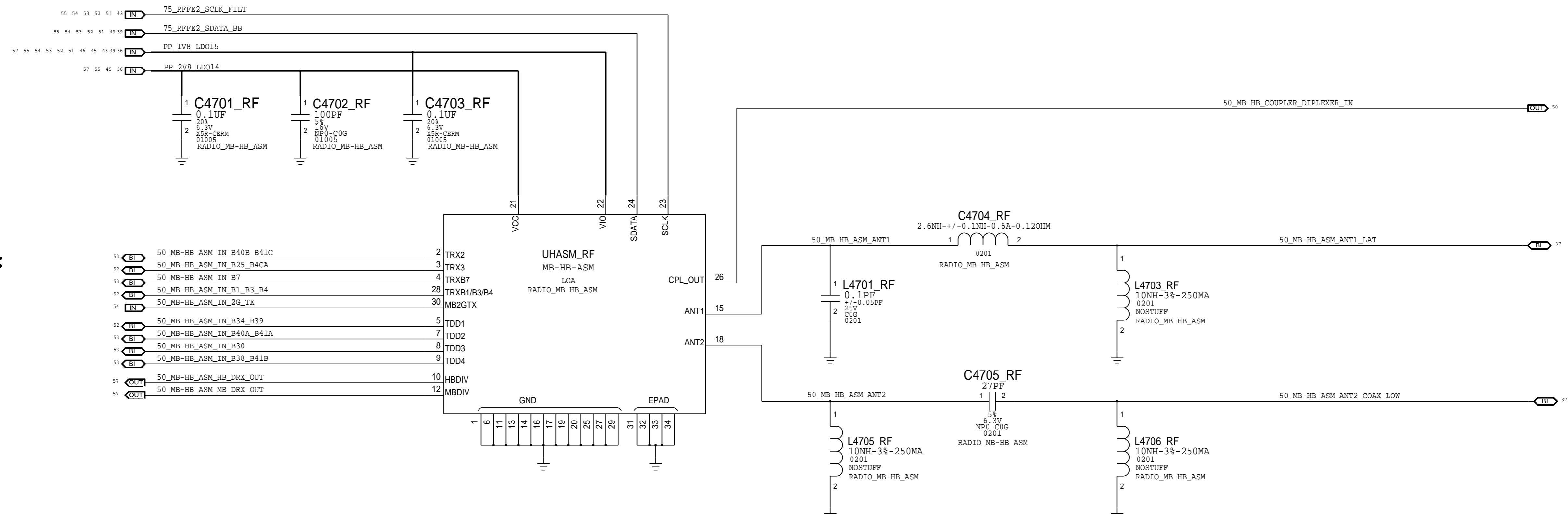
D

D

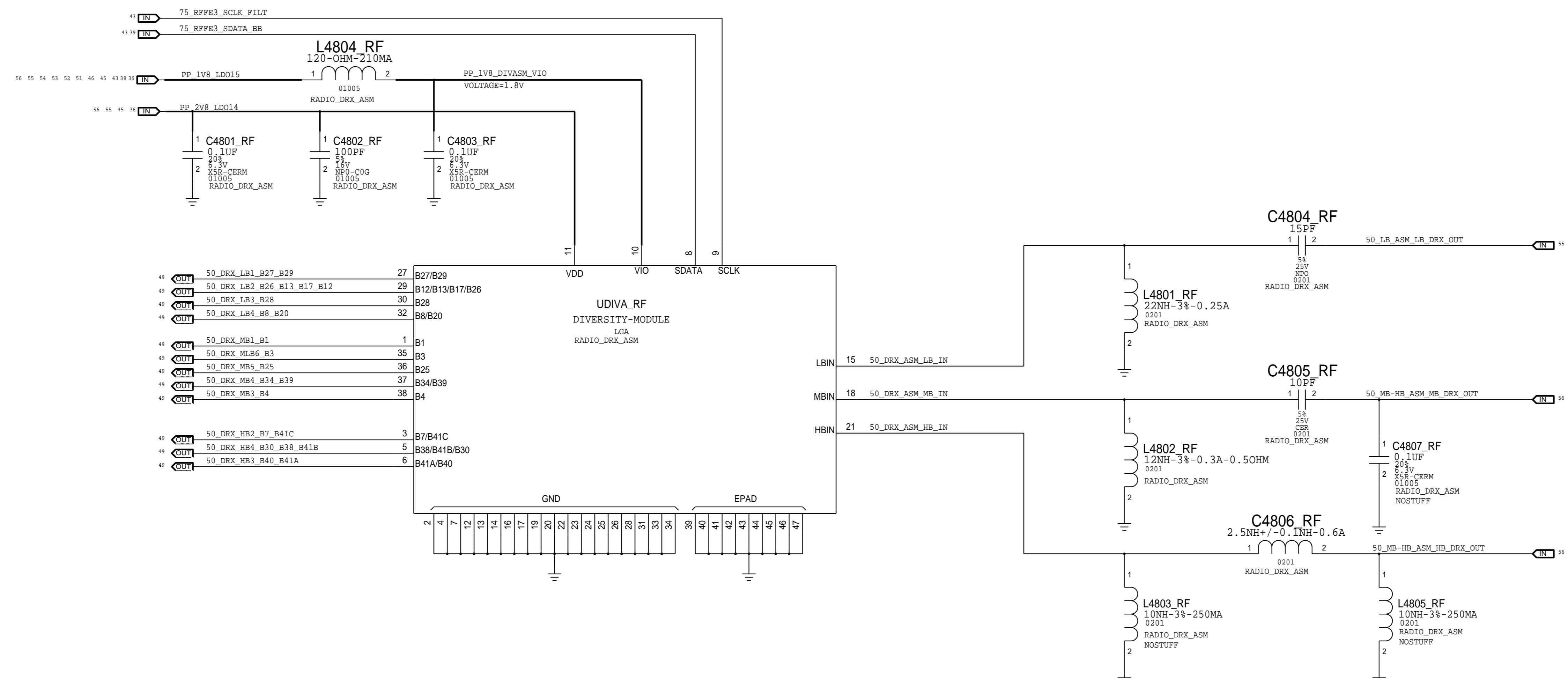
C

C

EVT ASM ASSIGNMENT:
 B40B/B41C - TRX2
 B30 - TDD3



DIVERSITY MODULE



SIM

D

D

C

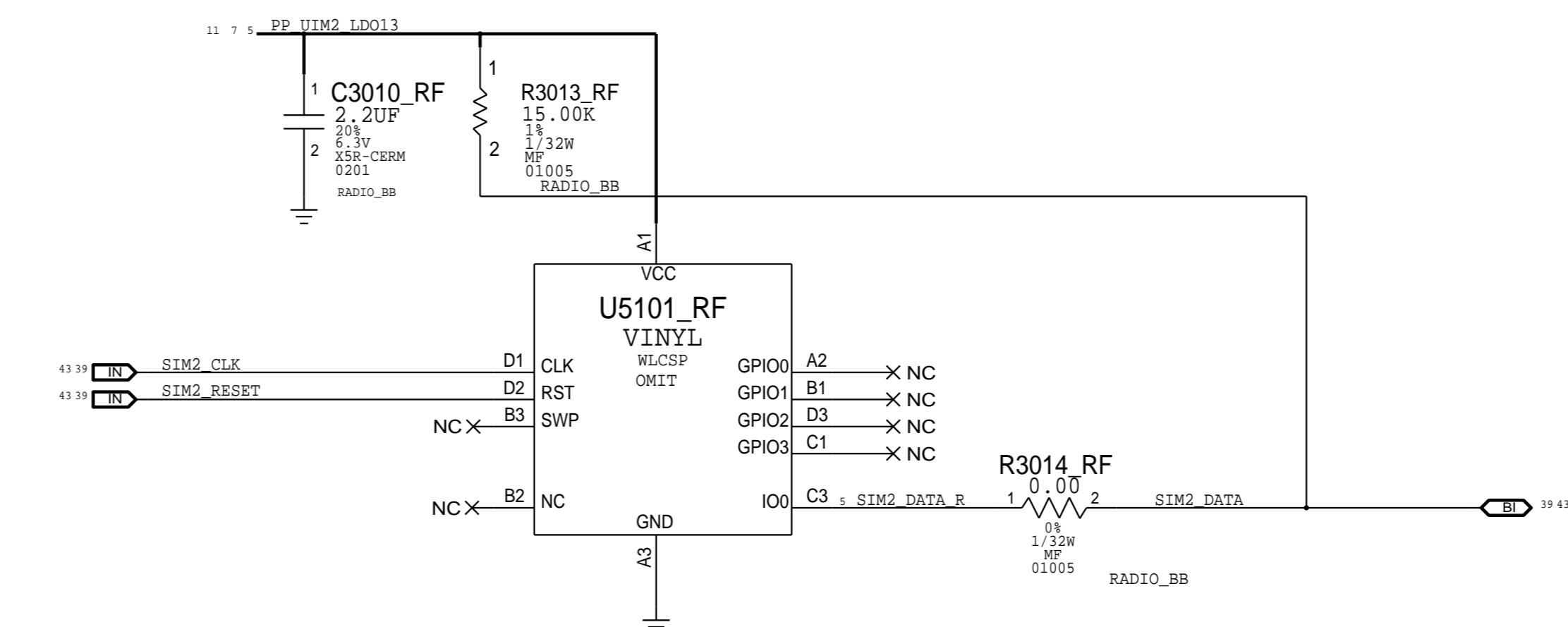
C

B

B

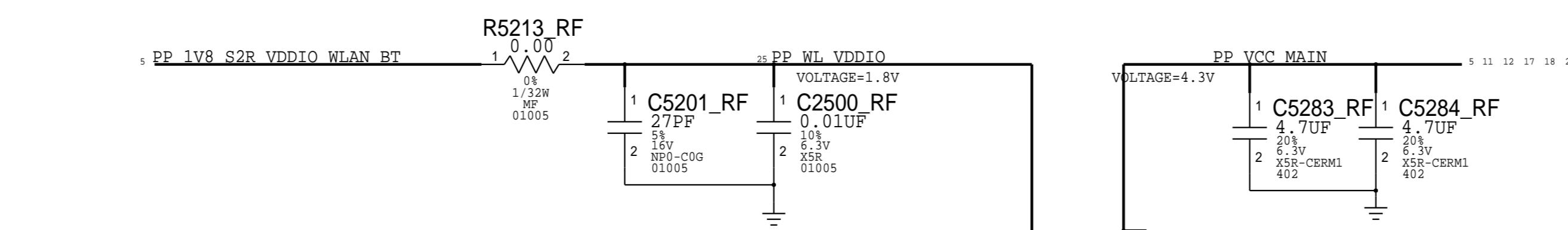
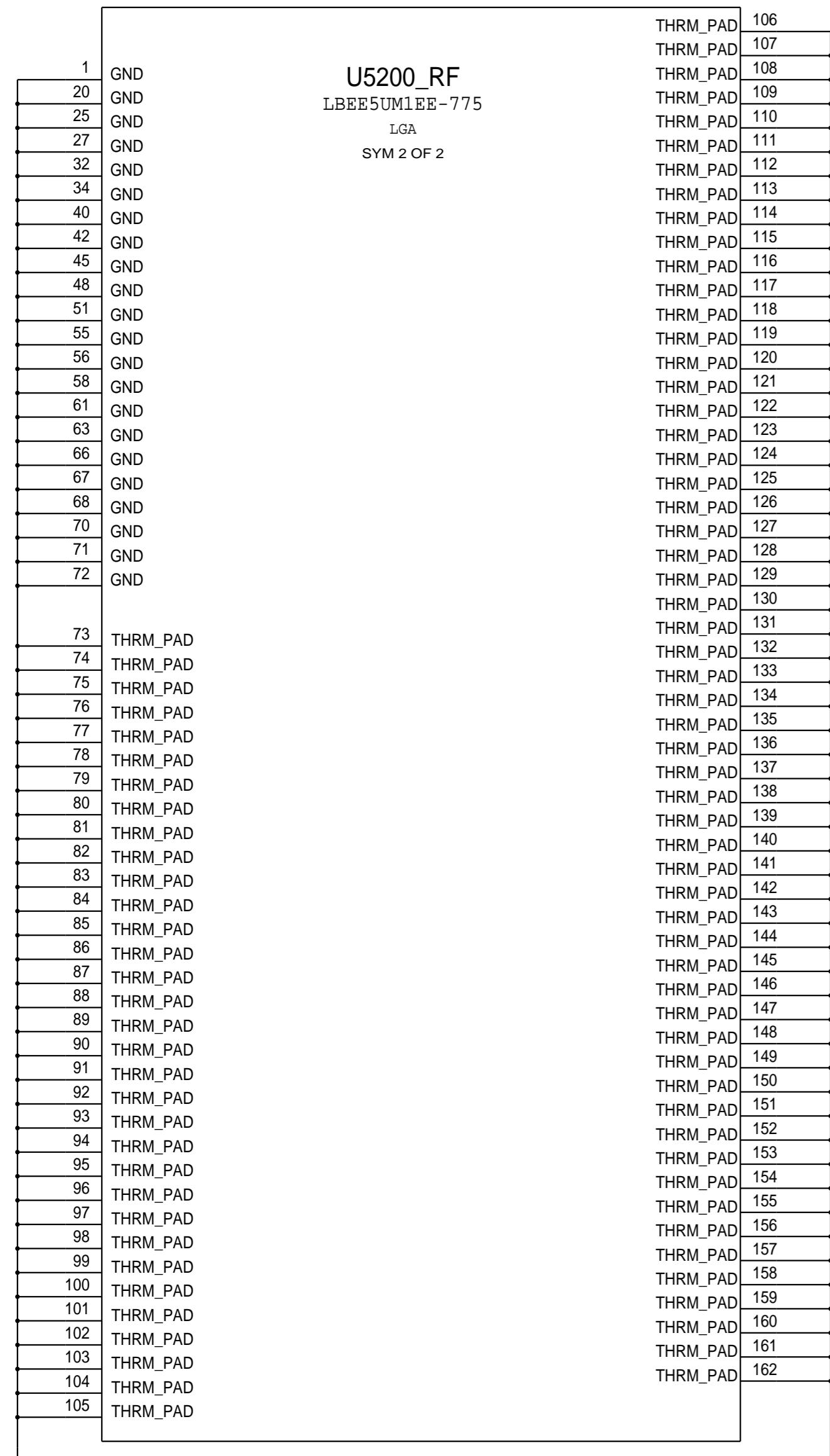
A

A

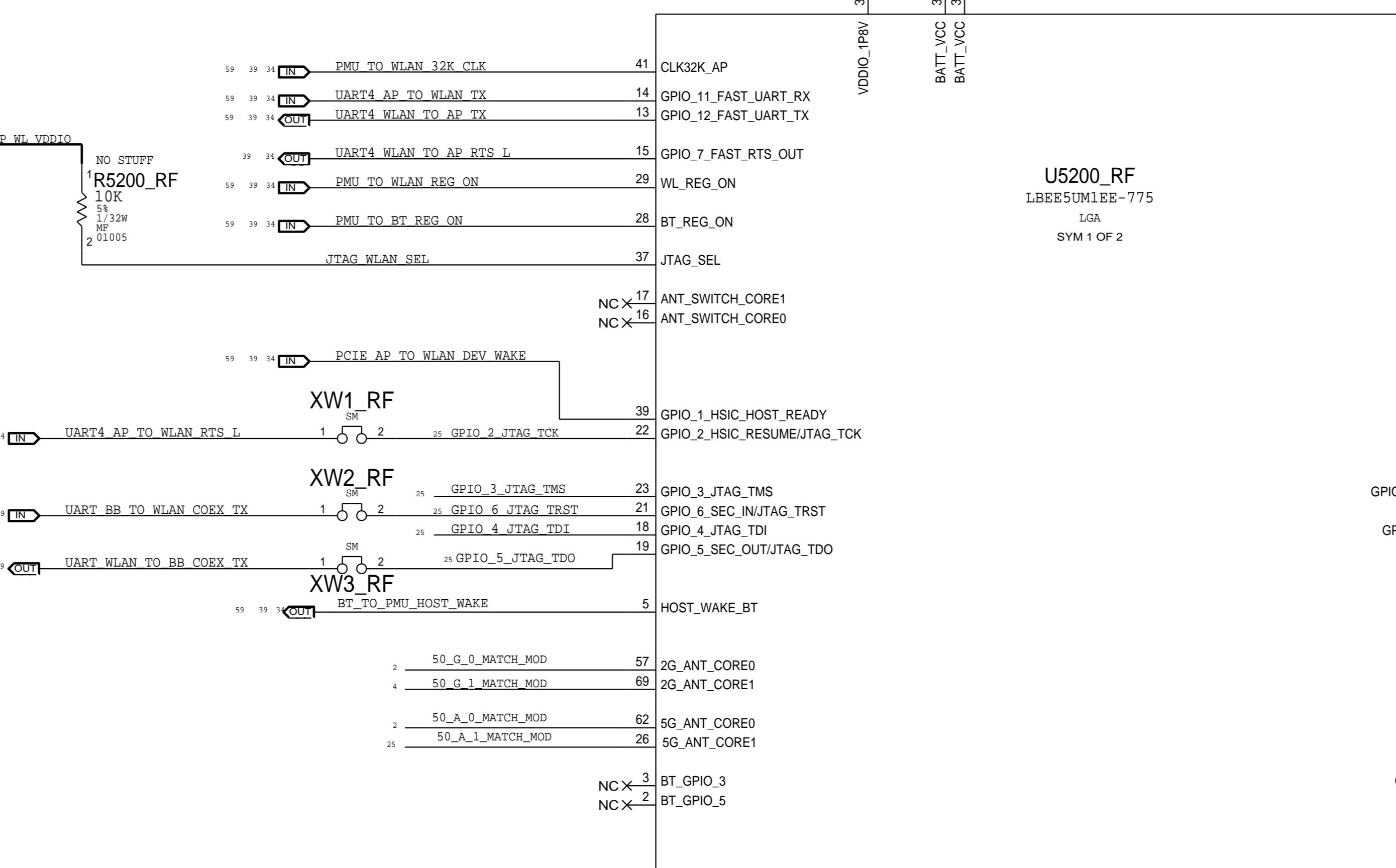


WIFI/BT

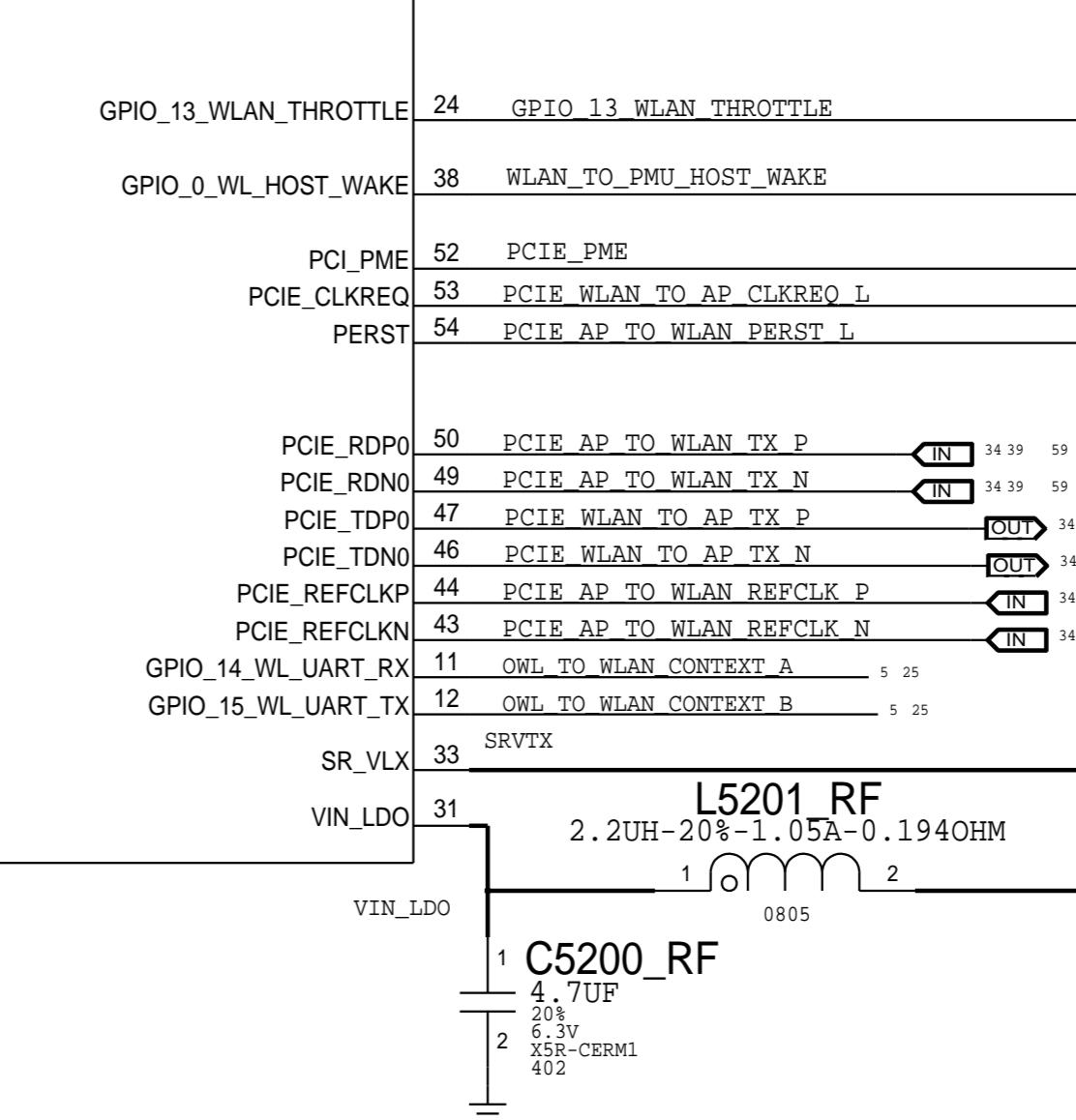
D



D



C



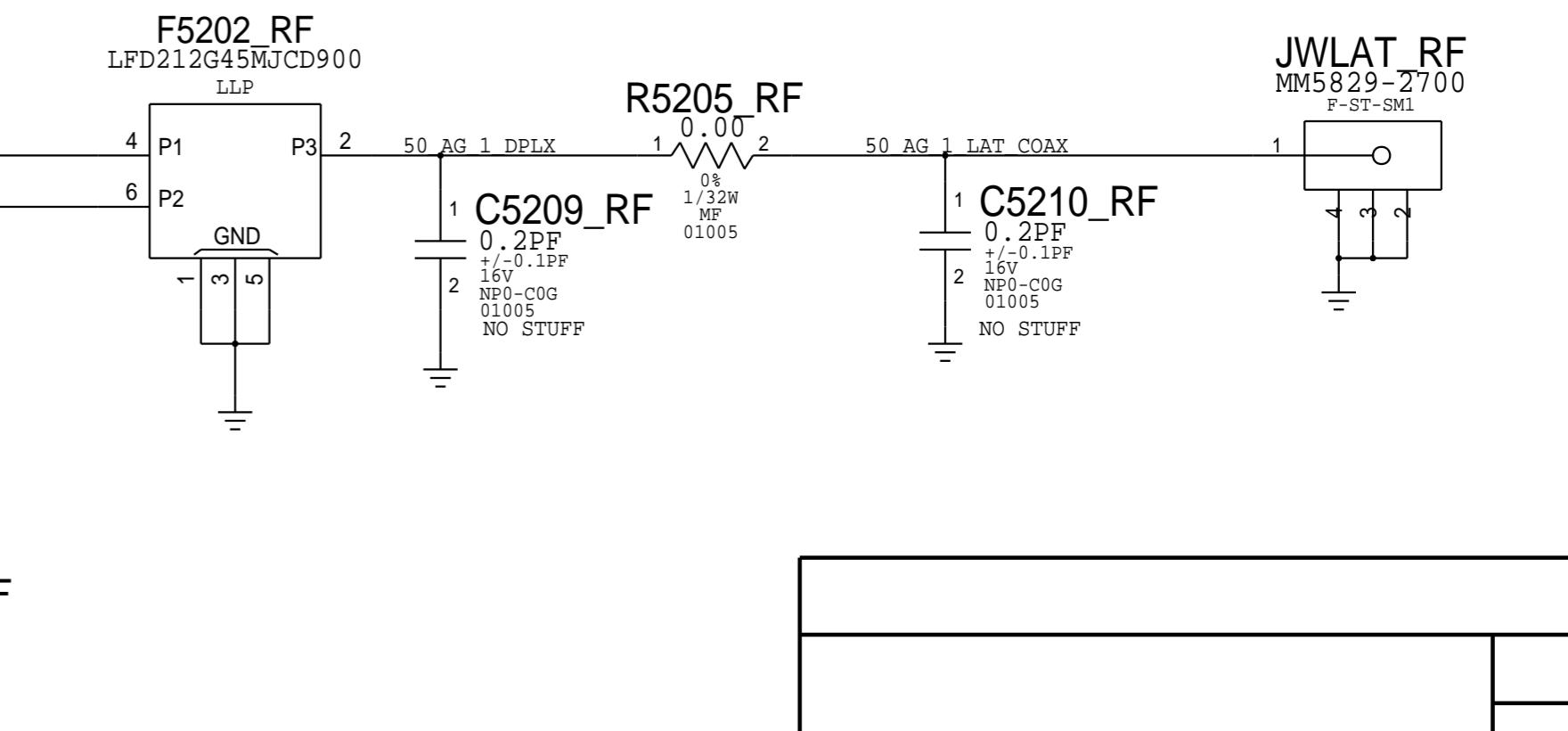
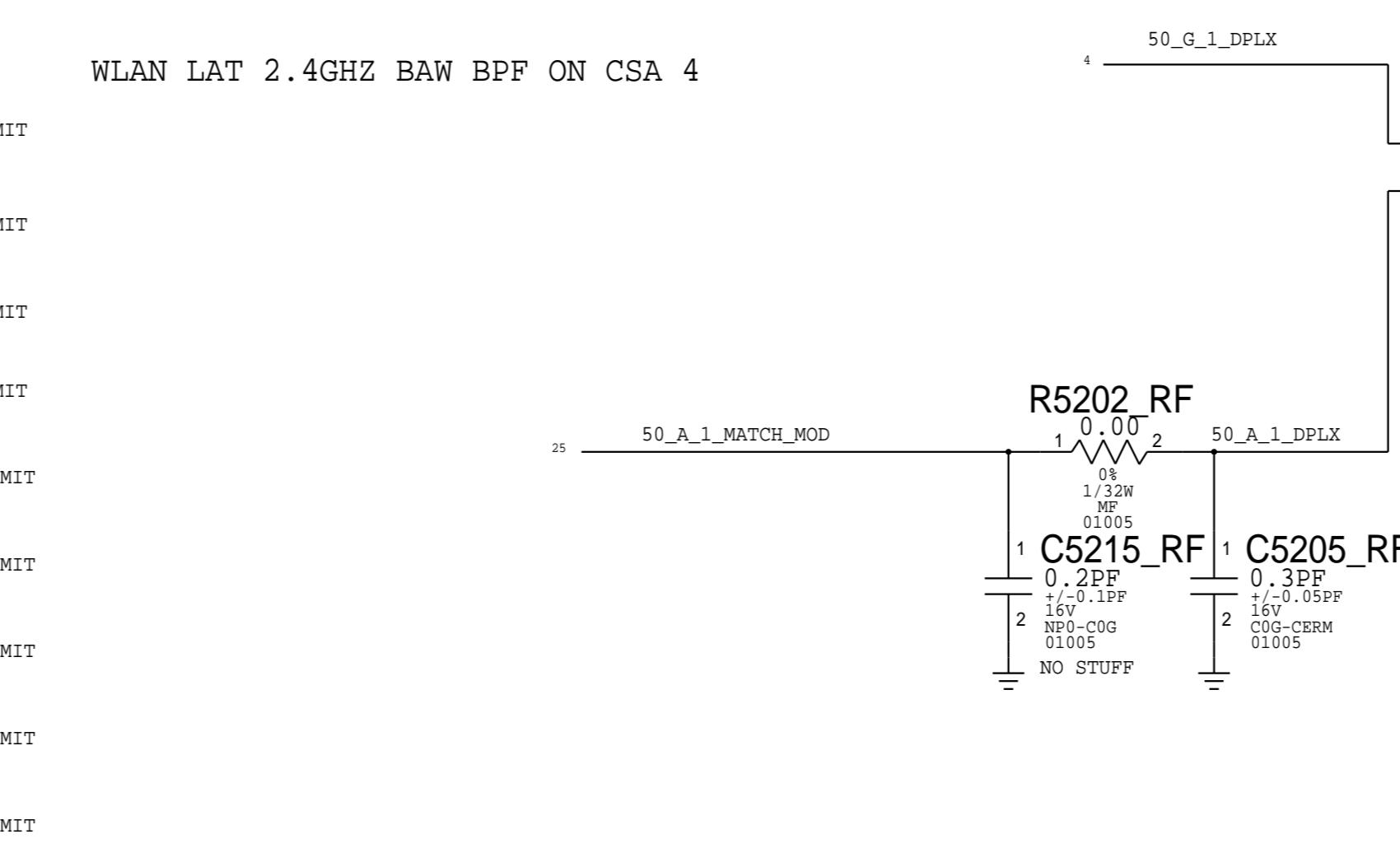
C

B

A

A

WIFI LAT COAX CONNECTOR



A

STOCKHOLM

ALL NETNAMES NEED TO BE CHECKED

