## Mehmet Ataş

## 2304020

## Preliminary Work for Exp. #5

```
PE INP
                                 0X40024020
                         EQU
    GPIO PORTE IS
 2
                        EQU
                                 0X40024404
   GPIO PORTE IBE
                        EQU
                               0X40024408
   GPIO PORTE IEV
                        EQU
                                0X4002440C
                       EQU 0X40024410
 5 GPIO PORTE IM
                       EQU 0X4002441C
 6 GPIO PORTE ICR
7 GPIO_PORTE_RIS EQU 0X40024414
8 GPIO_PORTE_DIR_R EQU 0X40024400
9 GPIO_PORTE_AFSEL_R EQU 0X40024420
10 GPIO PORTE DEN R EQU
                               0X4002451C
11 GPIO PORTE AMSEL R EQU 0X40024528
12 GPIO PORTE PDR
                       EQU
                             0X40024514
   SYSCTL RCGC2 R
13
                       EQU
                               0X400FE608
   RCGCADC
14
                       EQU
                               0x400FE638
15
                     AREA init gpio, CODE,
                                           READONLY, ALIGN=2
16
                    THUMB
17
                     EXPORT
                                 GPIOE Init
18
   GPIOE Init
                PROC
                        R1, =RCGCADC
19
                LDR
20
                LDR
                        RO, [R1]
21
                ORR
                        RO, RO, #0x01
                        RO, [R1]
22
                STR
23
                NOP
24
                NOP
25
                NOP
                       R1,=SYSCTL RCGC2 R
26
                LDR
27
                LDR
                       RO, [R1]
28
                ORR
                       RO, RO, #0X10
29
                       RO, [R1]
                STR
30
                NOP
31
                NOP
32
                NOP
                        DI -CDTO DODTE DID D
32
                NOP
33
                LDR
                        R1,=GPIO PORTE DIR R
34
                LDR
                       RO, [R1]
35
                BIC
                       RO, RO, #0X08
36
                STR
                       RO, [R1]
37
                LDR
                        R1,=GPIO PORTE AFSEL R
38
                LDR
                        RO, [R1]
                ORR
                        RO, RO, #0X08
                        RO, [R1]
40
                STR
41
                LDR
                        R1,=GPIO PORTE DEN R
42
                LDR
                        RO, [R1]
                        RO, RO, #0X08
43
                BIC
44
                STR
                        RO, [R1]
                        R1, =GPIO_PORTE_AMSEL_R
45
                LDR
46
                LDR
                        RO, [R1]
47
                        RO, RO, #0X08
                ORR
48
                STR
                       RO, [R1]
49
                BX
                        LR
50
                ENDP
                ALIGN
51
52
                END
```

Figure 1. GPIO

```
1 RCGCADC
                       EQU 0x400FE638
2 ADCO ACTSS
                       EQU 0x40038000
3 ADCO RIS
                       EQU 0x40038004
 4
   ADCO IM
                       EQU 0x40038008
5 ADCO EMUX
                      EQU 0x40038014
 6 ADCO PSSI
                      EQU 0x40038028
7
   ADCO SSMUX3
                       EQU 0x400380A0
   ADCO SSCTL3
8
                       EQU 0x400380A4
9 ADCO SSFIFO3
                       EQU 0x400380A8
10 ADCO PC
                       EQU 0x40038FC4
11
                    AREA init adc, CODE,
                                          READONLY,
                                                       ALIGN=2
12
                    THUMB
13
                    EXPORT
                               ADCO Init
14 ADCO_Init
                    PROC
                               R1, =ADC0 ACTSS
15
                    LDR
16
                    LDR
                               RO, [R1]
17
                    BIC
                               RO, RO, #0x08
                               RO, [R1]
18
                    STR
19
                               R1, =ADCO EMUX
                    LDR
20
                    LDR
                               RO, [R1]
                               RO, RO, #0xF000
21
                    BIC
                               RO, [R1]
22
                    STR
23
                    LDR
                               R1, =ADC0 SSMUX3
24
                    LDR
                               RO, [R1]
25
                               RO, RO, #0x000F
                    BIC
                    STR
26
                               RO, [R1]
27
                    LDR
                               R1, =ADC0 SSCTL3
28
                    LDR
                               RO, [R1]
                               RO, RO, #0x06
29
                    ORR
                               RO, [R1]
30
                    STR
31
                               R1, =ADCO PC
                    LDR
32
                    LDR
                               RO, [R1]
                               DO DO #0+01
                    ODD
                               R1, =ADC0 PC
31
                   LDR
32
                   LDR
                               RO, [R1]
33
                    ORR
                               RO, RO, #0x01
                               RO, [R1]
34
                    STR
35
                               R1, =ADC0 ACTSS
                   LDR
36
                    LDR
                               RO, [R1]
37
                    ORR
                               RO, RO, #0x08
38
                    STR
                               R0, [R1]
                   BX
39
                               LR
40
                    ENDP
41
                   ALIGN
42
                    END
```

Figure 2. init\_gpio.s

GPIO and ADC initilizations are same for questions. I only show them here not in all parts.

1) Program the ATD conversion system on the board to convert the analog signal to a 12-bit number between 0x000 and 0xFFF. The input is taken from PE3. The output value stored in R0.

1	ADCO_RIS	EQU 0x4	10038004	
2		EQU 0x40038028		
3	ADCO SSFIFO3	EQU 0x400380A8		
4	ADCO ISC	EQU 0X4003800C		
5	-	AREA main,	CODE, READONLY,	ALIGN=2
6		THUMB		
7		IMPORT	GPIOE Init	
8		IMPORT	ADCO Init	
9		EXPORT	main	
10	main	PROC		
11		BL	GPIOE Init	
12		BL	ADCO Init	
13		LDR	R3, =ADCO RIS	
14		LDR	R4, =ADC0 SSFIF03	
15		LDR	R2, =ADCO PSSI	
16		LDR	R6, = ADCO ISC	
17			_	
18	Smpl	LDR	RO, [R2]	
19		ORR	RO, RO, #0x08	
20		STR	RO, [R2]	
21				
22	Cont	LDR	RO, [R3]	
23		ANDS	RO, RO, #8	
24		BEQ	Cont	
25				
26		LDR	RO, [R4]	
27				
28		MOV	RO, #8	
29		STR	RO, [R6]	
30		В	Smpl	
31		ENDP		
32		END		

Figure 1. MAIN OF Q1

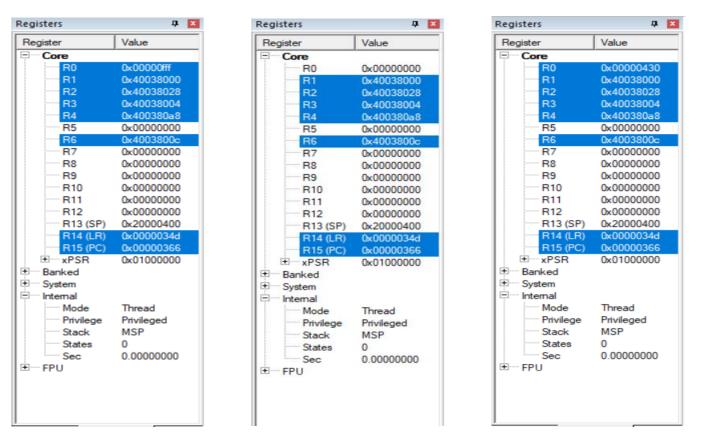
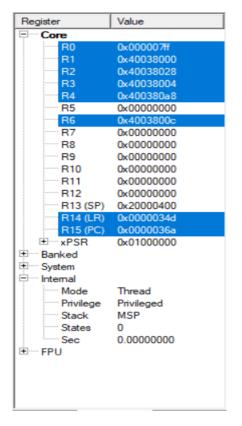


Figure 2. Screenshots of Registers in different situations

2) I assume that there is a 1.65 Volt DC offset in my input. I subtract the value corresponding to this offset from my reading. Put a screenshot of Registers window on your report.

## SUB RO, #2048



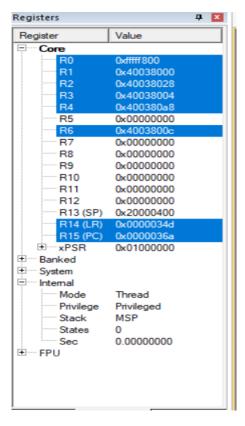


Figure 2. Screenshots of Registers at max and min values

3) I convert the resulting value to a BCD number with two decimal places (X.YZ) between -1.65 and 1.65.

```
1 ADCO RIS
                               EQU 0x40038004
       2 ADCO PSSI
                              EQU 0x40038028
       3 ADC0 SSFIF03
                              EQU 0x400380A8
       4 ADC0 ISC
                              EQU 0X4003800C
       5
                                      messages, DATA, READONLY
       6
                               AREA
       7
                               THUMB
      8
      9
                               DCB "Measured Voltage is: "
         print
     10
                               DCB 0x4
     11
     12
                               DCB "\n "
         new line
     13
                               DCB 0x4
     14
         floating_point
                               DCB "."
     15
                               DCB 0x4
                               DCB " V\n"
     16 voltage
     17
                               DCB 0x4
     18
                          AREA main, CODE, READONLY, ALIGN=2
     19
     20
                          THUMB
     21
                                      GPIOE Init
     22
                          IMPORT
     23
                          IMPORT
                                      ADC0 Init
     24
                          EXTERN
                                      OutStr
     25
                          EXTERN
                                      OutChar
                                      DELAY100
     26
                          EXTERN
     27
                          EXPORT __main
     28
     29
     30
          main
     31
                           PROC
                                       GPIOE_Init
     32
                           _{\mathrm{BL}}
      22
<
                                       GPIOE Init
     32
                           BL
     33
                                       ADC0 Init
                           BL
     34
     35
     36
                                       R3, =ADC0_RIS
                           LDR
                                       R4, =ADC0_SSFIFO3
R2, =ADC0_PSSI
      37
                           LDR
      38
                           LDR
                                       R6, = ADCO ISC
     39
                           LDR
      40
      41
                          LDR
                                       RO, [R2]
         Smpl
                                       RO, RO, #0x08
      42
                           ORR
                                       RO, [R2]
      43
                           STR
      44
      45
         Cont
                           LDR
                                       RO, [R3]
                                       RO, RO, #8
      46
                           ANDS
      47
                           BEQ
                                       Cont
      48
      49
                          LDR
                                       RO, [R4]
     50
                          MOV R6, #330
     51
                          MOV R7, #4095
     52
                          MUL RO, R6
     53
                          UDIV RO,R7
                          SUB R0,#165
     54
     55
                          ; MOV
                                       RO, #8
                          ;STR
     56
                                       RO, [R6]
                                                    ; clear flag
     57
                          MOV R8,#100
     58
                           LDR R5,=print
     59
                           BL OutStr
      60
                          UDIV R10, R0, R8
                          ADD R5,R10,#0X30
      61
      62
                          BL OutChar
                          LDR R5,=floating_point
      63
```

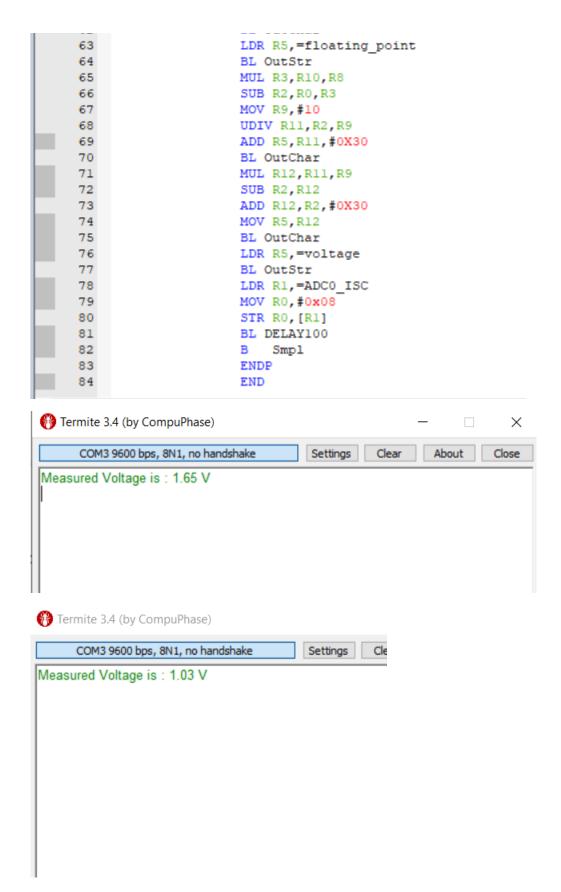


Figure 3. The code and some examples

```
ADC0_ADCACTSS
                       EQU 0x40038000
 2 ADCO_ADCEMUX
                      EQU 0x40038014
                      EQU 0x400380A4
 3 ADC0_ADCSSCTL3
4 ADC0_ADCPC
                       EQU 0x40038FC4
   ADC0_ADCPSSI
 5
                       EQU 0x40038028
 6 ADCO_RIS
                       EQU 0x40038004
   ADC0 ADCSSFIF03
                       EQU 0x400380A8
 8 ADCO ISC
                       EQU 0x4003800C
 9 ADC0 SSMUX3
                       EQU 0x400380A0
10 RCGCADC
                       EQU 0x400FE638
11 GPIO PORTE DIR
                       EQU 0x40024400
14 GPIO PORTE AMSEL
                      EQU 0x40024528
15 GPIO PORTE PCTL
                      EQU 0x4002452C
16
17
18
   SYSCTL_RCGCGPIO
                       EQU 0x400FE608
19
20
                        AREA
                              messages, DATA, READONLY
21
                        THUMB
22
23 print
                        DCB "Measured Voltage is : "
24
                        DCB 0x4
25
                       DCB "\n "
26 new_line
27
                       DCB 0x4
                       DCB "."
28 floating point
29
                        DCB 0x4
30 voltage
                        DCB " V\n"
                        DCB 0x4
31
32
22
33
34
                       AREA main, READONLY, CODE
                       EXTERN OutStr
37
                       EXTERN SETUP
                       EXTERN OutChar
38
                       EXTERN CONVRT
39
                       EXTERN DELAY100
40
41
                       EXPORT main
42
    main
43
                       LDR R1, =SYSCTL_RCGCGPIO
44
                       LDR RO, [R1]
45
46
                       ORR RO, RO, #0x10
47
                       STR RO, [R1]
48
                       NOP
49
                       NOP
50
                       NOP
                       NOP
51
52
                       NOP
                       LDR RO, =GPIO PORTE DIR
53
                       MOV R1, #0x00
54
55
                       STR R1, [R0]
                       LDR RO, =GPIO_PORTE_AFSEL
56
57
                       MOV R1, #0x08
58
                       STR R1, [R0]
59
                       LDR RO, =GPIO PORTE AMSEL
                       MOV R1, #0x08
60
61
                       STR R1, [R0]
62
63
                       LDR RO, = RCGCADC
64
                       MOV R1, #0x01
                       CTD D1 FD01
```

```
65
                           STR R1, [R0]
 66
                           NOP
 67
                           NOP
 68
                           NOP
 69
                           NOP
 70
                           NOP
 71
                           NOP
 72
 73
                           LDR R1, = ADCO ADCACTSS
 74
                           MOV RO, #0x00
 75
                           STR R0, [R1]
 76
 77
                           LDR R1, =ADCO ADCEMUX
                           MOV RO, #0x00
 78
 79
                           STR R0, [R1]
 80
 81
                           LDR R1, =ADC0 SSMUX3
                           MOV RO, #0x00
 82
 83
                           STR R0, [R1]
 84
                           LDR R1, =ADC0 ADCSSCTL3
 85
                           MOV RO, #0x06
 86
 87
                           STR R0, [R1]
 88
                           LDR R1, =ADC0 ADCPC
 89
 90
                           MOV RO, #0X01
 91
                           STR RO, [R1]
 92
 93
                           LDR R1, =ADCO_ADCACTSS
 94
                           MOV RO, #0X08
 95
                           STR R0, [R1]
 96
 96
 97
 98
                           LDR R1, =ADC0 ADCPSSI
    loop
 99
                           MOV RO, #0x08
                           STR R0, [R1]
100
101
102
                           LDR R1, =ADCO RIS
103
                           LDR RO, [R1]
104
                           CMP RO, #0X08
105
                           BNE loop
106
107
108
                           LDR R1, =ADC0 ADCSSFIF03
109
                           LDR RO, [R1]
110
111
                           MOV R6, #330
112
                           MOV R7, #4095
113
114
                           MUL RO, R6
115
                           UDIV RO, R7
116
117
                           SUB R0,165
                           MOV R8, #100
118
119
120
                           LDR R5, =print
121
                           BL OutStr
122
                           UDIV R10, R0, R8
123
                           ADD R5, R10, #0X30
124
125
                           BL OutChar
126
127
                           LDR R5,=floating_point
                            DT A .... C+-
```

```
BL OutStr
121
122
123
                            UDIV R10, R0, R8
                            ADD R5, R10, #0X30
124
125
                            BL OutChar
126
127
                            LDR R5,=floating point
                            BL OutStr
128
129
130
                            MUL R3, R10, R8
131
                            SUB R2, R0, R3
132
                            MOV R9, #10
133
134
                            UDIV R11, R2, R9
                            ADD R5, R11, #0X30
135
136
                            BL OutChar
137
138
                            MUL R12, R11, R9
139
                            SUB R2, R12
                            ADD R12, R2, #0X30
140
141
                            MOV R5, R12
                            BL OutChar
142
143
                            LDR R5, =voltage
144
145
                            BL OutStr
                            LDR R1,=ADC0 ISC
146
147
                            MOV RO, #0x08
148
                            STR R0, [R1]
149
                            BL DELAY100
150
                            B loop
151
152
```

Termite 3.4 (by CompuPhase)

```
COM3 9600 bps, 8N1, no handshake
                                            Settings
Measured Voltage is: 1.65 V
Measured Voltage is: 1.47 V
Measured Voltage is: 1.37 V
Measured Voltage is: 1.29 V
Measured Voltage is: 1.23 V
Measured Voltage is: 1.15 V
Measured Voltage is: 1.08 V
Measured Voltage is: 1.01 V
Measured Voltage is: 0.95 V
Measured Voltage is: 0.90 V
Measured Voltage is: 0.84 V
Measured Voltage is: 0.76 V
Measured Voltage is: 0.67 V
Measured Voltage is: 0.55 V
Measured Voltage is: 0.46 V
Measured Voltage is: 0.37 V
Measured Voltage is: 0.35 V
Measured Voltage is: 0.35 V
```

Figure 4. The code and some examples