Lab Experiment 1

1 – I compensated my oscilloscope probe by connecting Probe's BNC connector to Channel X end. Also I connected oscilloscope hook tip to demo 2 end and also ground output to ground end.

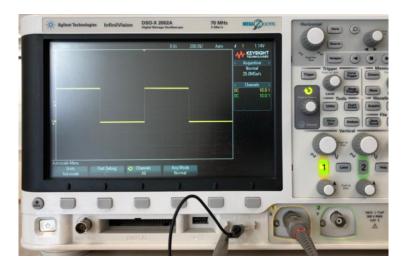


Figure 1 – Probe compensation

2 – In positive edge triggering, trigger samples according to rising slope; in negative edge triggering, trigger samples data with falling slope. We can say that positive edge triggering is an example of positive sine wave and also negative edge triggering is an example of negative sine wave.

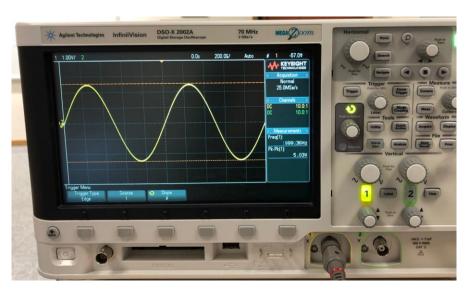


Figure 2 – Positive Edge Triggering

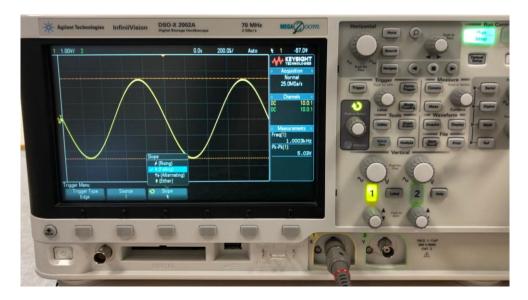


Figure3 – Negative Edge Triggering

3 – Oscilloscope's triggering function is very important to see clear signals. With using triggering, we can stabilize repetitive waveforms and observe stable signal. When we change trigger, signal slides to left or right unless trigger exceeds the wave. If trigger exceeds, signal will be meaningless.

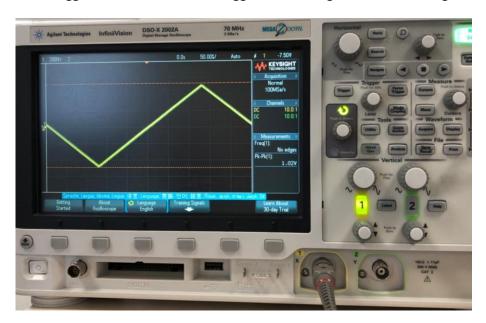


Figure4 – Original wave without triggering

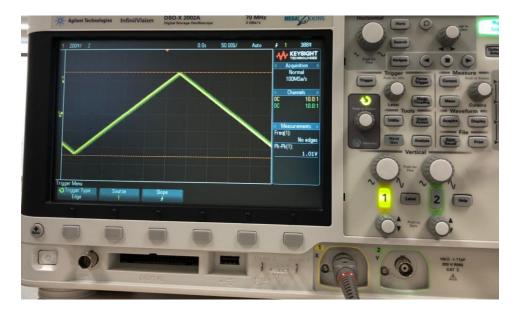


Figure5 – Trigger up

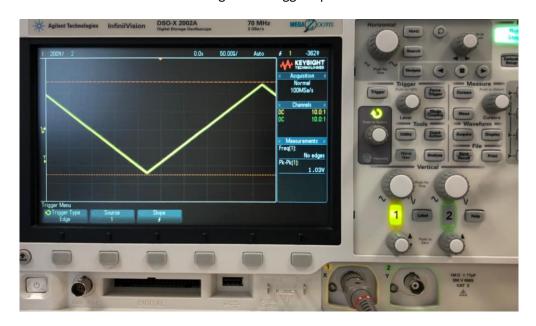


Figure6 – Trigger Down



Figure7 – Trigger Exceeds

4 – Digital to analog converter basically converts digital signal to analog signal. Also analog to digital converter converts analog signal to digital signal. A digital oscilloscope uses Analog-to-digital converter (ADC). A digital oscilloscope samples the waveform and uses an analog-to-digital converter to convert the voltage into digital information. We use acquisition modes to see clear signals. There is not many difference between acquisition modes.



Figure8 – Sample (Normal Acquisition)

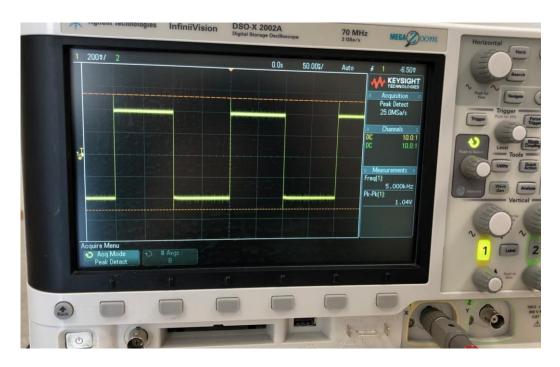


Figure9 – Peak Detect (Acq. Mode)

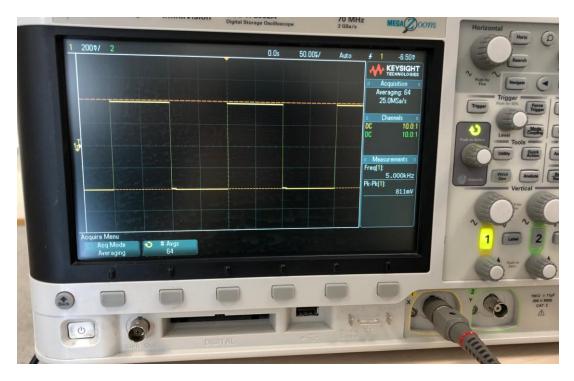


Figure 10 – Average (64) (Acq. Mode)

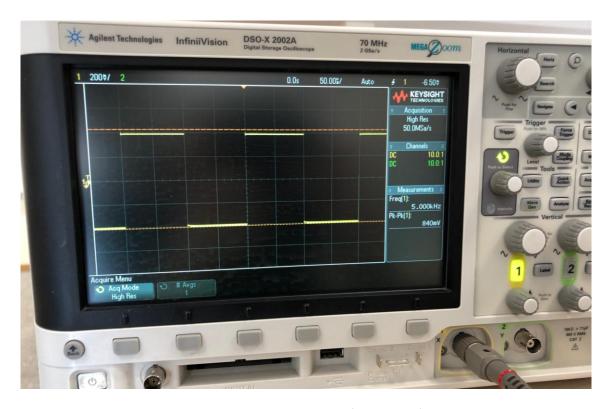


Figure 11 – High Resolution (Acq. Mode)

5 – AC coupling deletes the direct DC voltage from signal. So, signal will slide down when we use AC coupling. If we use 1V DC offset, our signal will be at the center of screen.

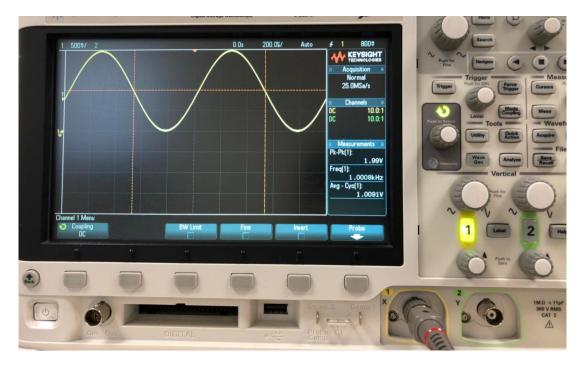


Figure 12 – DC coupling (with 1V DC offset)

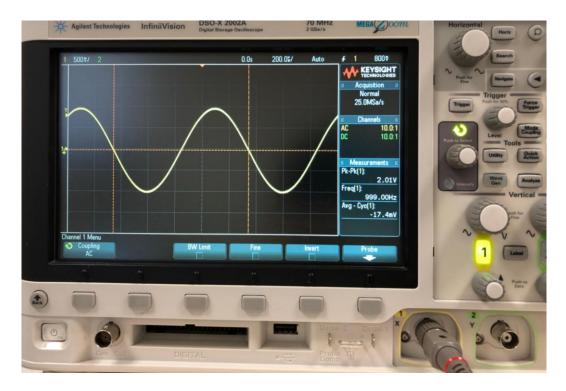


Figure 13 – AC coupling (with 1V DC offset)

6 – Breadboard provides us a useful space to design circuits without using many cables.

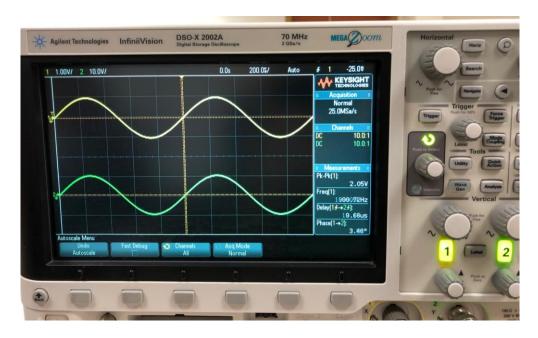


Figure 14 – 1kHz Signals

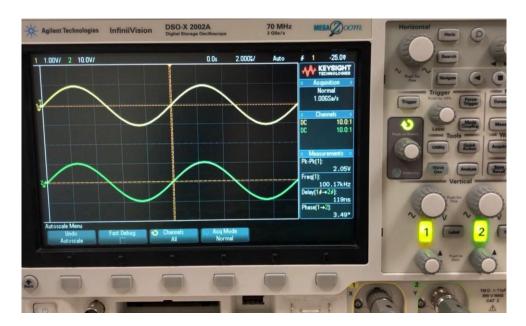


Figure 15 – 100kHz Signals

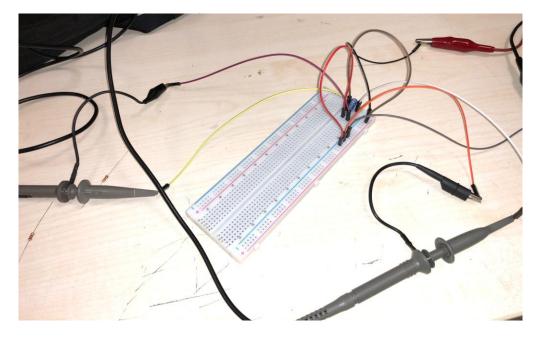


Figure 16 – Breadboard Circuit

The difference between these two experiment stems from frequency. Also there are a lot of factors such as our equipments may not be ideal but main cause is frequency and lower frequency lead to bigger phase.