AVR clk and Reset

hroodaki@kntu.ac.ir

Reset

- PC = 0
- Clear GPR and I/O Register
- Input mode for The I/O ports

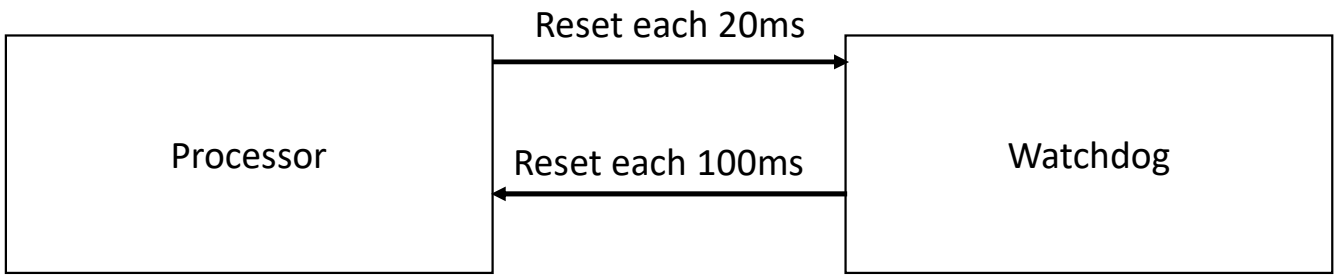
Reset

- 4 ways to reset the processor:
  - Power on Reset
    - Use Vcc
  - External Reset
  - Brown Out Reset
  - Watchdog Reset

Brown Out Reset

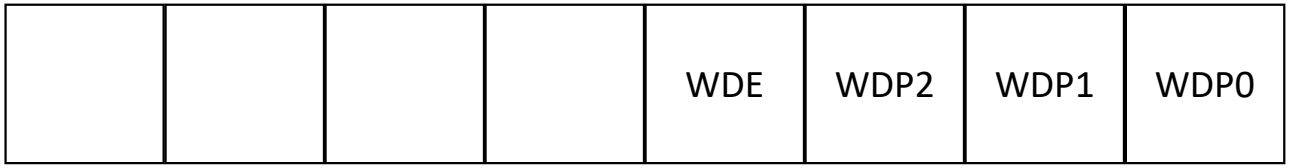
- If BODEN is enabled.
  - Brown out detection enabled
  - Brown out detection level
    - 0 if Vth = 4v
    - 1 if Vth = 2.7v

Watchdog Reset



Watchdog Reset

- Watchdog timer control register (WDTCR)



Watchdog Reset

WDP2	WDP1	WDP0	clk pulse for 1 MHz
0	0	0	16k
0	0	1	32k
0	1	0	64k
0	1	1	128k
1	0	0	256k
1	0	1	512k
1	1	0	1024k
1	1	1	2048k

Clk Distribution