

TLA+ specification of *Lamport's* distributed mutual-exclusion algorithm as mentioned in Assignment 2

EXTENDS *Naturals, Sequences, TLC*

The parameter  $N$  represents the number of processes. The parameter  $maxClock$  is used only for model checking in order to keep the state space finite.

CONSTANT  $N, maxClock$

ASSUME  $NType \triangleq N \in Nat$

ASSUME  $maxClockType \triangleq maxClock \in Nat$

$Proc \triangleq 1 \dots N$

$Clock \triangleq Nat \setminus \{0\}$

For model checking, add *ClockConstraint* as a state constraint to ensure a finite state space and override the definition of *Clock* by  $1 \dots maxClock + 1$  so that *TLC* can evaluate the definition of *Message*.

VARIABLES

$clock,$	local clock of each process
$req,$	requests received from processes stored as sequence (clock transmitted with request)
$ack,$	acknowledgements received from processes
$network,$	messages sent but not yet received
$crit$	set of processes in critical section

Messages used in the algorithm.

$ReqMessage(c) \triangleq [type \mapsto \text{"req"}, clock \mapsto c]$

$AckMessage(c) \triangleq [type \mapsto \text{"ack"}, clock \mapsto c]$

$RelMessage \triangleq [type \mapsto \text{"rel"}, clock \mapsto 0]$

$Message \triangleq \{RelMessage\} \cup \{ReqMessage(c) : c \in Clock\} \cup \{AckMessage(c) : c \in Clock\}$

The type correctness predicate.

$TypeOK \triangleq$

$clock[p]$  is the local clock of process  $p$

$\wedge clock \in [Proc \rightarrow Clock]$

$req[p][q]$  stores sequence of clock associated with request from  $q$  received by  $p$ , empty if none

$\wedge req \in [Proc \rightarrow [Proc \rightarrow Seq(Nat)]]$

$ack[p]$  stores the processes that have *ack*'ed  $p$ 's request

$\wedge ack \in [Proc \rightarrow [Proc \rightarrow Nat]]$

$network[p][q]$ : queue of messages from  $p$  to  $q$  – pairwise *FIFO* communication

$\wedge network \in [Proc \rightarrow [Proc \rightarrow Seq(Message)]]$

set of processes in critical section: should be empty or singleton

$\wedge crit \in SUBSET Proc$

$$Init \triangleq$$

*beats*( $p, q$ ) is true if process  $p$  believes that its request has higher priority than  $q$ 's request. This is true if either  $p$  has not received a request from  $q$  or  $p$ 's request has a smaller clock value than any received from  $q$ . If there is a tie, the numerical process *ID* decides.

Broadcast a message: send it to all processes except the sender.

Process  $p$  requests access to critical section. Increment  $Clock$  time also

Process  $p$  receives a request from  $q$ . It adds this new request to its request Queue and increment the clock *time*. Also it acknowledges it with a timestamp.

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Process  $p$  receives an acknowledgement from  $q$ .

$$\begin{aligned}
\text{ReceiveAck}(p, q) &\triangleq \\
&\wedge \text{network}[q][p] \neq \langle \rangle \\
&\quad \wedge \text{LET } m \triangleq \text{Head}(\text{network}[q][p]) \\
&\quad \text{IN } \wedge m.\text{type} = \text{"ack"} \\
&\quad \wedge \text{ack}' = [\text{ack} \text{ EXCEPT } ![p][q] = m.\text{clock}] \\
&\quad \wedge \text{network}' = [\text{network} \text{ EXCEPT } ![q][p] = \text{Tail}(@)] \\
&\quad \wedge \text{UNCHANGED } \langle \text{clock}, \text{req}, \text{crit} \rangle
\end{aligned}$$

Process  $p$  enters the critical section. *Process* enter *CS* if it has a request in the request queue and it beats all other processes. Also the timestamp of all the *Ack* should be greater than the request time stamp

$$\begin{aligned}
\text{Enter}(p) &\triangleq \\
&\wedge \text{req}[p][p] \neq \langle \rangle \\
&\quad \wedge \text{LET } p\text{Top} \triangleq \text{Head}(\text{req}[p][p]) \\
&\quad \text{IN } \wedge \forall q \in \text{Proc} \setminus \{p\} : p\text{Top} < \text{ack}[p][q] \\
&\quad \wedge \forall q \in \text{Proc} \setminus \{p\} : \text{beats}(p, q) \\
&\quad \wedge \text{crit}' = \text{crit} \cup \{p\} \\
&\quad \wedge \text{UNCHANGED } \langle \text{clock}, \text{req}, \text{ack}, \text{network} \rangle
\end{aligned}$$

Process  $p$  exits the critical section and notifies other processes. *It* deletes any one of its request from its request queue

$$\begin{aligned}
\text{Exit}(p) &\triangleq \\
&\wedge \text{LET } l \triangleq \text{RandomElement}(1 \dots (\text{Len}(\text{req}[p][p]))) \text{IN} \\
&\quad \wedge p \in \text{crit} \\
&\quad \wedge \text{crit}' = \text{crit} \setminus \{p\} \\
&\quad \wedge \text{network}' = [\text{network} \text{ EXCEPT } ![p] = \text{Broadcast}(p, \text{RelMessage})] \\
&\quad \wedge \text{req}' = [\text{req} \text{ EXCEPT } ![p][p] = \text{Tail}(@)] \quad \text{Uncomment this line of code to check for Liveness} \\
&\quad \wedge \text{req}' = [\text{req} \text{ EXCEPT } ![p][p] = [i \in 1 \dots (\text{Len}(@) - 1) \mapsto \text{IF } i < l \text{ THEN } @[i] \text{ ELSE } @[i + 1]]] \quad \text{Comment t} \\
&\quad \wedge \text{UNCHANGED } \langle \text{clock}, \text{ack} \rangle
\end{aligned}$$

Process  $p$  receives a release notification from  $q$  and deletes any request from its request queue

$$\begin{aligned}
\text{ReceiveRelease}(p, q) &\triangleq \\
&\wedge \text{LET } l \triangleq \text{RandomElement}(1 \dots (\text{Len}(\text{req}[p][q]))) \text{IN} \\
&\quad \wedge \text{network}[q][p] \neq \langle \rangle \\
&\quad \wedge \text{LET } m \triangleq \text{Head}(\text{network}[q][p]) \\
&\quad \text{IN } \wedge m.\text{type} = \text{"rel"} \\
&\quad \wedge \text{req}' = [\text{req} \text{ EXCEPT } ![p][q] = \text{Tail}(@)] \\
&\quad \wedge \text{req}' = [\text{req} \text{ EXCEPT } ![p][q] = [i \in 1 \dots (\text{Len}(@) - 1) \mapsto \text{IF } i < l \text{ THEN } @[i] \text{ ELSE } @[i + 1]]] \\
&\quad \wedge \text{network}' = [\text{network} \text{ EXCEPT } ![q][p] = \text{Tail}(@)] \\
&\quad \wedge \text{UNCHANGED } \langle \text{clock}, \text{ack}, \text{crit} \rangle
\end{aligned}$$

$$\wedge Print(l, \text{TRUE})$$

Next-state relation.

$$\begin{aligned} Next &\triangleq \\ &\vee \exists p \in Proc : Request(p) \vee Enter(p) \vee Exit(p) \\ &\vee \exists p \in Proc : \exists q \in Proc \setminus \{p\} : ReceiveRequest(p, q) \vee ReceiveAck(p, q) \vee ReceiveRelease(p, q) \end{aligned}$$

$$vars \triangleq \langle req, network, clock, ack, crit \rangle$$

$$Spec \triangleq Init \wedge \Box [Next]_{vars} \wedge WF_{vars}(Next)$$

A state constraint that is useful for validating the specification using finite-state model checking. *Constraints* imposed on clock, network and request

$$\begin{aligned} StateConstraint &\triangleq \wedge \forall p \in Proc : clock[p] \leq maxClock \\ &\wedge \forall p, q \in Proc : Len(network[p][q]) \leq 1 \\ &\wedge \forall p, q \in Proc : Len(req[p][q]) \leq 2 \end{aligned}$$

The main safety property of mutual exclusion.

$$Mutex \triangleq \forall p, q \in crit : p = q$$

$$Live \triangleq \forall p \in Proc : \Diamond(p \in crit)$$

\ \* Modification History

\ \* Last modified Sun Oct 07 20:57:29 EDT 2018 by mehuljain

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