
MODULE *TimeClock*

EXTENDS *Naturals, Sequences* Import *Naturals* and *Sequences* for *Nat* and *Append, Head, Tail*

CONSTANT *Proc*, \ll , *NumofNats*

ASSUME $\forall p \in Proc : \neg p \ll p$
 $\quad \quad \quad \wedge \forall q \in Proc \setminus \{p\} : (p \ll q) \vee (q \ll p)$
 $\quad \quad \quad \wedge \forall q, r \in Proc : (p \ll q) \wedge (q \ll r) \Rightarrow (p \ll r)$

$a \prec b \triangleq \vee a.TS < b.TS$
 $\quad \quad \vee (a.TS = b.TS) \wedge (a.proc \ll b.proc)$

VARIABLES *state*, *msgQ*, *reqSet*, *clock*, *lastTSent*, *lastTRcvd*

vars $\triangleq \langle state, msgQ, reqSet, clock, lastTSent, lastTRcvd \rangle$

Init \triangleq
 $\quad \wedge state = [p \in Proc \mapsto \text{"idle"}]$
 $\quad \wedge msgQ = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto \langle \rangle]]$
 $\quad \wedge reqSet = [p \in Proc \mapsto \{\}]$
 $\quad \wedge clock \in [Proc \rightarrow 0 \dots NumofNats]$
 $\quad \wedge lastTSent = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto 0]]$
 $\quad \wedge lastTRcvd = [p \in Proc \mapsto [q \in Proc \setminus \{p\} \mapsto 0]]$

Request(*p*) \triangleq
 $\quad \wedge state[p] = \text{"idle"}$
 $\quad \wedge state' = [state \text{ EXCEPT } ![p] = \text{"waiting"}]$
 $\quad \wedge \exists n \in 0 \dots NumofNats :$
 $\quad \quad \wedge clock' = [clock \text{ EXCEPT } ![p] = n]$
 $\quad \quad \wedge n > clock[p]$
 $\quad \quad \wedge \text{LET } msg \triangleq [TS \mapsto n, proc \mapsto p, cmd \mapsto \text{"acquire"}]$
 $\quad \quad \text{IN } \wedge msgQ' = [msgQ \text{ EXCEPT } ![p] =$
 $\quad \quad \quad [q \in Proc \setminus \{p\} \mapsto Append(@[q], msg)]]$
 $\quad \quad \wedge reqSet' = [reqSet \text{ EXCEPT } ![p] = @ \cup \{msg\}]$
 $\quad \quad \wedge lastTSent' = [lastTSent \text{ EXCEPT } ![p] = [q \in Proc \setminus \{p\} \mapsto n]]$
 $\quad \wedge \text{UNCHANGED } lastTRcvd$

Acquire(*p*) \triangleq
 $\quad \text{LET } pReq \triangleq \text{CHOOSE } req \in reqSet[p] : req.proc = p$
 $\quad \text{IN } \wedge state[p] = \text{"waiting"}$
 $\quad \quad \wedge \forall req \in reqSet[p] \setminus \{pReq\} : pReq \prec req$

$$\begin{aligned}
& \wedge \forall q \in Proc \setminus \{p\} : pReq \prec [TS \mapsto lastTRcvd[p][q] + 1, proc \mapsto q] \\
& \wedge state' = [state \text{ EXCEPT } ![p] = \text{"owner"}] \\
& \wedge reqSet' = [reqSet \text{ EXCEPT } ![p] = @ \setminus \{pReq\}] \\
& \wedge \text{UNCHANGED } \langle msgQ, clock, lastTSent, lastTRcvd \rangle
\end{aligned}$$

$$\begin{aligned}
Release(p) & \triangleq \\
& \wedge state[p] = \text{"owner"} \\
& \wedge state' = [state \text{ EXCEPT } ![p] = \text{"idle"}] \\
& \wedge \text{LET } msg \triangleq [TS \mapsto clock[p], proc \mapsto p, cmd \mapsto \text{"release"}] \\
& \quad \text{IN } msgQ' = [msgQ \text{ EXCEPT } ![p] = \\
& \quad \quad [q \in Proc \setminus \{p\} \mapsto Append(@[q], msg)]] \\
& \wedge lastTSent' = [lastTSent \text{ EXCEPT } ![p] = [q \in Proc \setminus \{p\} \mapsto clock[p]]] \\
& \wedge \text{UNCHANGED } \langle clock, lastTRcvd, reqSet \rangle
\end{aligned}$$

$$\begin{aligned}
RcvMsg(p, q) & \triangleq \\
& \text{LET } msg \triangleq Head(msgQ[q][p]) \\
& \quad msgQTail \triangleq [msgQ \text{ EXCEPT } ![q][p] = Tail(@)] \\
& \quad ack \triangleq [TS \mapsto clock'[p], proc \mapsto p, cmd \mapsto \text{"ack"}] \\
& \text{IN } \wedge msgQ[q][p] \neq \langle \rangle \\
& \quad \wedge clock' = [clock \text{ EXCEPT } ![p] = \text{IF } msg.TS > @ \text{ THEN } msg.TS \\
& \quad \quad \quad \text{ELSE } @] \\
& \quad \wedge \text{IF } \wedge msg.cmd = \text{"acquire"} \\
& \quad \quad \wedge [TS \mapsto lastTSent[p][q] + 1, proc \mapsto p] \prec msg \\
& \quad \quad \text{THEN } \wedge msgQ' = [msgQTail \text{ EXCEPT } ![p][q] = Append(@, ack)] \\
& \quad \quad \quad \wedge lastTSent' = [lastTSent \text{ EXCEPT } ![p][q] = clock'[p]] \\
& \quad \quad \text{ELSE } \wedge msgQ' = msgQTail \\
& \quad \quad \quad \wedge \text{UNCHANGED } lastTSent \\
& \quad \wedge lastTRcvd' = [lastTRcvd \text{ EXCEPT } ![p][q] = msg.TS] \\
& \quad \wedge reqSet' = [reqSet \text{ EXCEPT } ![p] = \\
& \quad \quad \text{CASE } msg.cmd = \text{"acquire"} \rightarrow @ \cup \{msg\} \\
& \quad \quad \quad \square \quad msg.cmd = \text{"release"} \rightarrow \{m \in @ : m.proc \neq q\} \\
& \quad \quad \quad \square \quad msg.cmd = \text{"ack"} \rightarrow @] \\
& \quad \wedge \text{UNCHANGED } state
\end{aligned}$$

$$\begin{aligned}
Tick(p) & \triangleq \wedge \exists n \in 0 \dots NumofNats : \wedge n > clock[p] \\
& \quad \wedge clock' = [clock \text{ EXCEPT } ![p] = n] \\
& \quad \wedge \text{UNCHANGED } \langle state, msgQ, reqSet, lastTSent, lastTRcvd \rangle
\end{aligned}$$

$$\begin{aligned}
Next & \triangleq \exists p \in Proc : \vee Request(p) \vee Acquire(p) \vee Release(p) \\
& \quad \vee \exists q \in Proc \setminus \{p\} : RcvMsg(p, q)
\end{aligned}$$

$$\vee \textit{Tick}(p)$$

$$\begin{aligned} \textit{Liveness} \triangleq & \forall p \in \textit{Proc} : \wedge \textit{WF}_{\textit{vars}}(\textit{Acquire}(p)) \\ & \wedge \forall q \in \textit{Proc} \setminus \{p\} : \textit{WF}_{\textit{vars}}(\textit{RcvMsg}(p, q)) \end{aligned}$$

$$\textit{Constraint} \triangleq \forall p \in \textit{Proc} : \textit{clock}[p] < \textit{NumofNats}$$

$$\textit{Mutex} \triangleq \forall p, q \in \textit{Proc} : p \neq q \Rightarrow (\{ \textit{state}[p], \textit{state}[q] \} \neq \{ \text{"owner"} \})$$

$$\textit{EventuallyAcquires} \triangleq \forall p \in \textit{Proc} : (\textit{state}[p] = \text{"waiting"}) \rightsquigarrow (\textit{state}[p] = \text{"owner"})$$

$$\textit{AlwaysReleases} \triangleq \forall p \in \textit{Proc} : \textit{WF}_{\textit{state}}(\textit{Release}(p))$$

$$\textit{Spec} \triangleq \textit{Init} \wedge \Box[\textit{Next}]_{\textit{vars}} \wedge \textit{Liveness}$$

$$\textit{LIVE} \triangleq \textit{Spec} \wedge \textit{AlwaysReleases}$$

\ * Modification History
 \ * Last modified Sun Oct 07 01:26:14 EDT 2018 by mehuljain
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