

MR10Q010 – 1Mb Quad SPI MRAM

VERILOG Model Readme File

Introduction

This is the VERILOG model for MR10Q010 - a 1Mb Quad SPS MRAM Product from Everspin.

Device Summary

The MR10Q010 Quad SPI MRAM is a high bandwidth extension of our SPI MRAM product. It is a 1,048,576 bit magneto-resistive random access memory (MRAM) device organized as 131,072 words of 8 bits. Four I/O's allow very fast reads and writes, making it an attractive alternative to conventional parallel data bus interfaces in next generation RAID controllers, server system logs, storage device buffers, and embedded system data and program memory. The device is offered in a 16 pin SOIC.

Model Release Notes

Product Datasheet: http://www.everspin.com/PDF/EST_MR10Q010_Datasheet.pdf

Model Revision 0.0

Model Release Date: January 2014

Files:

Readme_MR10Q010 -This file
mr10q010.v -Device Model

VERILOG Model

Mr10q010.v is the abstracted model of a 1Mb Quad SPI MRAM. The model is setup for operation up to 104MHz

Warning: These VERILOG models are provided “as is” without warranty of any kind, including, but not limited to, any implied warranty of merchantability and fitness for a particular purpose.

Revision History

Date	Revision	Changes
1/20/2014	0.0	New Model – Initial Release

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