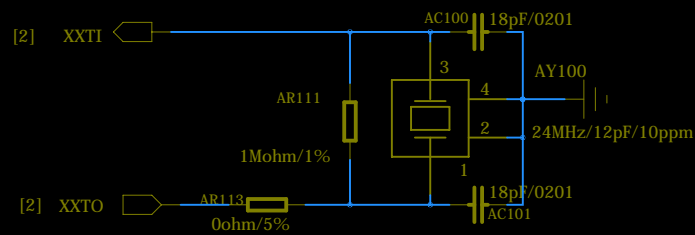


REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE

COMPANY: Meizu			
TITLE: M86			
DRAWN: Evan	DATED: 2015.02.03		
CHECKED: <Checked By>	DATED: <Checked Date>	CODE: <Code>	SIZE: A1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	DRAWING NO: <Drawing Number>	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	REV: V1.0
SHEET: 1 of 27			

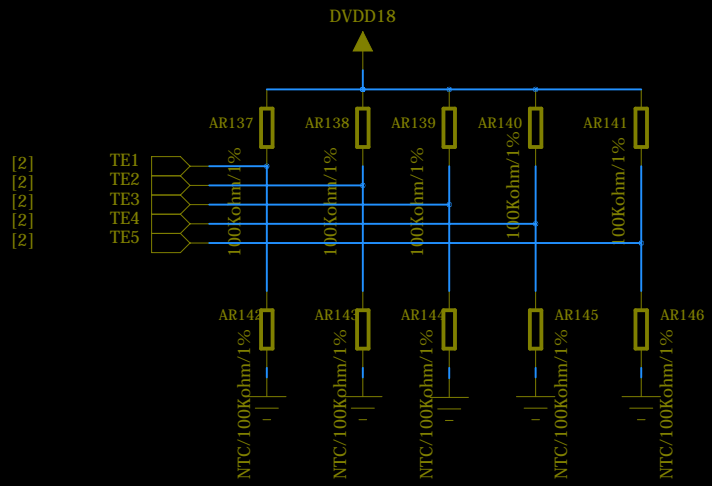


REVISION RECORD			
UTR	ECO NO.	APPROVED:	DATE



Bootling Device Option		
OM[5:1]	1st	2nd
00000	sMMC_CH0 with SWIEN	USB3
00010	sMMC_CH2	USB2
00100	sMMC_CH0	USB3
00101	UFS	USB3
00110	SPI_CH1	USB

Pin	High	Low
XCCHPASS	CCT_BUS_Dns	CCT_BUS_Rn
XOM0	AXIS	APOLLO
XCPUMISSELA		ACE



COMPANY: Meizu			
TITLE: M86			
DRAWN: Evan	DATED: 2015.02.03	CODE	SIZE
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO.	REV.
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	A1<Drawing Number> V1.0
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 2/ 27



C

B

D

C

B

A

DRAWN: Evan	DATED: 2015.02.03	M86			
CHECKED: <Checked By>	DATED: <Checked Date>	CODE	SIZE	DRAWING NO.	REV.
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	A1	<Drawing Number>	V1.0
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: 3 of 27



D

C

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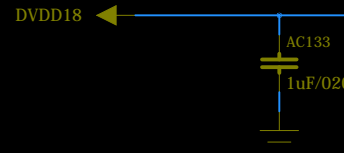
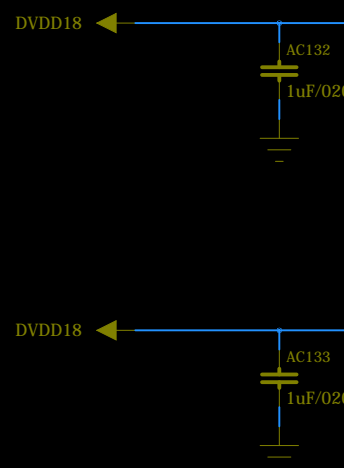
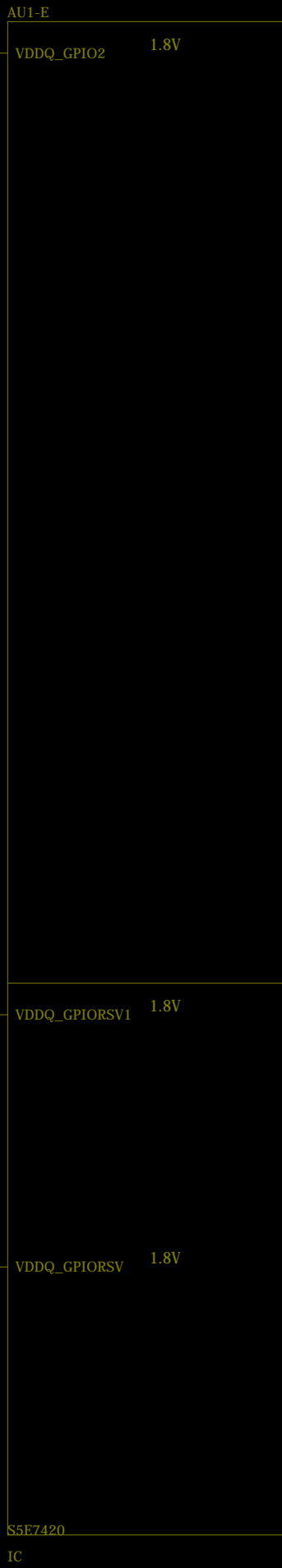
C

B

A



REVISION RECORD			
LD#	REV NO.	APPROVED	DATE



EINT VDDQ_AUIVF (1.8V)		XINT 0/GPA0_0/ALV_TCK_WAKE_EXT_INT0_0 XINT 1/GPA0_1/ALV_TDC_WAKE_EXT_INT0_1 XINT 2/GPA0_2/ALV_TDI_WAKE_EXT_INT0_2 XINT 3/GPA0_3/ALV_TDO_WAKE_EXT_INT0_3 XINT 4/GPA0_4/ALV_DBG_0_WAKE_EXT_INT0_4 XINT 5/GPA0_5/ALV_DBG_1_WAKE_EXT_INT0_5 XINT 6/GPA0_6/ALV_DBG_2_WAKE_EXT_INT0_6 XINT 7/GPA0_7/ALV_DBG_3_WAKE_EXT_INT0_7 XINT 8/GPA1_0/ALV_DBG_4_WAKE_EXT_INT0_8 XINT 9/GPA1_1/ALV_DBG_5_WAKE_EXT_INT0_9 XINT 10/GPA1_2/ALV_DBG_6_WAKE_EXT_INT1_0 XINT 11/GPA1_3/ALV_DBG_7_WAKE_EXT_INT1_1 XINT 12/GPA1_4/ALV_DBG_8_WAKE_EXT_INT1_2 XINT 13/GPA1_5/ALV_DBG_9_WAKE_EXT_INT1_3 XINT 14/GPA1_6/ALV_DBG_10_WAKE_EXT_INT1_4 XINT 15/GPA1_7/ALV_DBG_11_WAKE_EXT_INT1_5 XINT 16/GPA2_0/ALV_DBG_12_WAKE_EXT_INT2_0 XINT 17/GPA2_1/ALV_DBG_13_WAKE_EXT_INT2_1 XINT 18/GPA2_2/ALV_DBG_14_WAKE_EXT_INT2_2 XINT 19/GPA2_3/ALV_DBG_15_WAKE_EXT_INT2_3 XINT 20/GPA2_4/ALV_DBG_16_WAKE_EXT_INT2_4 XINT 21/GPA2_5/ALV_DBG_17_WAKE_EXT_INT2_5 XINT 22/GPA2_6/ALV_DBG_18_WAKE_EXT_INT2_6 XINT 23/GPA2_7/ALV_DBG_19_WAKE_EXT_INT2_7 XINT 24/GPA3_0/HDMI_CEC_ALV_DBG_20_WAKE XINT 25/GPA3_1/HDMI_CEC_ALV_DBG_21_WAKE XINT 26/GPA3_2/PCIE_WRRGIN_ALV_DBG_22 XINT 27/GPA3_3/PCIE_WRRGIN_ALV_DBG_23 XINT 28/GPA3_4/ALV_DBG_24_WAKE_EXT_INT3_0 XINT 29/GPA3_5/ALV_DBG_25_WAKE_EXT_INT3_1 XINT 30/GPA3_6/ALV_DBG_26_WAKE_EXT_INT3_2 XINT 31/GPA3_7/ALV_DBG_27_WAKE_EXT_INT3_3	
GPIO VDDQ_FERIO (1.8V)		XGPIO_0/GPB0_0/SCALL_CSD_ALD_EXT_INT0_4 XGPIO_1/GPB0_0/SSRAM_NWE/EXT_INT0_5 XGPIO_2/GPB1_0/SSRAM_NWE/TRACEDATA_0 XGPIO_3/GPB0_2/SSRAM_WWE_0/EXT_INT0_5 XGPIO_4/GPB0_3/SSRAM_NWE_1/EXT_INT0_5 XGPIO_5/GPB0_4/SSRAM_CNS_0/EXT_INT0_5 XGPIO_6/GPB1_0/CTIOUOT_GPIO_CSSYS_ATLAS XGPIO_8/GPB1_2/SSRAM_DATA_2/TRACEDATA_19 XGPIO_9/GPB1_3/SSRAM_DATA_3/TRACEDATA_20 XGPIO_10/GPB1_4/SSRAM_DATA_4/TRACEDATA_21 XGPIO_11/GPB1_5/SSRAM_DATA_5/TRACEDATA_22 XGPIO_12/GPB1_6/SSRAM_DATA_6/TRACEDATA_23 XGPIO_13/GPB1_7/SSRAM_DATA_7/TRACEDATA_24 XGPIO_14/GPB2_0/SPI_5_CLS/SSRAM_DATA_8/8TR XGPIO_15/GPB2_1/SPI_5_NSS/SSRAM_DATA_9/9TR XGPIO_16/GPB2_2/SPI_5_MOSI/SSRAM_DATA_10 XGPIO_17/GPB2_3/SPI_5_MOSI/SSRAM_DATA_11 XGPIO_18/GPB3_0/SSRAM_DATA_12/TRACEDATA_2 XGPIO_19/GPB3_1/SSRAM_DATA_13/TRACEDATA_2 XGPIO_20/GPB3_2/SSRAM_DATA_14/TRACEDATA_3 XGPIO_21/GPB3_3/SSRAM_DATA_15/TRACEDATA_3 XGPIO_22/GPB3_4/SSRAM_NWAIT/TRACEDATA_4 XGPIO_23/GPB4_5/SSRAM_ADDR_5/TRACEDATA_6 XGPIO_24/GPB4_6/SSRAM_ADDR_6/TRACEDATA_6 XGPIO_25/GPB4_7/SSRAM_ADDR_7/TRACEDATA_6 XGPIO_26/GPB5_5/SSRAM_ADDR_13/TRACEDATA_1 XGPIO_27/GPB5_6/SSRAM_ADDR_14/TRACEDATA_1 XGPIO_28/GPB5_7/SSRAM_ADDR_15/TRACEDATA_1 XGPIO_29/GPB0_0/TE_DECON_EXT_EXT_INT0_3 XGPIO_30/GPB2_0/PCIE_WRRGIN_EXT_INT2_0 XGPIO_31/GPB2_1/PCIE_WRRGIN_EXT_INT2_1 XGPIO_32/GPB2_2/EXT_INT0_2 XGPIO_33/GPB1_2/EXT_INT0_4 XGPIO_34/GPB1_4/EXT_INT0_4 XPCIEW_WARE/GP2_2/EXT_INT2_2 XPCIEW_WARE/GP2_3/EXT_INT2_3	
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
VDDQ_GPIORSV (1.8V)	VDDQ_GPIORSV (1.8V)		
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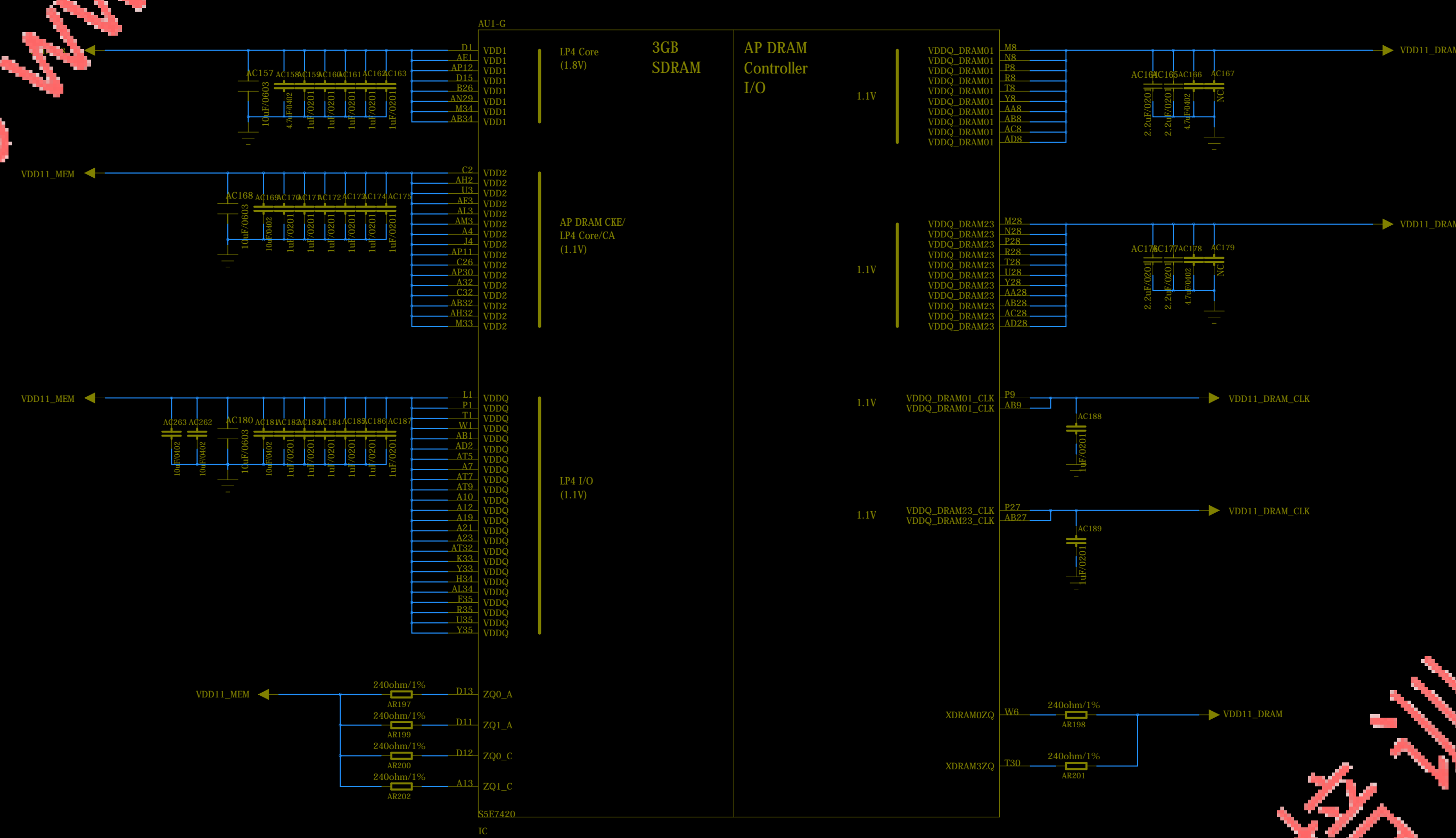
ISPT_MIRC [28]	ISPT_MIRC [28]
INT_MIRC [29]	INT_MIRC [29]
CHG2_INT [12]	CHG2_INT [12]
CHG1_INT [12]	CHG1_INT [12]
CP2AP_STATUS [22]	CP2AP_STATUS [22]
PUBAC_INT [22]	PUBAC_INT [22]
CP2AP_WAKEUP [22]	CP2AP_WAKEUP [22]
KEY_VOL+ [12,15,26,27]	KEY_VOL+ [12,15,26,27]
FLASH_INT [19]	FLASH_INT [19]
TSP_ATTEN [19]	TSP_ATTEN [19]
KEY_VOL- [12,15,26,27]	KEY_VOL- [12,15,26,27]
MSDC_INT0 [22]	MSDC_INT0 [22]
INT_ACTIVE_N [22]	INT_ACTIVE_N [22]
INT_CCD [29]	INT_CCD [29]
WCU_INT [13]	WCU_INT [13]
INT_APA [13]	INT_APA [13]
NFC_IRQ [18]	NFC_IRQ [18]
INT_PP [28]	INT_PP [28]
WLAN_WAKE_AP [18]	WLAN_WAKE_AP [18]
KEY_OFF [11]	KEY_OFF [11]
INT_LIGHT [28]	INT_LIGHT [28]
BT_HOST_WAKE [10]	BT_HOST_WAKE [10]
GPS_HOST_WAKE [17]	GPS_HOST_WAKE [17]
INT_PMC [11]	INT_PMC [11]
NFC_SPI_IRQ [18]	NFC_SPI_IRQ [18]
KEY_ADGE [29]	KEY_ADGE [29]
PCIEW_WRRGIN_WIFI [10]	PCIEW_WRRGIN_WIFI [10]
INT_PP [19,20]	INT_PP [19,20]
PCIE_CLKREQ_WIFI [7,10]	PCIE_CLKREQ_WIFI [7,10]
AJUNT [13]	AJUNT [13]
DATA_OUTPUT [13]	DATA_OUTPUT [13]

BUCKEN [11]	BUCKEN [11]
SLEEP_MCU [15]	SLEEP_MCU [15]
DAC_RESET [14]	DAC_RESET [14]
LOW_GAIN [14]	LOW_GAIN [14]
MOTOR_EN [20]	MOTOR_EN [20]
MIPI [14]	MIPI [14]
HHH/9998 [11]	HHH/9998 [11]
LED_SDA [20]	LED_SDA [20]
LED_SCL [20]	LED_SCL [20]
MCLR_EN [14]	MCLR_EN [14]
DIVIDER_TP_EN [22]	DIVIDER_TP_EN [22]
MCLR_EN1 [14]	MCLR_EN1 [14]
RESETN_PA [13]	RESETN_PA [13]
WKUP_MCU [15]	WKUP_MCU [15]
MCU_BUSY [15]	MCU_BUSY [15]
RESETNC [13]	RESETNC [13]
WL_REG_ON [16]	WL_REG_ON [16]
TP_IRQ_ON [16]	TP_IRQ_ON [16]
CP_PMIC_PWR_EN [21]	CP_PMIC_PWR_EN [21]
PCIE_WRRGIN_WIFI [14]	PCIE_WRRGIN_WIFI [14]
ADEN [13]	ADEN [13]
GPSPOWERON [13]	GPSPOWERON [13]
RESET_PP [13]	RESET_PP [13]
SMPL_WARR [10]	SMPL_WARR [10]
PMIC_BUCK0_DVS [10]	PMIC_BUCK0_DVS [10]
AP_RST_CT [21]	AP_RST_CT [21]
RCSTN_SF [21]	RCSTN_SF [21]
BT_WAKE [10]	BT_WAKE [10]
BT_WAKE [14]	BT_WAKE [14]
GP2_IRQ [14]	GP2_IRQ [14]
LED_EN [20]	LED_EN [20]

COMPANY: Meizu	
TITLE: M86	
DRAWN: Evan	DATED: 2015.02.03
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>
CODE: A1<Drawing Number> V1.0	
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REV: 27	
SCALE: <Scale>	
SHEET: 6 of 27	

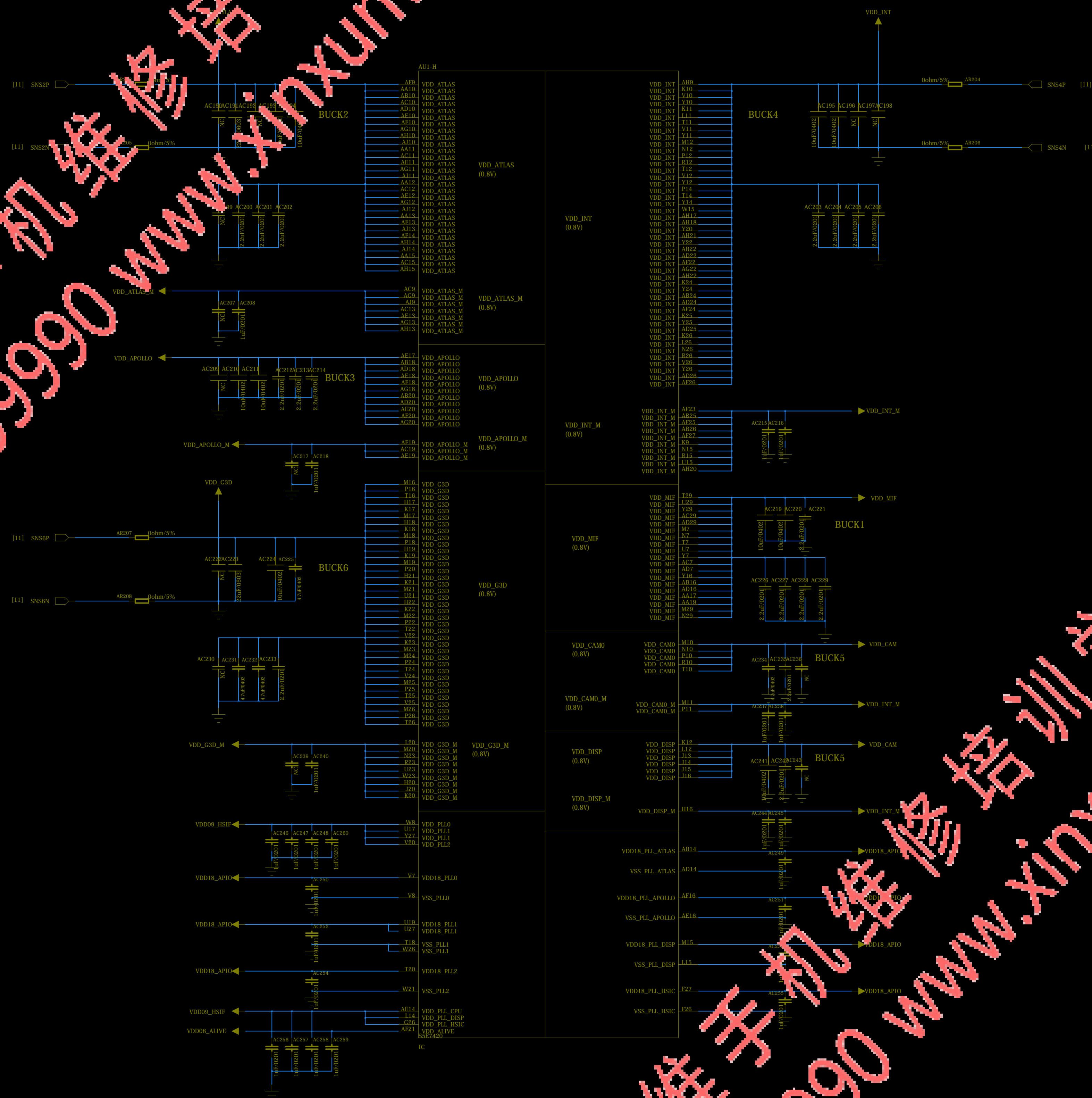


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:





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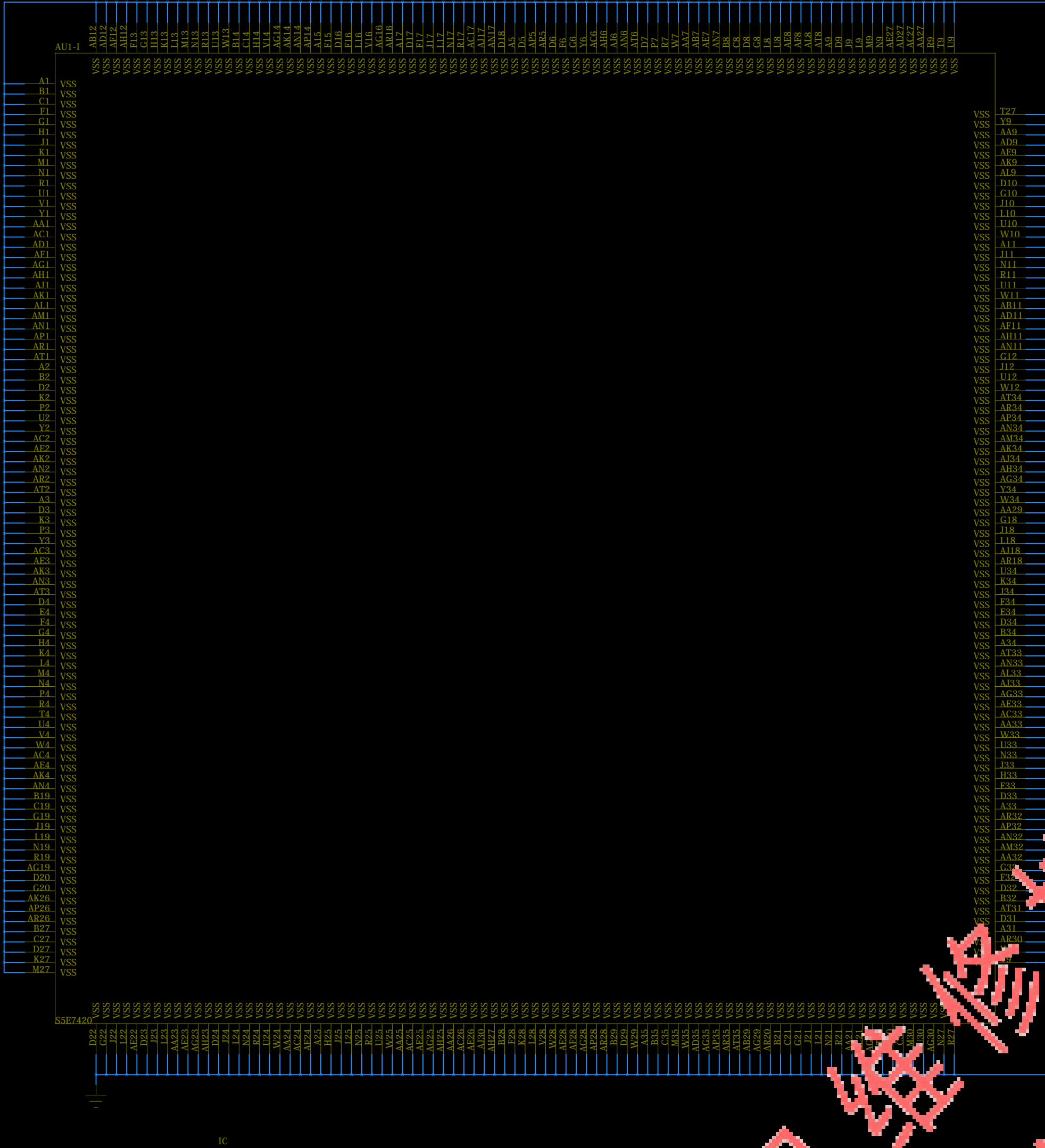
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1

REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE



		COMPANY: Meizu				
		TITLE:				
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DRAWN: Evan	DATED: 2015.02.03					
CHECKED: <Checked By>	DATED: <Checked Date>	CODE:	SIZE:	DRAWING NO:	REV:	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	<Code>	A1	<Drawing Number>	V1.0	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: 10 27	



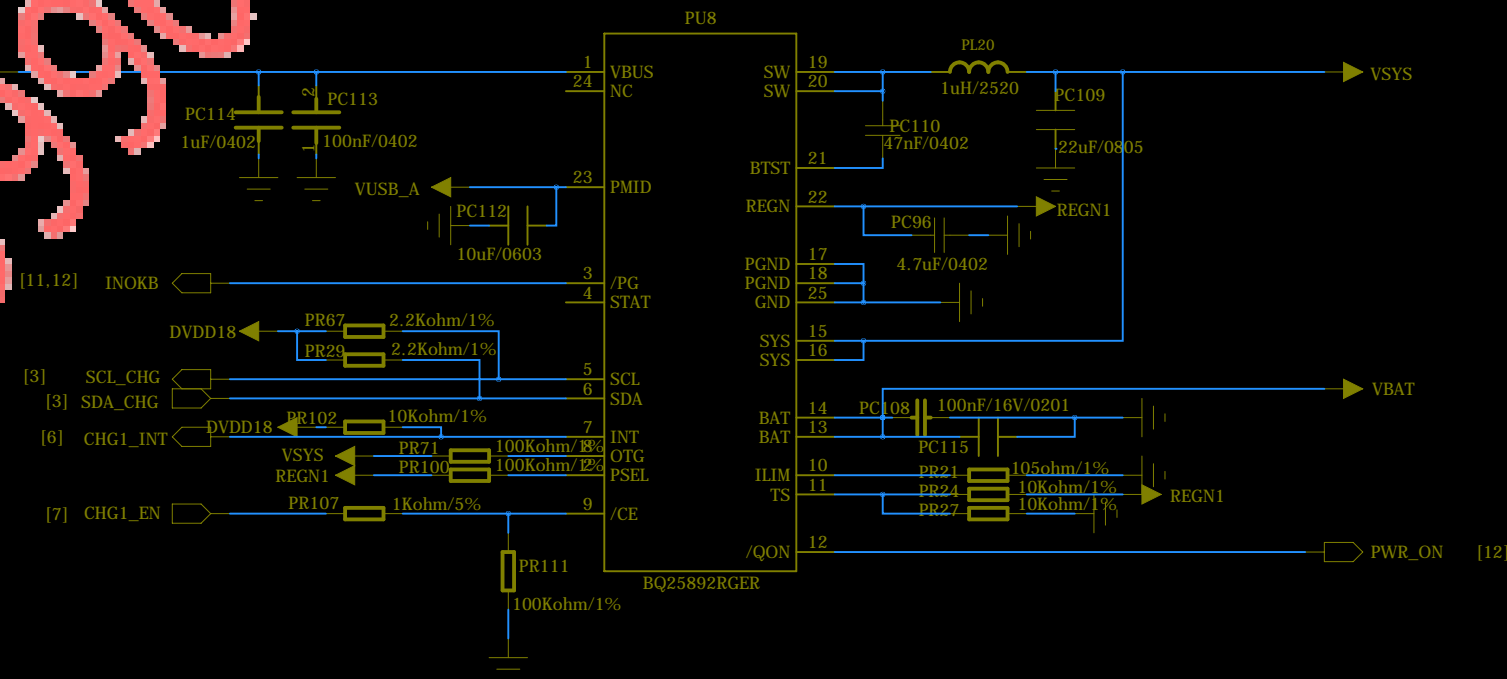
COMPANY:		Meizu	
TITLE:			
M86			
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SCALE: <Scale>		SHEET: 1 of 27	

DRAWN: Evan	DATED: 2015.02.03
CHECKED: <Checked By>	DATED: <Checked Date>
QUALITY CONTROL: <QC By>	DATED: <QC Date>
RELEASED: <Released By>	DATED: <Release Date>

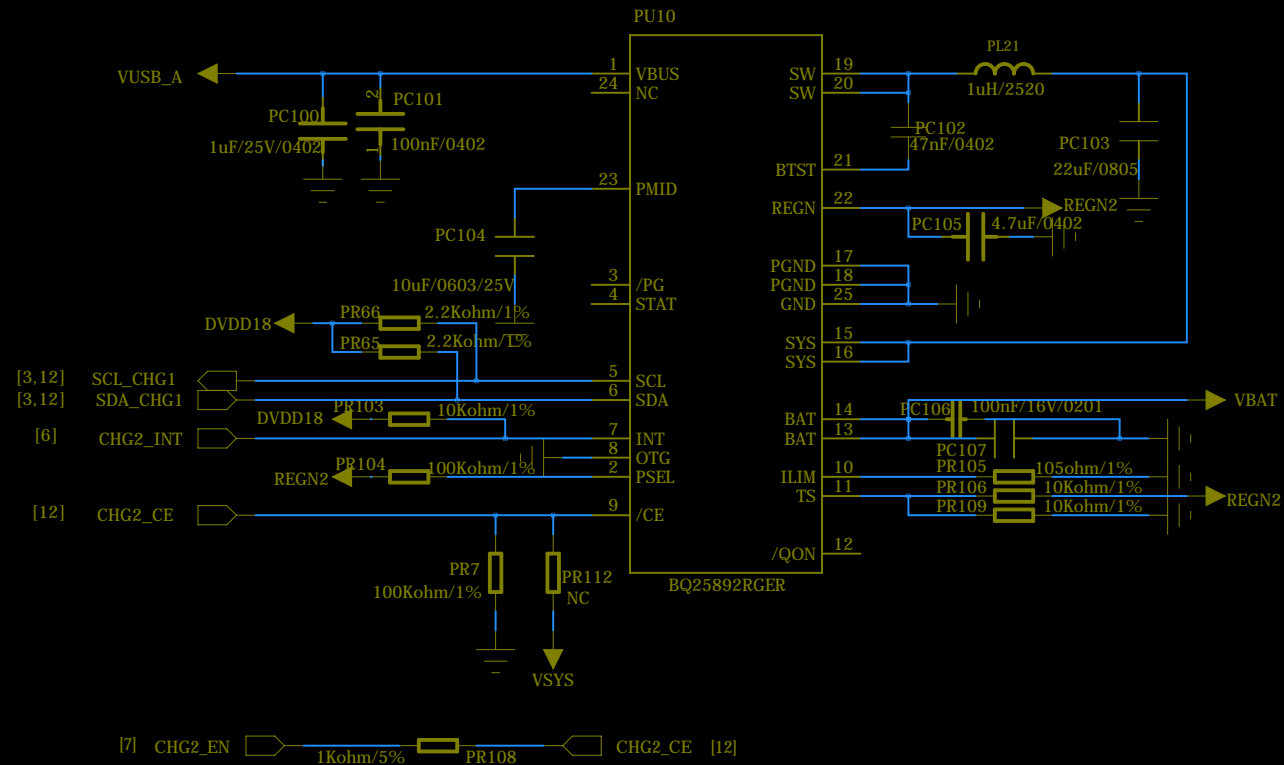


REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE

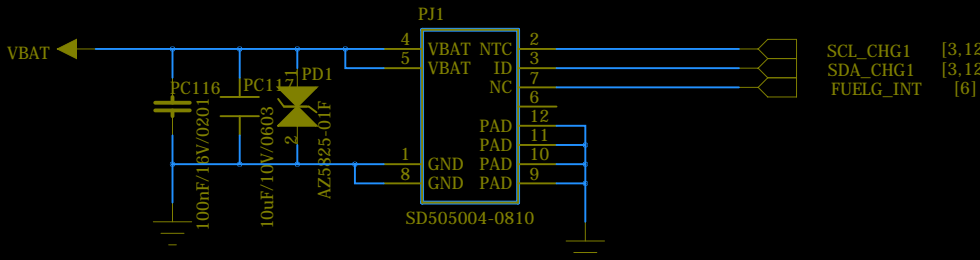
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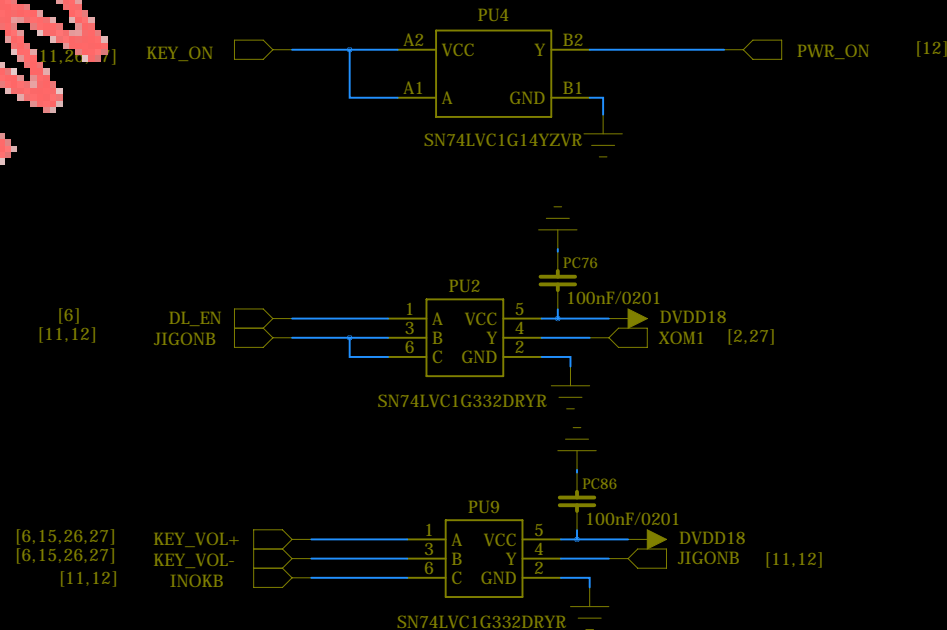
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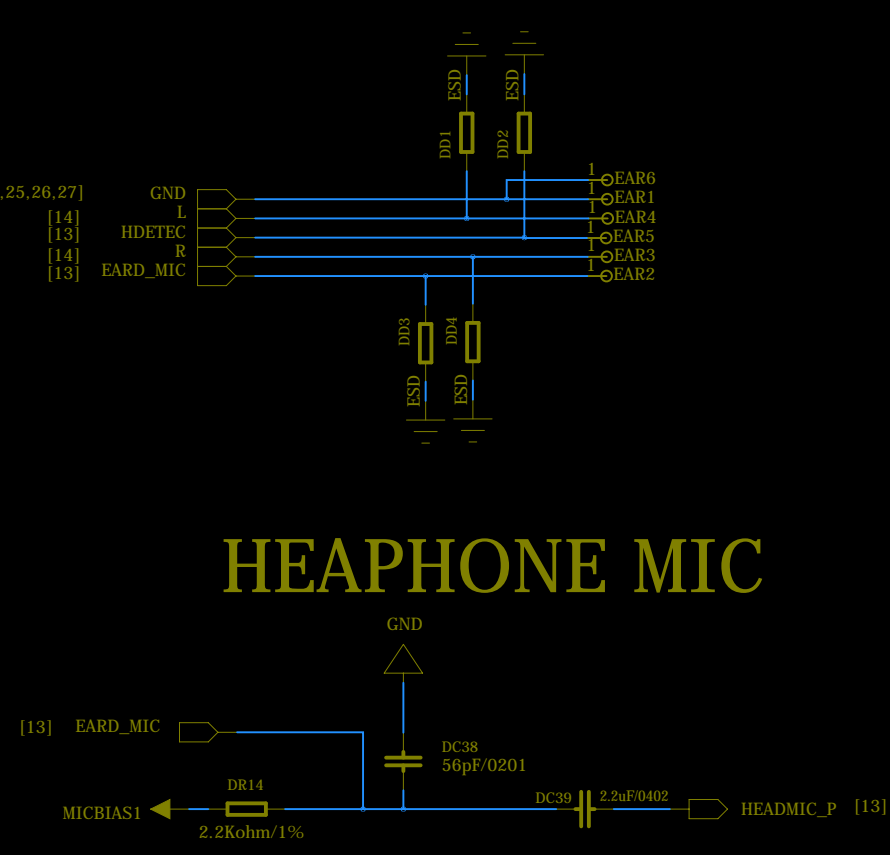
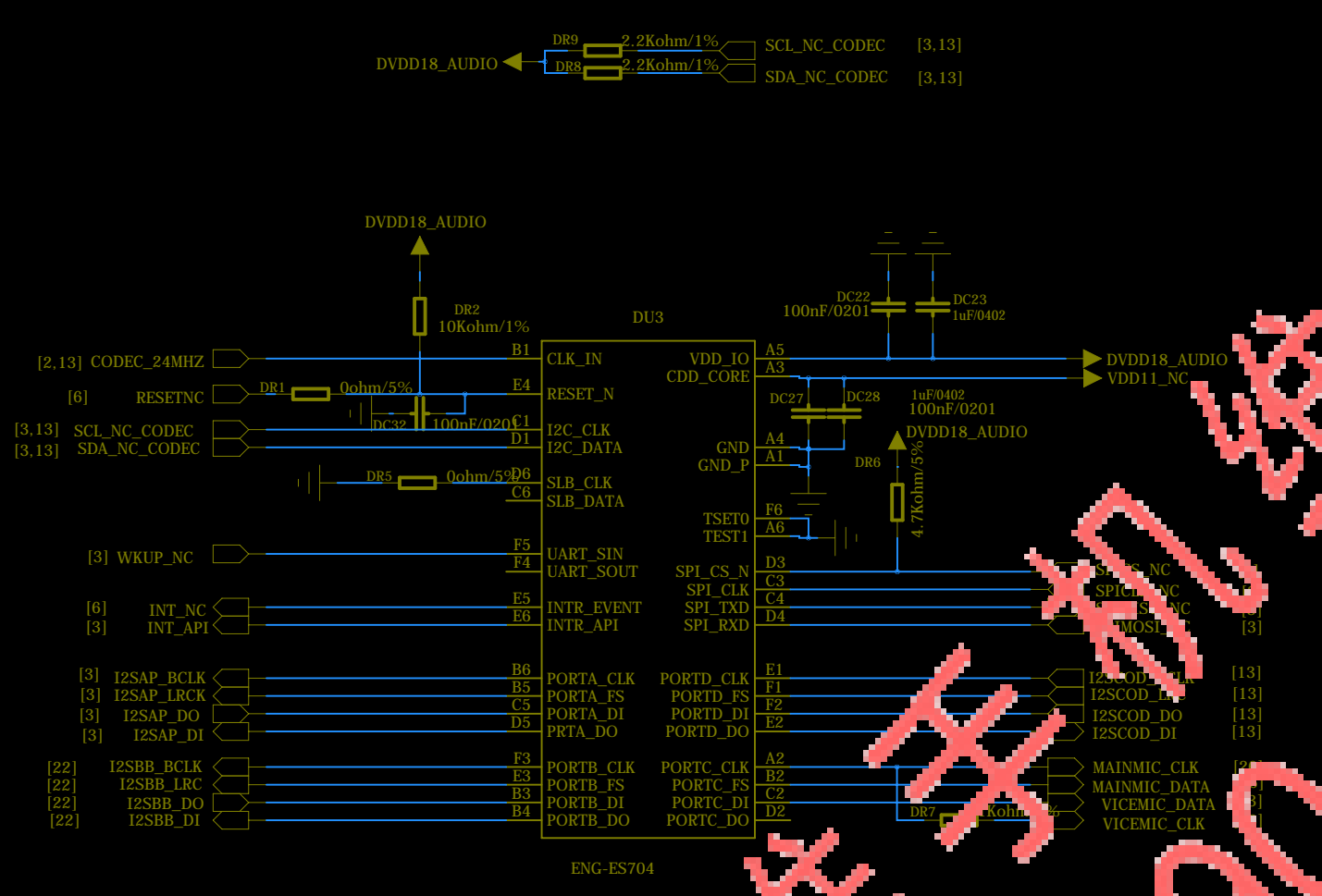
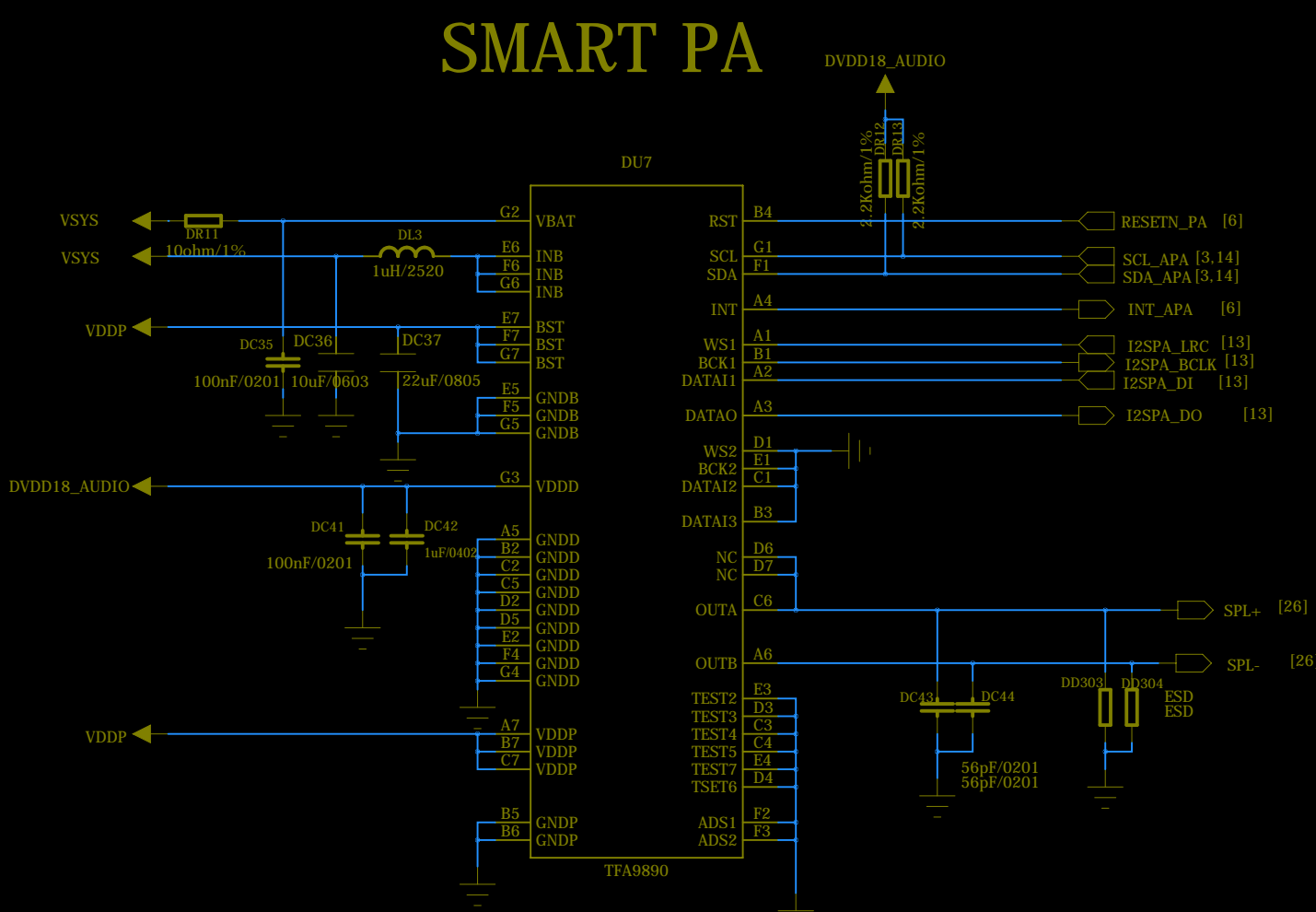
BAT CONNECTOR



DRAWN: Evan		DATED: 2015.02.03		COMPANY: Meizu	
CHECKED: <Checked By>		DATED: <Checked Date>		TITLE: M86	
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: <Code>	SIZE: A1
RELEASED: <Released By>		DATED: <Release Date>		DRAWING NO: <Drawing Number>	
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				SHEET: 12 27	



REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

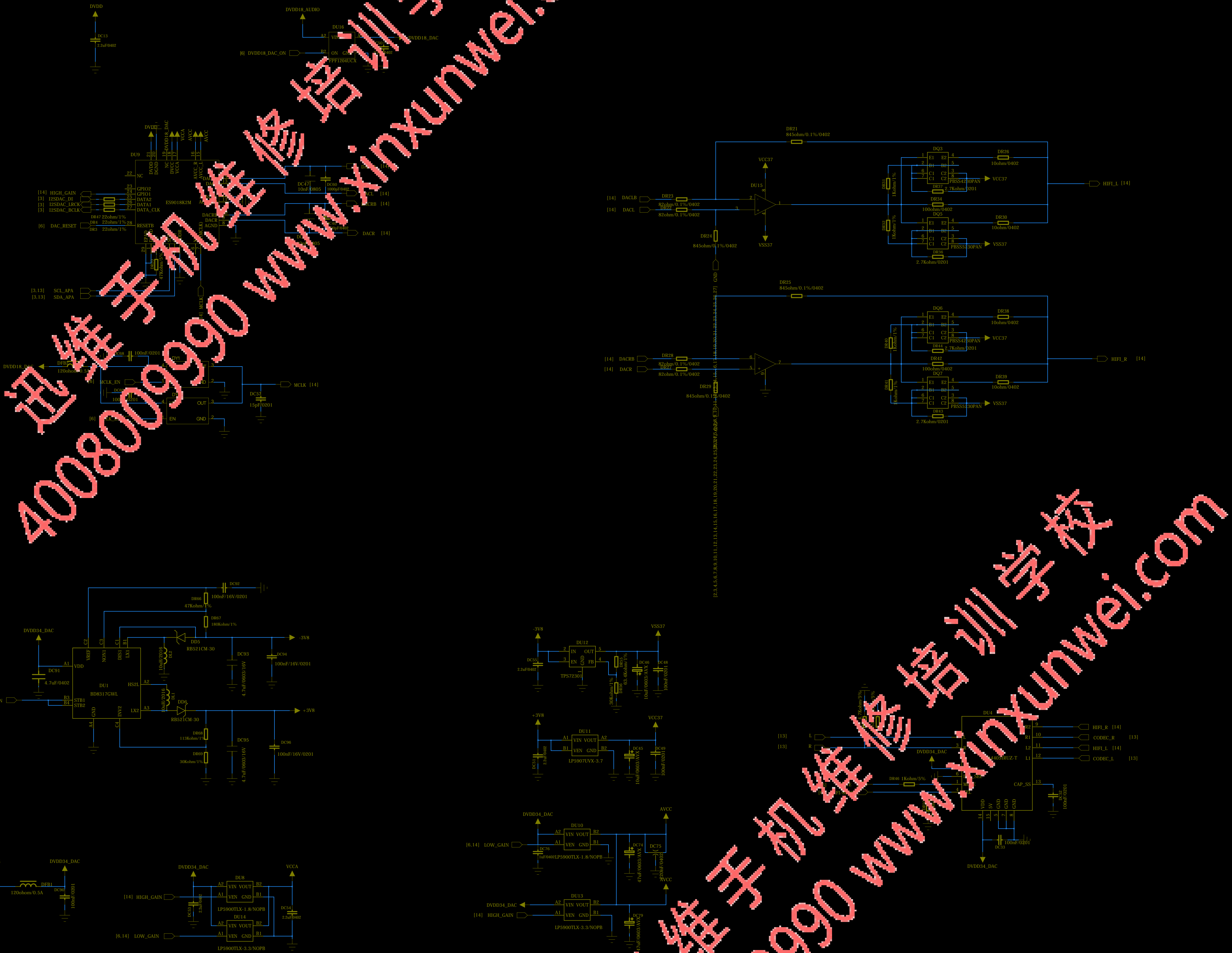


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QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE:		A1	DRAWING Number		V1.0
RELEASED: <Released By>		DATED: <Release Date>		CODE:		A1	DRAWING Number		V1.0

COMPANY: Meizu	
TITLE: M86	
SHEET: 13 27	



REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE



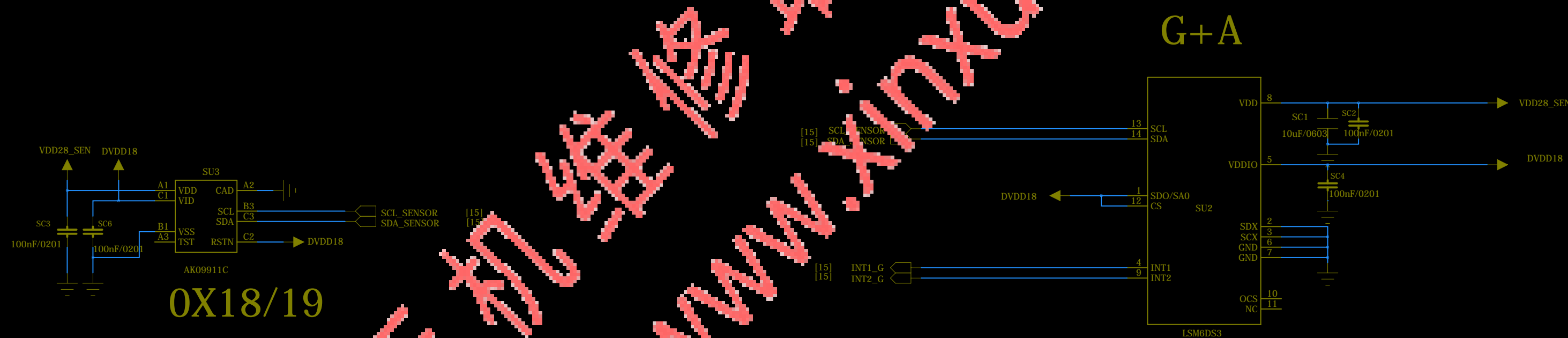
COMPANY: Meizu			
TITLE: M86			
CODE	SIZE	DRAWING NO.	REV.
<Code>	A1	<Drawing Number>	V1.0
SCALE: <Scale>		SHEET: 1 of 27	

DRAWN: Evan	DATED: 2015.02.03
CHECKED: <Checked By>	DATED: <Checked Date>
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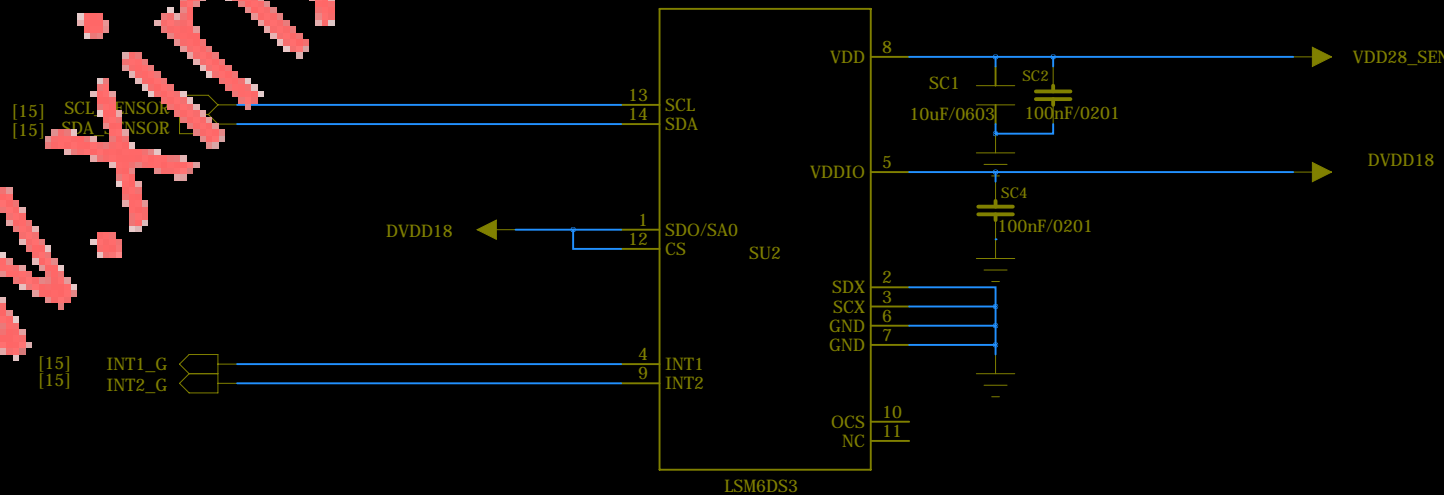


REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE

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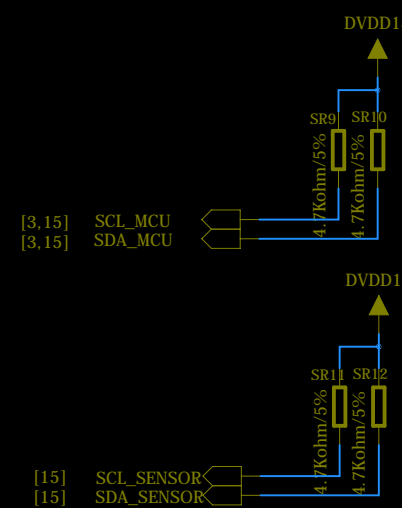
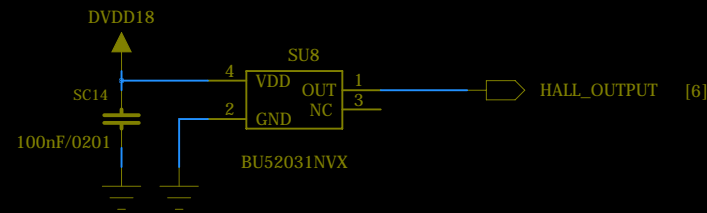


G+A



0X18/19

HALL



COMPANY: Meizu

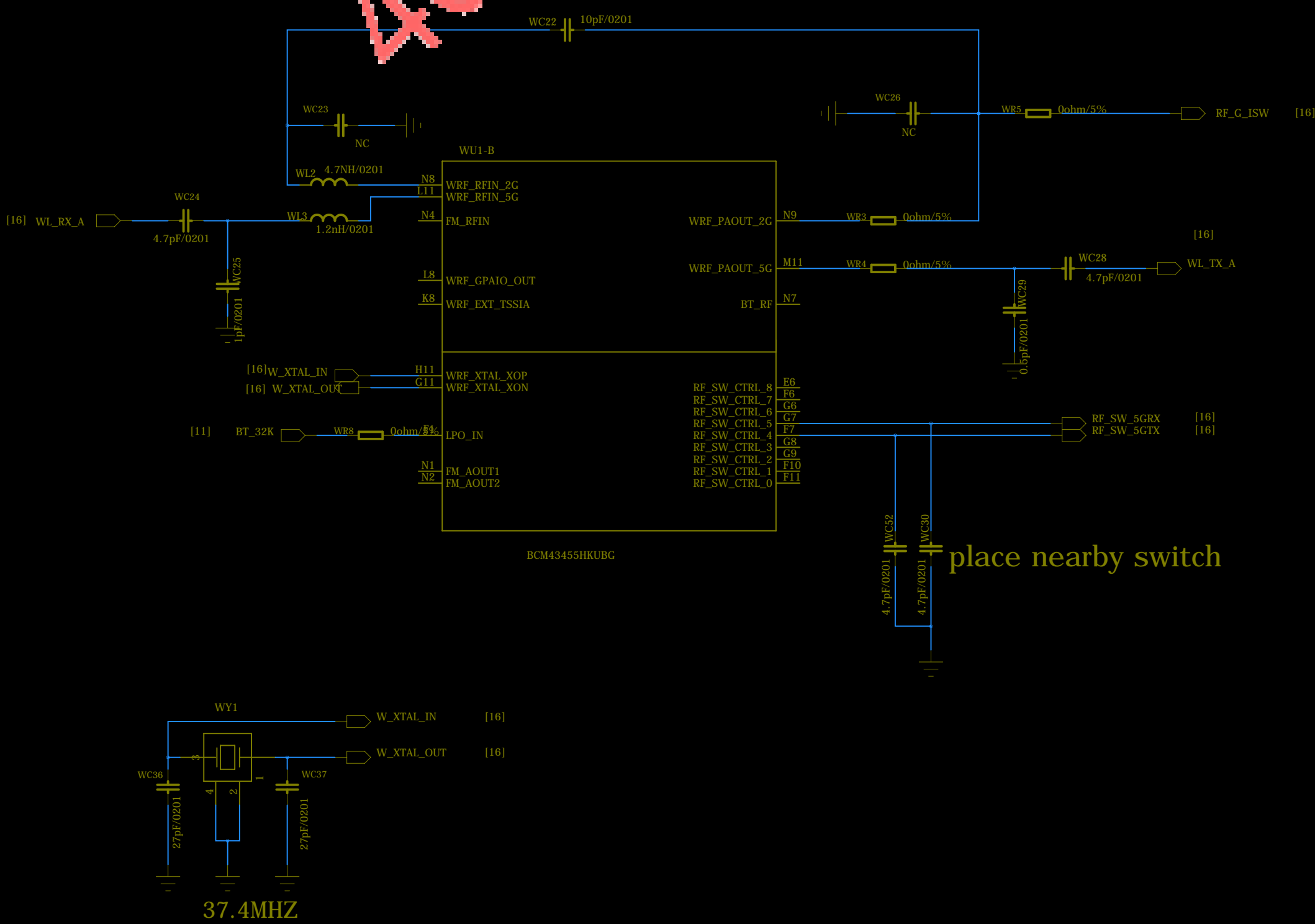
TITLE: M86

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QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>			SHEET: 15 27

TX, RX



2GWIFI RXTX switch intergated



POWER

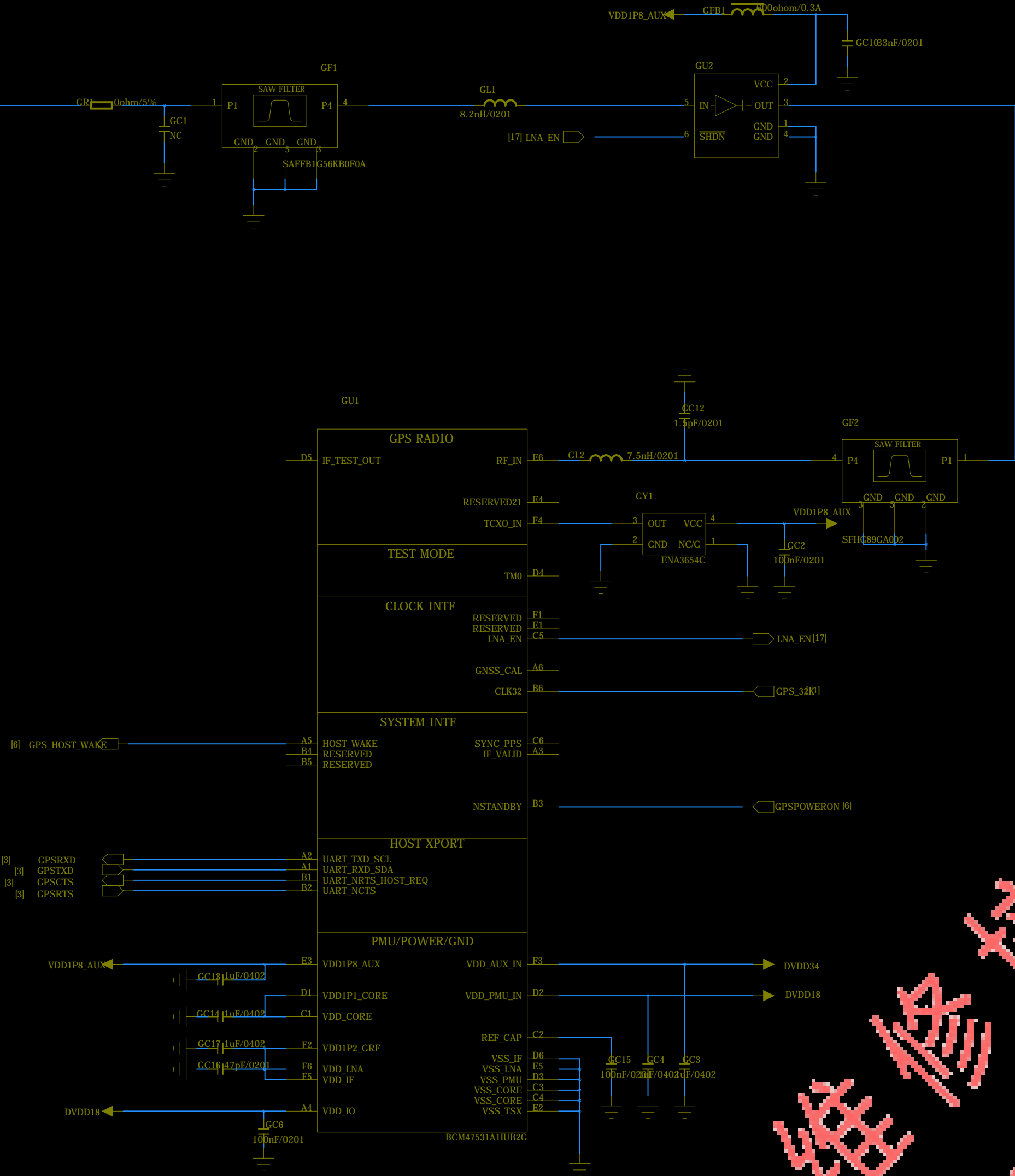


REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE

COMPANY:		Meizu					
TITLE:		M86					
CODE:		SIZE:		DRAWING NO:		REV:	
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LTR	ECO NO.	APPROVED:	DATE:



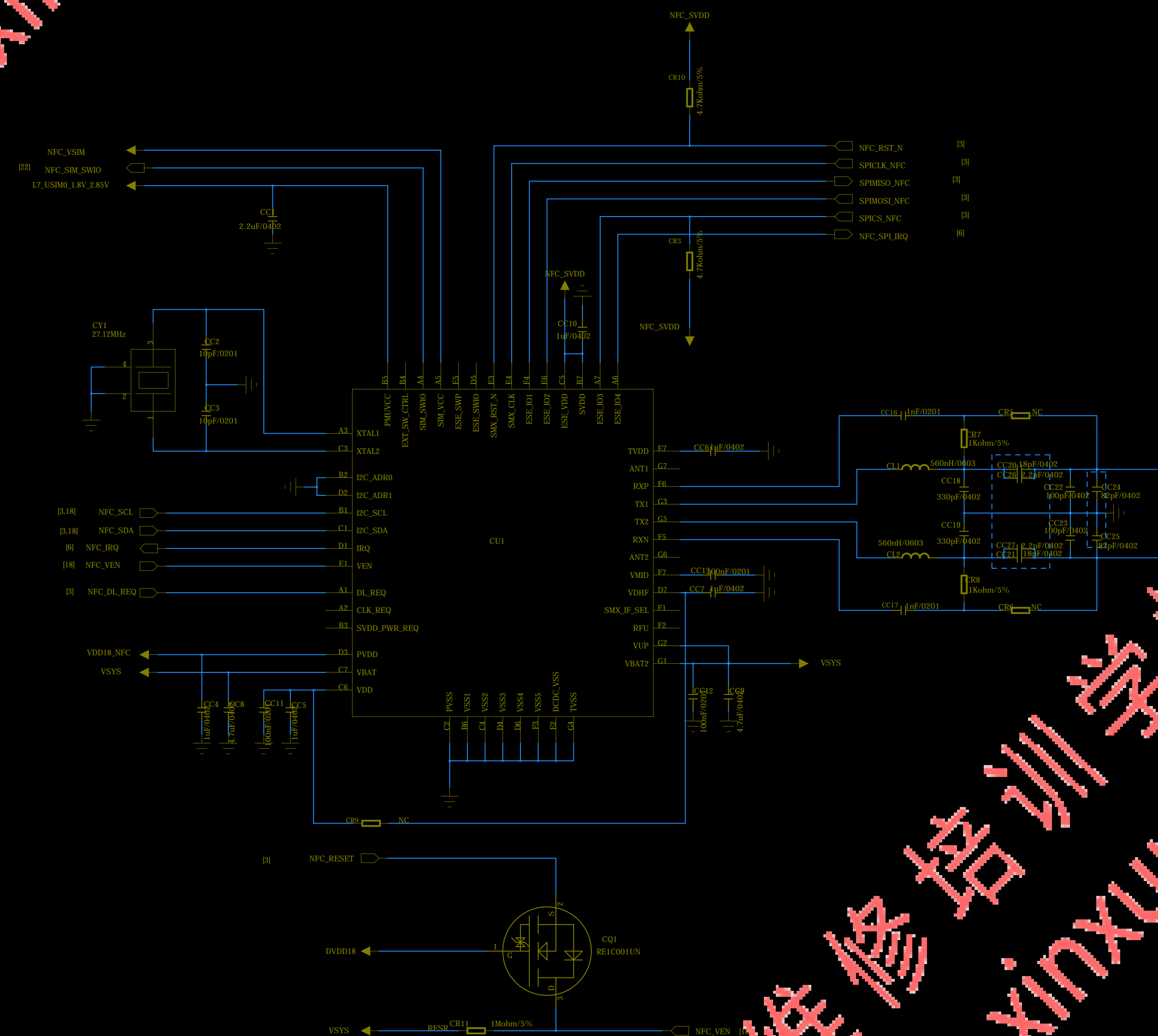
The 1uF capacitors used need to have a voltage rating of 6.3V at least to make sure the capacitance stays above minimum required (0.5uF) after taking into account variations due to initial tolerance, temperature and DC voltage derating.

COMPANY: Meizu

TITLE: M86

DRAWN: Evan	DATED: 2015.02.03	CODE:	SIZE:	DRAWING NO.:	REV:
CHECKED: <Checked By>	DATED: <Checked Date>	<Code>	A1	<Drawing Number>	V1.0
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LTR	ECO NO:	APPROVED:	DATE:





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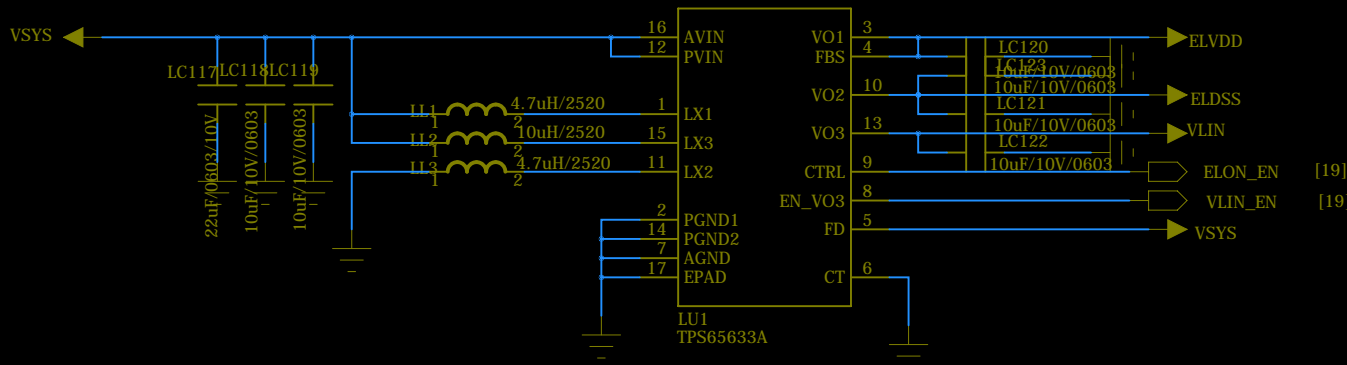
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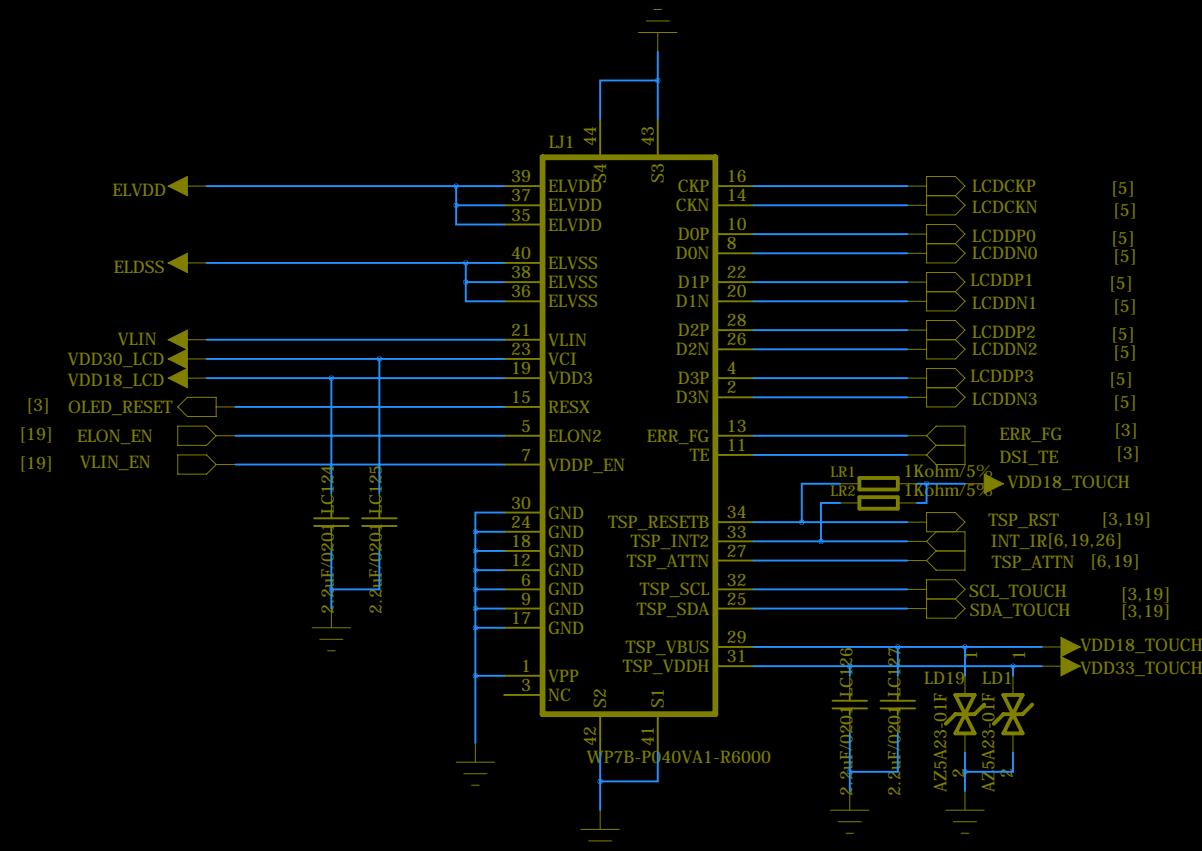
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UTR	ECO NO.	APPROVED	DATE

AMOLED DRIVER



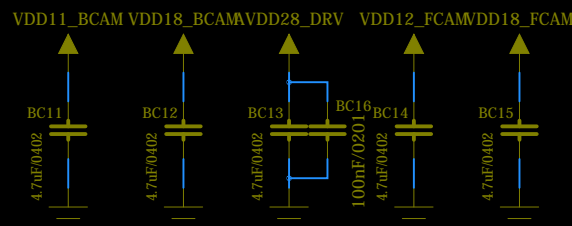
AMOLED CONNECTOR



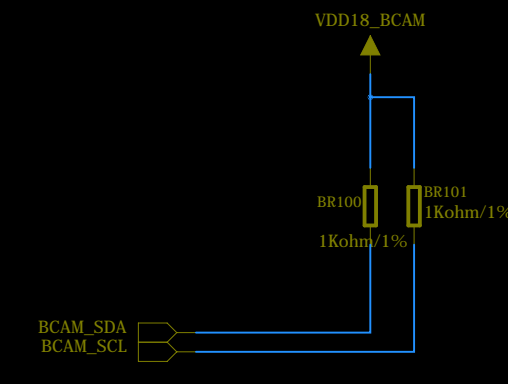
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TITLE: M86			
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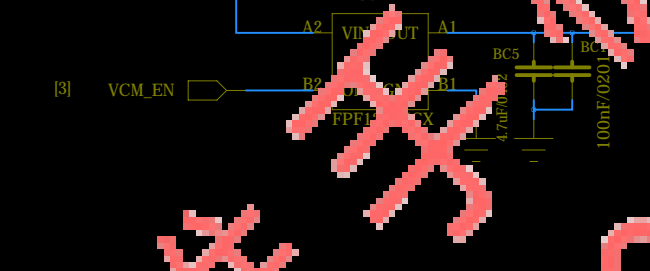
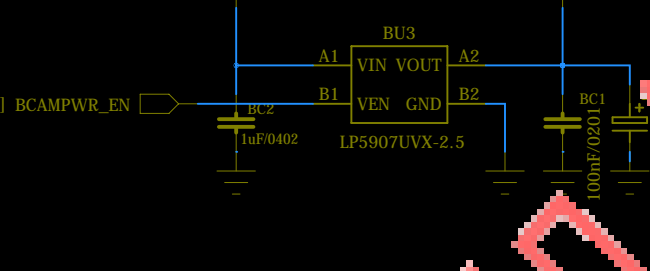
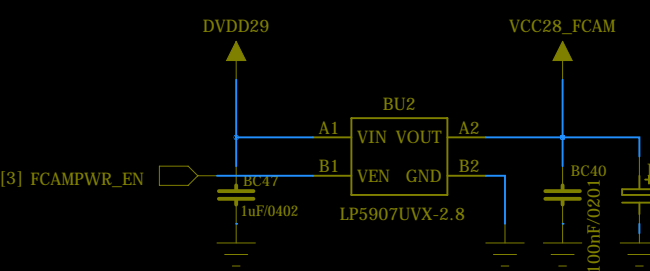
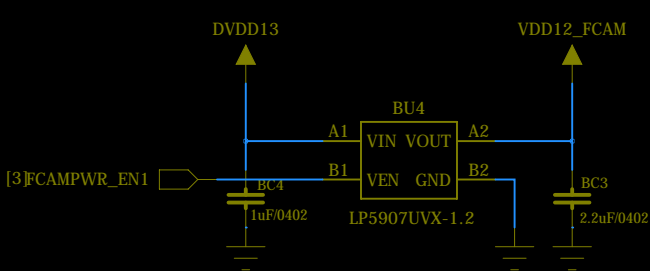
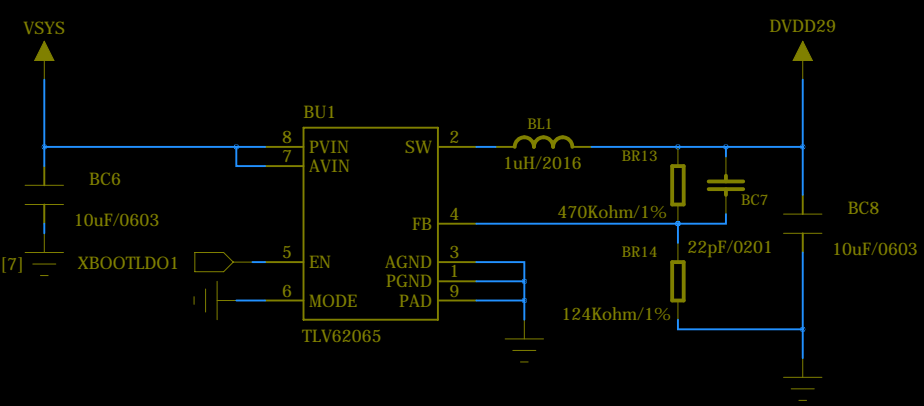
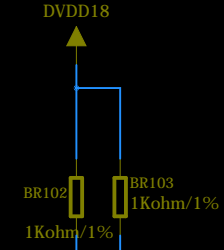
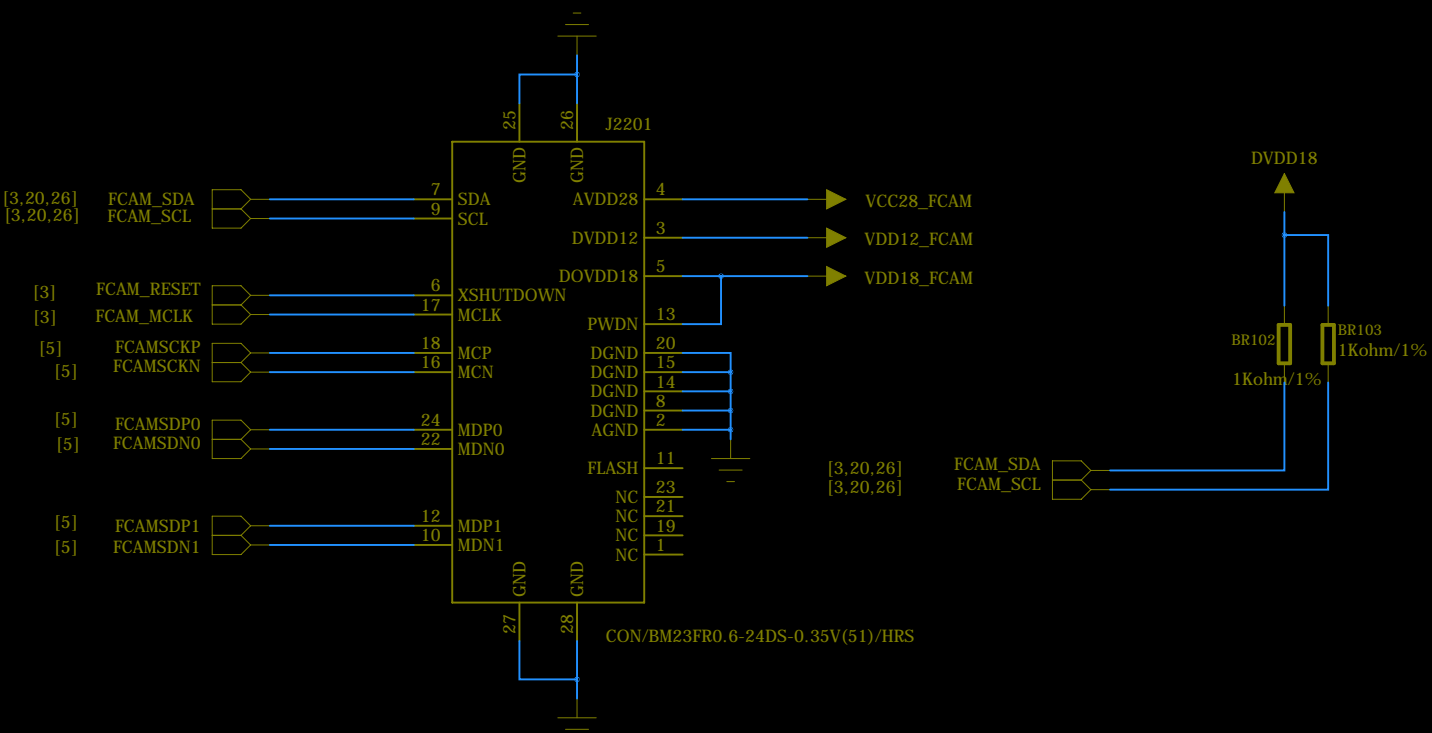
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UTE	ECO NO.	APPROVED	DATE



Main Camera

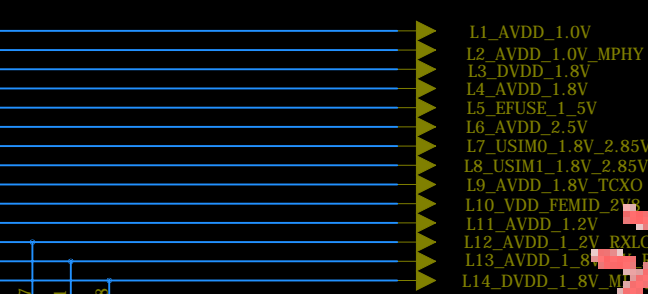


Sub Camera



COMPANY: Meizu			
TITLE: M86			
DRAWN: Evan	DATED: 2015.02.03	CODE: A1	
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QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: V1.0	
RELEASED: <Released By>	DATED: <Release Date>	SHEET 20 27	

debug test point



LDO:1,3,11,13:300mA
LDO :others 150mA



COMPANY:		Meizu	
TITLE:		M86	
CODE:	SIZE:	DRAWING NO:	REV:
<Code>	A1	<Drawing Number>	V1.0
SCALE: <Scale>	SHEET: 22		27



REVISION RECORD			
UTR	RCO NO.	APPROVED	DATE



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TITLE:		M86				
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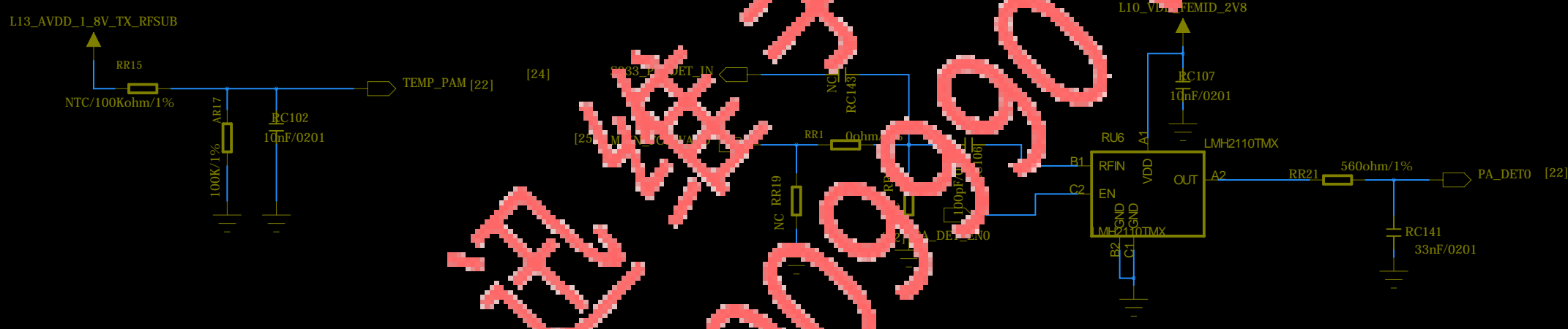
1

The image displays a detailed PCB layout for a mobile phone. The central component is the CPU, surrounded by various peripheral components including memory modules, power management ICs, and connectors. The layout is color-coded, with different colors representing different functional blocks or components. The components are labeled with their respective part numbers and values, such as resistors (R1, R2, etc.), capacitors (C1, C2, etc.), and integrated circuits (IC1, IC2, etc.). The layout also shows the placement of various connectors, including the USB port, ear jack, and antenna connectors. The overall design is compact and efficient, typical of modern mobile phone PCBs.

The schematic diagram illustrates the internal structure of the MM3B PA block, which is a multi-band power amplifier. The central component is the MM3B PA block, which is connected to various external components and pins. The diagram shows the following components and connections:

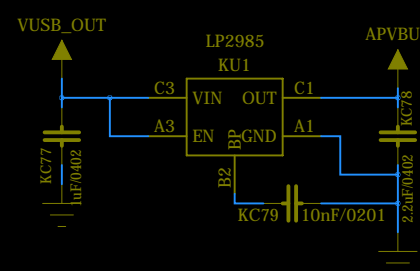
- MM3B PA Block:** The central component, which is a multi-band power amplifier. It has several pins: B7, B40, B38, B41, and B39.
- Resistors (RC):** Various resistors are used for biasing and matching. Examples include RC94, RC97, RC98, RC99, RC100, RC101, RC102, RC103, RC104, RC105, RC106, RC107, RC108, RC109, RC110, RC111, RC112, RC113, RC114, RC115, RC116, RC117, RC118, RC119, RC120, RC121, RC122, RC123, RC124, RC125, RC126, RC127, RC128, RC129, RC130, RC131, RC132, RC133, RC134, RC135, RC136, RC137, RC138, RC139, RC140, RC141, RC142, RC143, RC144, RC145, RC146, RC147, RC148, RC149, RC150, RC151, RC152, RC153, RC154, RC155, RC156, RC157, RC158, RC159, RC160, RC161, RC162, RC163, RC164, RC165, RC166, RC167, RC168, RC169, RC170, RC171, RC172, RC173, RC174, RC175, RC176, RC177, RC178, RC179, RC180, RC181, RC182, RC183, RC184, RC185, RC186, RC187, RC188, RC189, RC190, RC191, RC192, RC193, RC194, RC195, RC196, RC197, RC198, RC199, RC200, RC201, RC202, RC203, RC204, RC205, RC206, RC207, RC208, RC209, RC210, RC211, RC212, RC213, RC214, RC215, RC216, RC217, RC218, RC219, RC220, RC221, RC222, RC223, RC224, RC225, RC226, RC227, RC228, RC229, RC230, RC231, RC232, RC233, RC234, RC235, RC236, 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PAM is changed with risky SKY77646-51(capable PA) due to 3G ACL problem.
Reference Matching value should be checked with PAM vendor

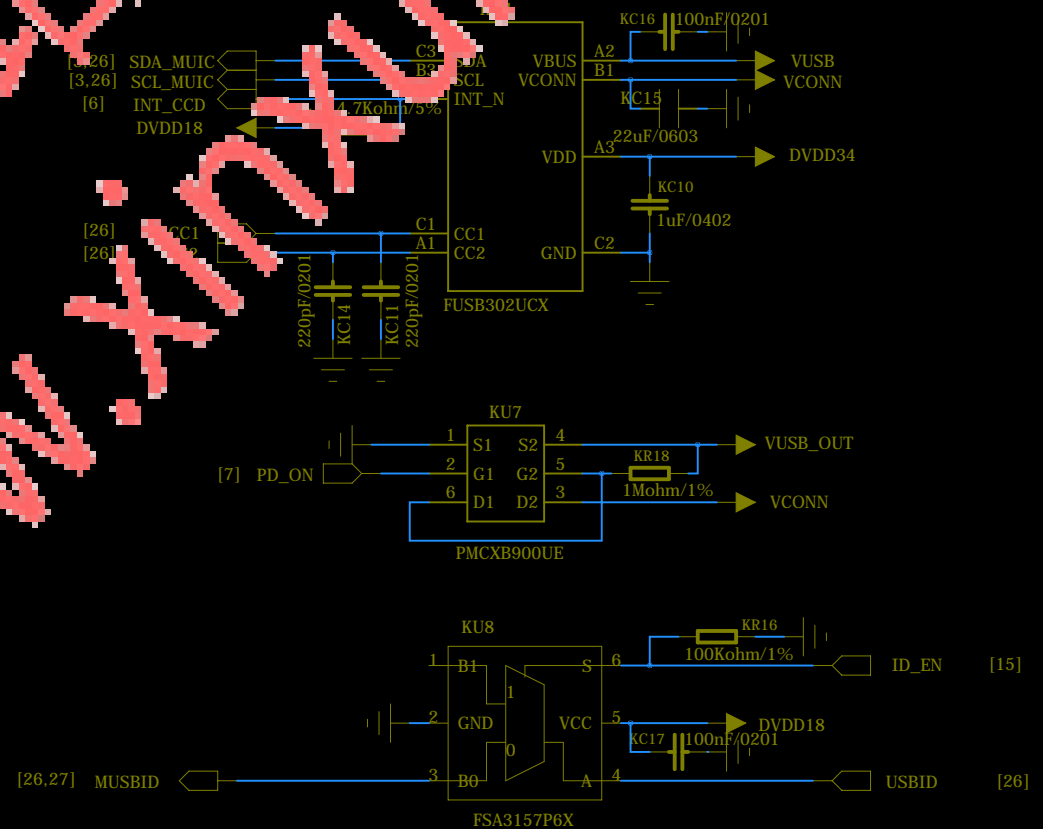


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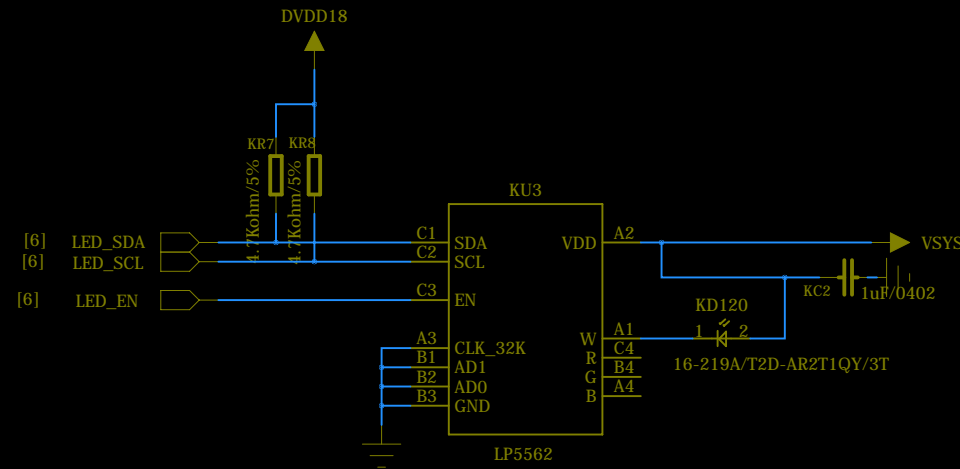
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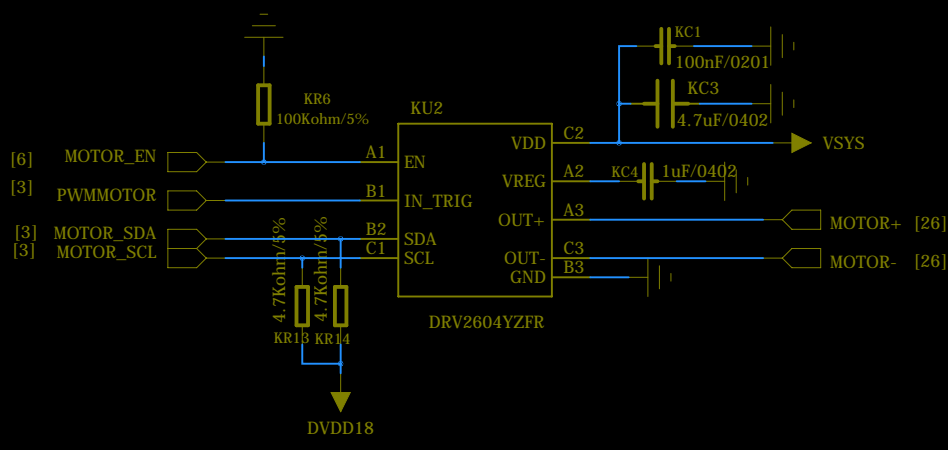
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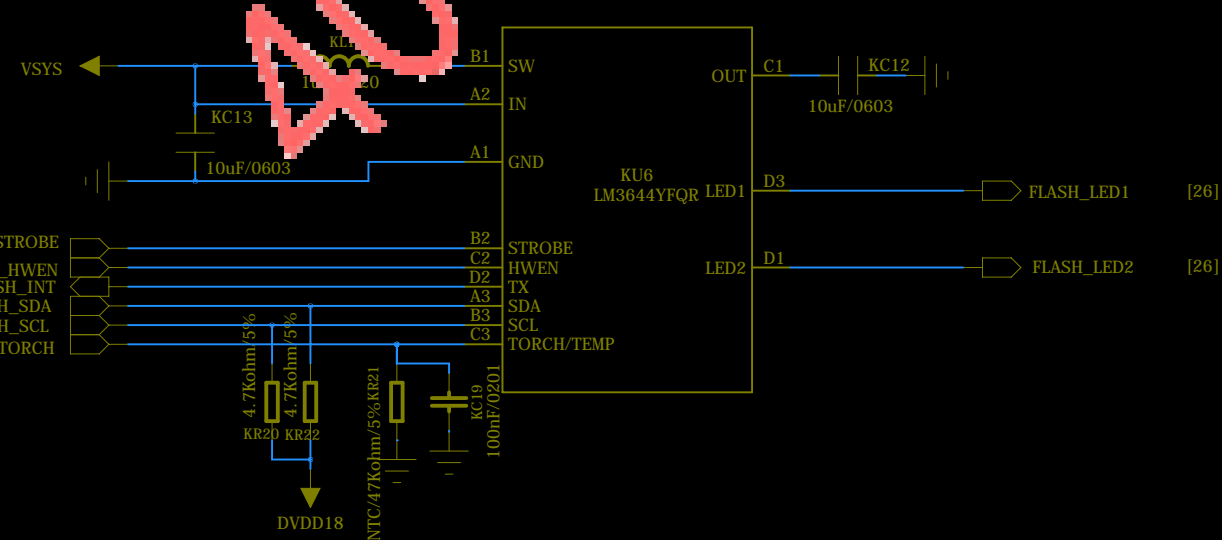
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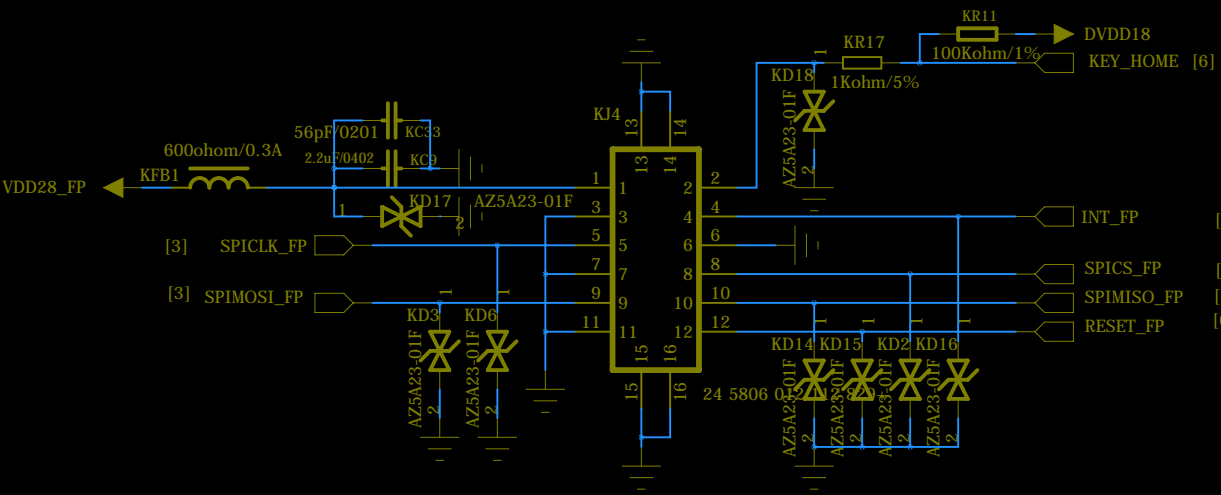
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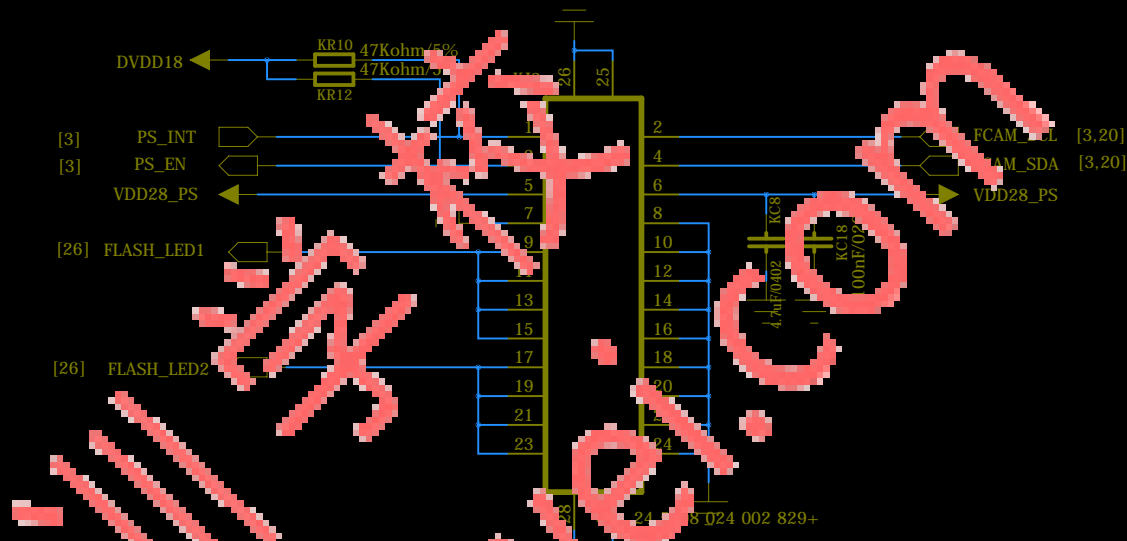
FLASH LED



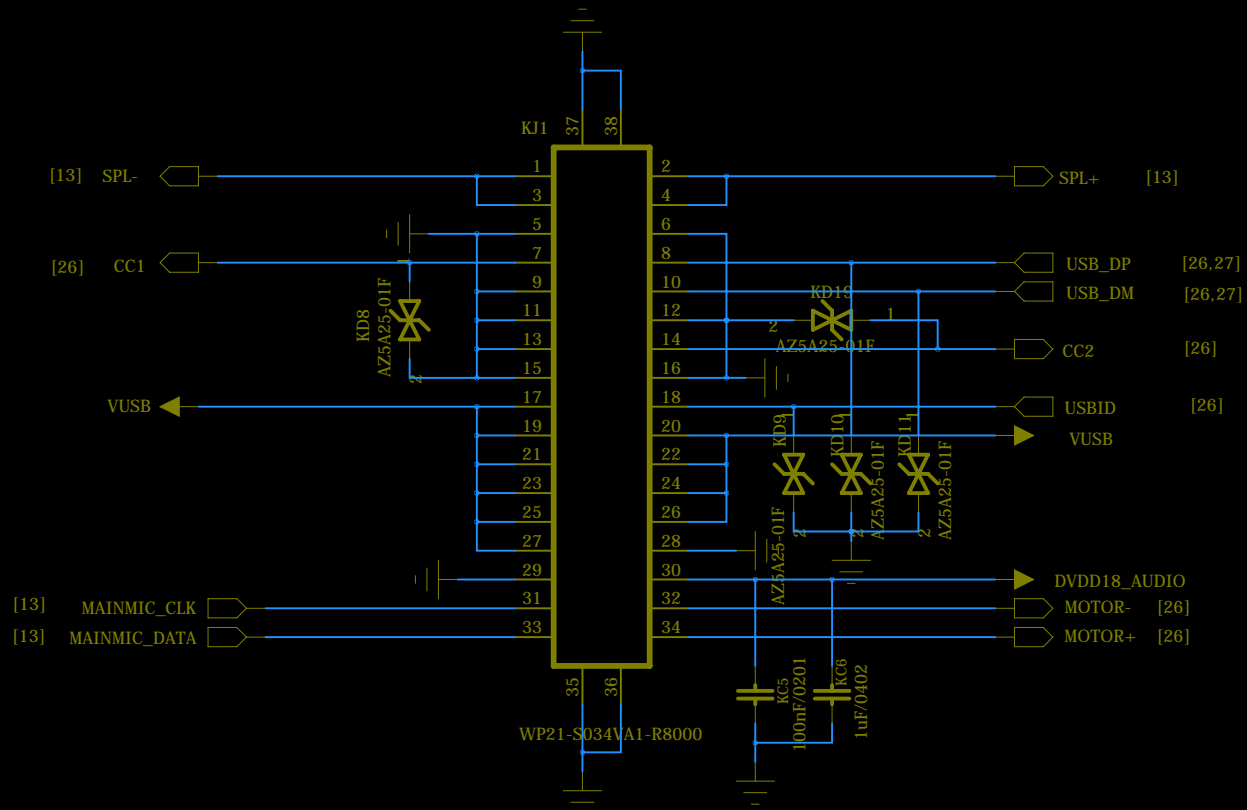
FP&HOME KEY CONNECTOR



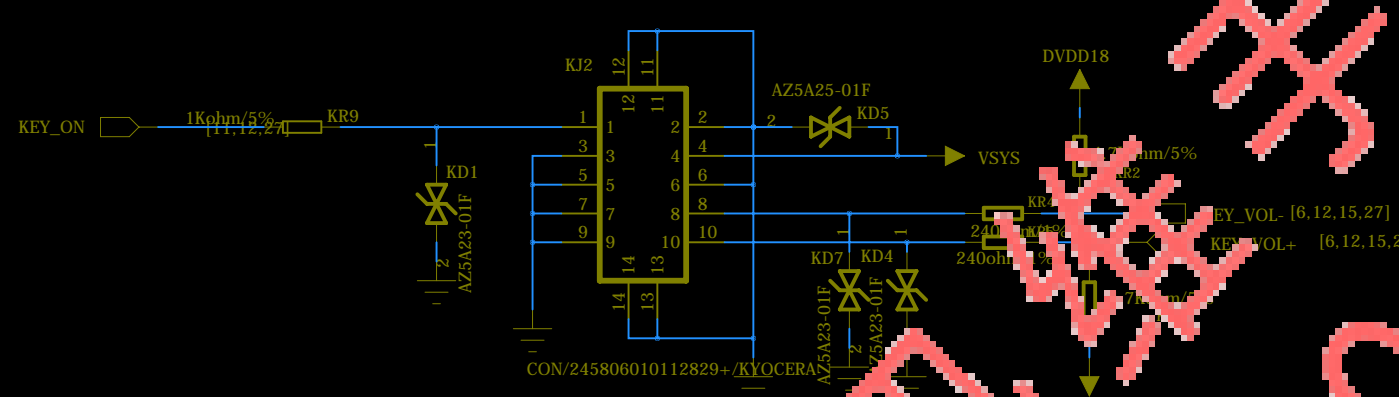
FLASH&PS CONNECTOR



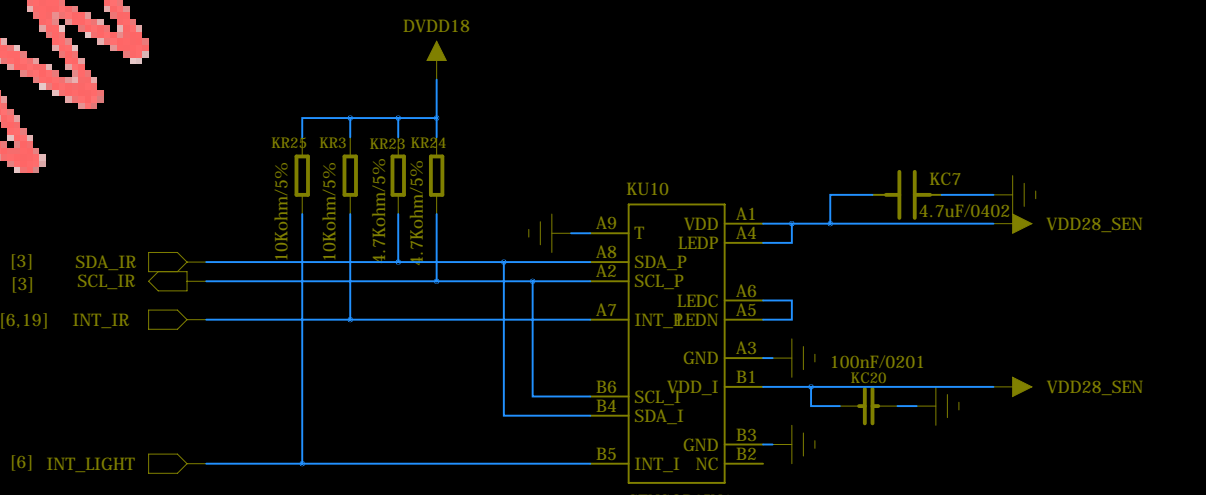
USB CONNECTOR



PWR&VOL KEY CONNECTOR



ALS&PS



COMPANY:		Meizu			
TITLE:		M86			
DRAWN:	Evan	DATED:	2015.02.03		
CHECKED:	<Checked By>	DATED:	<Checked Date>	CODE	SIZE
QUALITY CONTROL	<QC By>	DATED:	<QC Date>	<Code>	A1<Drawing Number> V1.0
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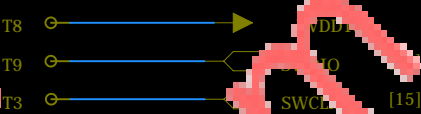
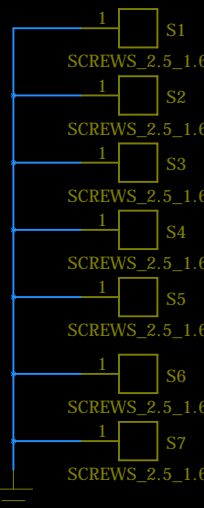
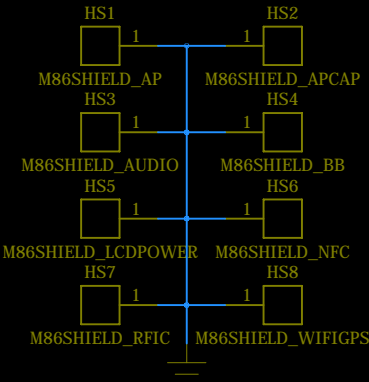
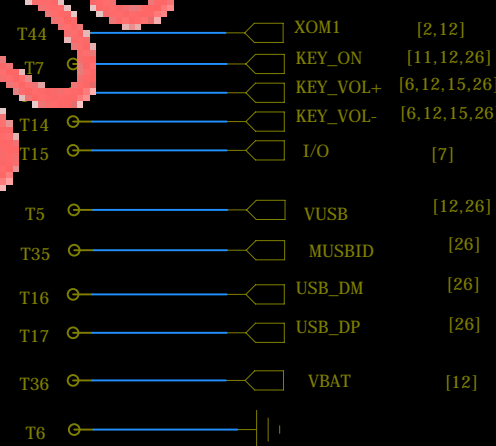
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REVISION RECORD			
UTR	ECO NO.	APPROVED	DATE

AP MCU LOAD TEST POINT

MCU DOWNLOAD



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TITLE: M86			
DRAWN: Evan	DATED: 2015.02.03	CODE:	SIZE:
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