1. Description

1.1. Project

Project Name	spion
Generated with:	MicroXplorer 3.1
Date	12/08/2014

1.2. MCU

MCU family	STM32F1
MCU name	STM32F103R(8-B)Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration
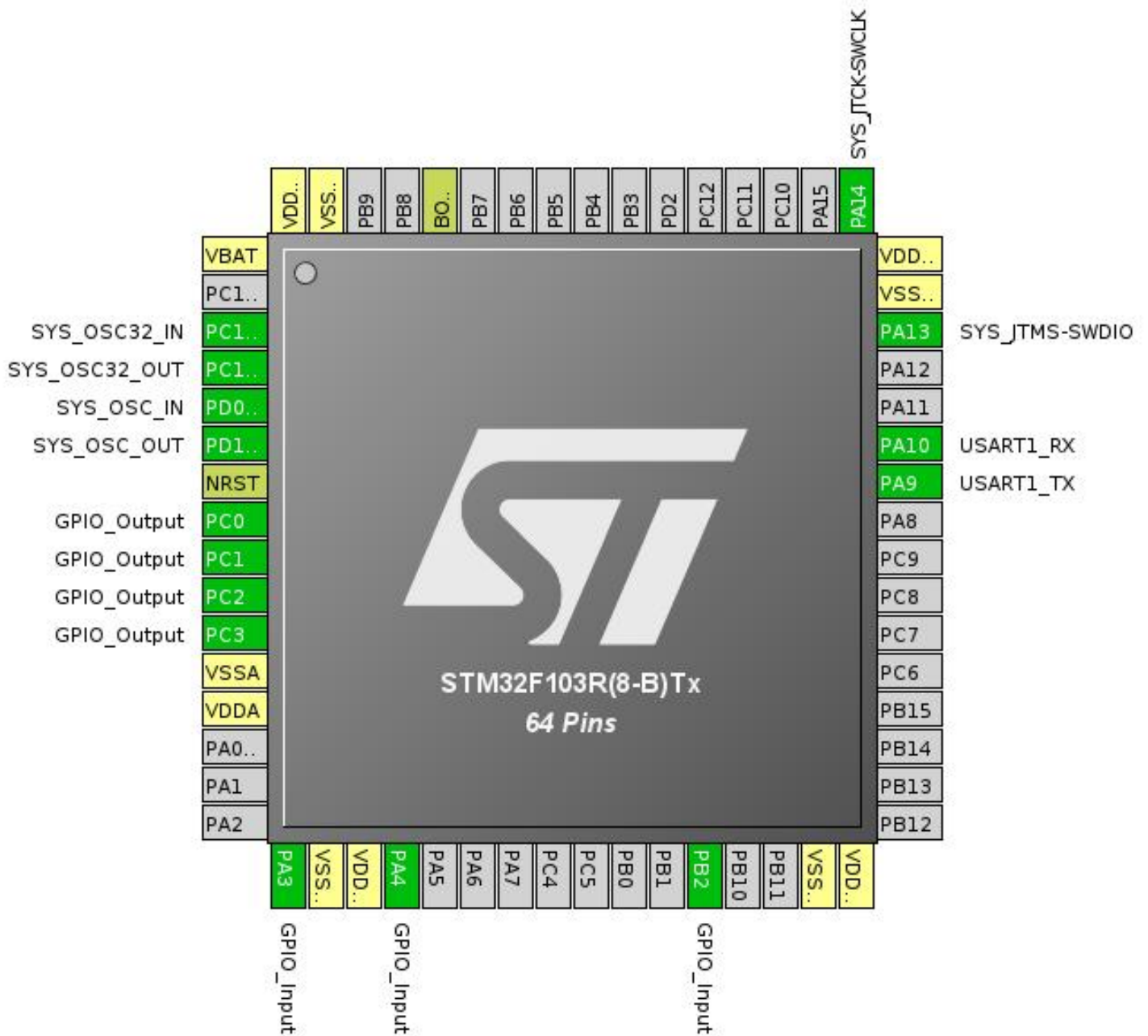
3. Peripherals Configuration

Peripheral	Mode	Remap	Fonction	Pin
SYS	Serial-Wire	0	SYS_JTCK-SWCLK	PA14
			SYS_JTMS-SWDIO	PA13
	HSE-External-Oscillator	0	SYS_OSC_IN	PD0-OSC_IN
			SYS_OSC_OUT	PD1-OSC_OUT
	LSE-External-Oscillator	0	SYS_OSC32_IN	PC14-OSC32_IN
			SYS_OSC32_OUT	PC15-OSC32_OUT
USART1	Asynchronous	0	USART1_RX	PA10
			USART1_TX	PA9

4. Pins Configuration

Pins	Pos	Functions
PC14-OSC32_IN	3	SYS_OSC32_IN
PC15-OSC32_OUT	4	SYS_OSC32_OUT
PD0-OSC_IN	5	SYS_OSC_IN
PD1-OSC_OUT	6	SYS_OSC_OUT
PC0 *	8	GPIO_Output
PC1 *	9	GPIO_Output
PC2 *	10	GPIO_Output
PC3 *	11	GPIO_Output
PA3 *	17	GPIO_Input
PA4 *	20	GPIO_Input
PB2 *	28	GPIO_Input
PA9	42	USART1_TX
PA10	43	USART1_RX
PA13	46	SYS_JTMS-SWDIO
PA14	49	SYS_JTCK-SWCLK

* The pin is affected with an I/O function