

Opcode[3]	Opcode[2]	Opcode[1]	Opcode[0]	Func[2]	Func[1]	Func[0]	Instr
0	0	0	0	0	0	0	AND
0	0	0	0	0	0	1	ADD
0	0	0	0	0	1	0	SUB
0	0	0	0	0	1	1	XOR
0	0	0	0	1	0	0	NOR
0	0	0	0	1	0	1	OR
0	0	0	1	x	x	x	ADDI
0	0	1	0	x	x	x	ANDI
0	0	1	1	x	x	x	ORI
0	1	0	0	x	x	x	NORI
0	1	0	1	x	x	x	BEQ
0	1	1	0	x	x	x	BNE
0	1	1	1	x	x	x	SLTI
1	0	0	0	x	x	x	LW
1	0	0	1	x	x	x	SW

RegDst	RegWrit	ALUSrc	ALUOP[2]	ALUOP[1]	ALUOP[0]	MemWrite	MemRead	MemToReg
1	1	0	1	1	0	0	0	0
1	1	0	0	0	0	0	0	0
1	1	0	0	1	0	0	0	0
1	1	0	0	0	1	0	0	0
1	1	0	1	0	1	0	0	0
1	1	0	1	1	1	0	0	0
0	1	1	0	0	0	0	0	0
0	1	1	1	1	0	0	0	0
0	1	1	1	1	1	0	0	0
0	1	1	1	0	1	0	0	0
0	0	1	0	1	0	0	0	0
0	0	1	0	1	0	0	0	0
0	1	1	1	0	0	0	0	0
0	1	1	0	0	0	0	1	1
0	0	1	0	0	0	1	0 x	

PCSrc

[illegible]