Operation	PC_sel	RF_sel	ALU_sel	op2_sel	we_reg	we_mem
LUI	00	010	XXX	XX	1	0
AUIPC	00	100	XXX	XX	1	0
JAL	10	011	000	10	1	0
JALR	10	011	XXX	XX	1	0
BEQ-1	01	XXX	001	11	0	0
BEQ-0	00	XXX	001	11	0	0
BNE-1	01	XXX	001	11	0	0
BNE-0	00	XXX	001	11	0	0
BLT-1	01	XXX	001	11	0	0
BLT-0	00	XXX	001	11	0	0
BGE-1	01	XXX	001	11	0	0
BGE-0	00	XXX	001	11	0	0
LB	00	001	000	00	1	0
LH	00	001	000	00	1	0
LW	00	001	000	00	1	0
LBU	00	001	000	00	1	0
LHU	00	001	000	00	1	0
SB	00	XXX	000	01	0	1
SH	00	XXX	000	01	0	1
SW	00	XXX	000	01	0	1
ADDI	00	000	000	00	1	0
SLTI-1	00	101	001	00	1	0
SLTI-0	00	110	001	00	1	0
XORI	00	00	100	00	1	0
ORI	00	00	011	00	1	0
ANDI	00	00	010	00	1	0
SLLI	00	00	101	00	1	0
SRLI	00	00	110	00	1	0
SRAI	00	00	111	00	1	0
ADD	00	00	000	11	1	0
SUB	00	00	001	11	1	0
SLL	00	00	101	11	1	0
SLT-1	00	101	001	11	1	0
SLT-0	00	110	001	11	1	0
XOR	00	00	100	11	1	0
SRL	00	00	110	11	1	0
SRA	00	00	111	11	1	0
OR	00	00	011	11	1	0
AND	00	00	010	11	1	0
Operation	PC_sel	RF_sel	ALU_sel	op2_sel	we_reg	we_mem