Project Proposal

# Team Name and Members

Team name: Team 9

Team leader: Tyler Hannis

Team member: Melissa Harkins

Team member: Vijayant “VJ” Kanwar

# Design Overview

Explain the set of instructions, clocking methodology (falling or rising edge triggered), registers file, and other specifications

## Instruction format

Example:

R-Format:

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Opcode | rs | rt | rd | Func |
| 4 bits | 3 bits | 3 bits | 3 bits | 3 bits |

I–Format: …

J- Format: …

## Instructions

Example:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Name | Mnemonic | Operation | Opcode | Func | Format |
| Add immediate | addi | addi $s1, $s2, 100; $s1 = $s2 + 100 | 1000 | 000 | I |
| … | … | … | … | … | … |

## Assembly language and machine code for the test program (Pseudocode)

Pseudocode for the test program (refer to the project handout):

while ($a1>0) do

{

……

}

|  |  |
| --- | --- |
| Assembly Language | Machine Code |
| addi $2, $3, 4 | 8FFF: 1000 0100 1100 0100 |
| … | … |

\*\* You can explain your assembly language in brief if needed.

# Tasks and Schedule

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Name | Tasks | Schedule | | | |
| 1st week | 2nd week | … | 5th week |
| Richard Brown | Instruction,  Datapath,  Control Unit,  Coding,  Report,  Web Design, etc |  |  |  |  |
| … | … | … | … | … | … |

# Planned Meetings

The design team will meet on the following dates to discuss the project progress and assigned tasks:

* Monday March 23: 2-3 pm.
* …

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