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	Math Routines	
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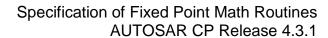
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1 Introduction and functional overview

This specification specifies the functionality, API and the configuration of the AUTOSAR library dedicated to arithmetic routines for fixed point values.

This mathematical library (MFX) contains the following routines:

- addition
- subtraction
- absolute value
- absolute value of differences
- multiplication
- division
- combination of multiplication and division
- combination of multiplication and shift right
- combination of division and shift left
- modulo
- limitation

Some of these functions are proposed too for 2ⁿ Scaled Integers:

- addition
- subtraction
- absolute value
- absolute value of differences
- multiplication
- division
- conversion (specific to 2ⁿ Scaled Integers)

All routines are re-entrant and can be used by multiple runnables at the same time.



2 Acronyms and abbreviations

Acronyms and abbreviations, which have a local scope and therefore are not contained in the AUTOSAR glossary, must appear in a local glossary.

Abbreviation /	Description:
Acronym:	
Abs	Absolute value
AbsDiff	Absolute value of a difference
Add	Addition
AR	Autosar
BSW	Basic Software
DET	Default Error Tracer
Div	Division
DivShLeft	Combination of division and shift left
ECU	Electronic Control Unit
Limit	Limitation routine
Max	Maximum
MFX/Mfx	Math – Fixed Point library
Min	Minimum
Minmax	Limitation with only one value for min and max
Mod	Modulo routine
Mul	Multiplication
MulDiv	Combination of multiplication and division
MulShRight	Combination of multiplication and shift right
s16	Mnemonic for the sint16, specified in AUTOSAR_SWS_PlatformTypes
s32	Mnemonic for the sint32, specified in AUTOSAR_SWS_PlatformTypes
s8	Mnemonic for the sint8, specified in AUTOSAR_SWS_PlatformTypes
Sub	Subtraction
SWS	Software Specification
u16	Mnemonic for the uint16, specified in AUTOSAR_SWS_PlatformTypes
u32	Mnemonic for the uint32, specified in AUTOSAR_SWS_PlatformTypes
u8	Mnemonic for the uint8, specified in AUTOSAR_SWS_PlatformTypes



3 Related documentation

3.1 Input documents

- [1] List of Basic Software Modules, AUTOSAR_TR_BSWModuleList.pdf
- [2] Layered Software Architecture, AUTOSAR_EXP_LayeredSoftwareArchitecture.pdf
- [3] General Requirements on Basic Software Modules, AUTOSAR_SRS_BSWGeneral.pdf
- [4] Specification of ECU Configuration, AUTOSAR_TPS_ECUConfiguration.pdf
- [5] AUTOSAR Basic Software Module Description Template, AUTOSAR_TPS_BSWModuleDescriptionTemplate.pdf
- [6] Specification of Platform Types, AUTOSAR_SWS_PlatformTypes.pdf
- [7] Requirement on Libraries, AUTOSAR_SRS_Libraries.pdf
- [8] Specification of C Implementation Rules, AUTOSAR_TR_CImplementationRules.pdf
- [9] Specification of Standard Types, AUTOSAR_SWS_StandardTypes.pdf

3.2 Related standards and norms

[10] ISO/IEC 9899:1990 Programming Language - C



4 Constraints and assumptions

4.1 Limitations

 No requirements on Service library can be implemented in multiple ways. Many small routines can be combined into one implementation file. For bigger routines, one file shall contain one routine implementation. Generally one routine per object file is recommended from linker optimization point of view. For Bit handling routines more routines can contribute to form one object file. This kind of grouping is not achieved in Release 4.0, Rev001 and will be addressed in Release 4.0, rev002

4.2 Applicability to car domains

No restrictions.



5 Dependencies to other modules

5.1 File structure

[SWS Mfx 00001] [The MFX module shall provide the following files:

- C files, Mfx_<name>.c used to implement the library. All C files shall be pre-fixed with 'Mfx'.
- Header file Mfx.h provides all public function prototypes and types defined by the Mfx library specification | (SRS_LIBS_00005)

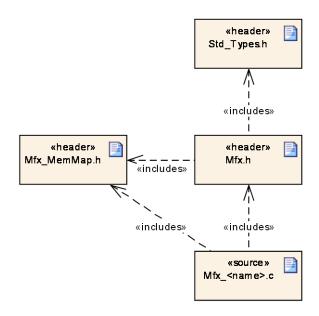


Figure 1: File structure

Implementation & grouping of routines with respect to C files is recommended as per below options and there is no restriction to follow the same.

Option 1 : <Name> can be function name providing one C file per function, eg.: Mfx_Add_u8u8_u8.c etc.

Option 2 : <Name> can have common name of group of functions:

2.1 Group by object family:

eg.: Mfx_NomMath.c, Mfx_ScaledMath.c

2.2 Group by routine family:

eg.: Mfx Add.c

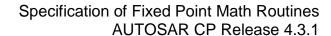
2.3 Group by method family: if it makes sense

2.4 Group by architecture:

eq.: Mfx Add8.c

2.5 Group by other methods: (individual grouping allowed)

Option 3 : <Name> can be removed so that single C file shall contain all MFX functions, eg.: Mfx.c.





Using above options gives certain flexibility of choosing suitable granularity with reduced number of C files. Linking only on-demand is also possible in case of some options.



6 Requirements traceability

Requirement	Description	Satisfied by
SRS_BSW_00003	All software modules shall provide version and identification information	SWS_Mfx_00215
SRS_BSW_00007	All Basic SW Modules written in C language shall conform to the MISRA C 2012 Standard.	SWS_Mfx_00209
SRS_BSW_00304	All AUTOSAR Basic Software Modules shall use the following data types instead of native C data types	SWS_Mfx_00212
SRS_BSW_00305	Data types naming convention	SWS_Mfx_00024
SRS_BSW_00306	AUTOSAR Basic Software Modules shall be compiler and platform independent	SWS_Mfx_00213
SRS_BSW_00318	Each AUTOSAR Basic Software Module file shall provide version numbers in the header file	SWS_Mfx_00215
SRS_BSW_00321	The version numbers of AUTOSAR Basic Software Modules shall be enumerated according specific rules	SWS_Mfx_00215
SRS_BSW_00348	All AUTOSAR standard types and constants shall be placed and organized in a standard type header file	SWS_Mfx_00211
SRS_BSW_00374	All Basic Software Modules shall provide a readable module vendor identification	SWS_Mfx_00214
SRS_BSW_00378	AUTOSAR shall provide a boolean type	SWS_Mfx_00212
SRS_BSW_00379	All software modules shall provide a module identifier in the header file and in the module XML description file.	SWS_Mfx_00214
SRS_BSW_00402	Each module shall provide version information	SWS_Mfx_00214
SRS_BSW_00407	Each BSW module shall provide a function to read out the version information of a dedicated module implementation	SWS_Mfx_00215, SWS_Mfx_00216
SRS_BSW_00411	All AUTOSAR Basic Software Modules shall apply a naming rule for enabling/disabling the existence of the API	SWS_Mfx_00216
SRS_BSW_00437	Memory mapping shall provide the possibility to define RAM segments which are not to be initialized during startup	SWS_Mfx_00210
SRS_LIBS_00001	The functional behavior of each library functions shall not be configurable	SWS_Mfx_00218
SRS_LIBS_00002	A library shall be operational before all BSW modules and application SW-Cs	SWS_Mfx_00200
SRS_LIBS_00003	A library shall be operational until the shutdown	SWS_Mfx_00201
SRS_LIBS_00004	Using libraries shall not pass through a port interface	SWS_Mfx_00203
SRS_LIBS_00005	Each library shall provide one header file with its public interface	SWS_Mfx_00001
SRS_LIBS_00007	Using a library should be documented	SWS_Mfx_00205
SRS_LIBS_00013	The error cases, resulting in the check at runtime of the value of input parameters, shall be listed in SWS	SWS_Mfx_00217, SWS_Mfx_00220
SRS_LIBS_00015	It shall be possible to configure the microcontroller so that the library code is shared between all callers	SWS_Mfx_00206
SRS_LIBS_00017	Usage of macros should be avoided	SWS_Mfx_00207



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SRS_LIBS_00018 A library function may only call library functions SWS_Mfx_00208



7 Functional specification

7.1 Error classification

[SWS_Mfx_00227] No error classification definition as DET call not supported by library] ()

7.2 Error detection

[SWS_Mfx_00220] [Error detection: Function should check at runtime (both in production and development code) the value of input parameters, especially cases where erroneous value can bring to fatal error or unpredictable result, if they have the values allowed by the function specification. All the error cases shall be listed in SWS and the function should return a specified value (in SWS) that is not configurable. This value is dependant of the function and the error case so it is determined case by case.

If values passed to the routines are not valid and out of the function specification, then such error are not detected.

E.g. If passed value > 32 for a bit-position

or a negative number of samples of an axis distribution is passed to a routine. (SRS_LIBS_00013)

7.3 Error notification

[SWS_Mfx_00217] [The functions shall not call the DET for error notification.] (SRS_LIBS_00013)

7.4 Initialization and shutdown

[SWS_Mfx_00200] [MFX library shall not require initialization phase. A Library function may be called at the very first step of ECU initialization, e.g. even by the OS or EcuM, thus the library shall be ready.] (SRS_LIBS_00002)

[SWS_Mfx_00201] [MFX library shall not require a shutdown operation phase.] (SRS_LIBS_00003)

7.5 Using Library API

[SWS_Mfx_00203] [MFX API can be directly called from BSW modules or SWC. No port definition is required. It is a pure function call.] (SRS_LIBS_00004)



[SWS_Mfx_00205] [Using a library should be documented. if a BSW module or a SWC uses a Library, the developer should add an Implementation-DependencyOnArtifact in the BSW/SWC template.

minVersion and maxVersion parameters correspond to the supplier version. In case of AUTOSAR library, these parameters may be left empty because a SWC or BSW module may rely on a library behaviour, not on a supplier implementation. However, the SWC or BSW modules shall be compatible with the AUTOSAR platform where they are integrated.] (SRS_LIBS_00007)

7.6 Library implementation

[SWS_Mfx_00206] [The MFX library shall be implemented in a way that the code can be shared among callers in different memory partitions.] (SRS_LIBS_00015)

[SWS_Mfx_00207] [Usage of macros should be avoided. The function should be declared as function or inline function. Macro #define should not be used.] (SRS_LIBS_00017)

[SWS_Mfx_00208] [A library function shall not call any BSW modules functions, e.g. the DET. A library function can call other library functions. Because a library function shall be re-entrant. But other BSW modules functions may not be re-entrant.] (SRS_LIBS_00018)

[SWS_Mfx_00209] [The library, written in C programming language, should conform to the MISRA C Standard. Please refer to SWS_BSW_00115 for more details.] (SRS_BSW_00007)

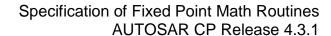
[SWS_Mfx_00210] [Each AUTOSAR library Module implementation library>*.c and

< library>*.h shall map their code to memory sections using the AUTOSAR memory
mapping mechanism.] (SRS_BSW_00437)

[SWS_Mfx_00211] [Each AUTOSAR library Module implementation library>*.c, that uses AUTOSAR integer data types and/or the standard return, shall include the header file Std_Types.h.] (SRS_BSW_00348)

[SWS_Mfx_00212] [All AUTOSAR library Modules should use the AUTOSAR data types (integers, boolean) instead of native C data types, unless this library is clearly identified to be compliant only with a platform.] (SRS_BSW_00378, SRS_BSW_00304)

[SWS_Mfx_00213] [All AUTOSAR library Modules should avoid direct use of compiler and platform specific keyword, unless this library is clearly identified to be compliant only with a platform. | (SRS_BSW_00306)





[SWS_Mfx_00225] [Integral promotion has to be adhered to when implementing Mfx services. Thus, to obtain maximal precision, intermediate results shall not be limited.] ()



8 API specification

8.1 Imported types

In this chapter, all types included from the following files are listed:

Header file	Imported Type
Std_Types.h	sint8, uint8, sint16, uint16, sint32, uint32

It is observed that since the sizes of the integer types provided by the C language are implementation-defined, the range of values that may be represented within each of the integer types will vary between implementations.

Thus, in order to improve the portability of the software, these types are defined in PlatformTypes.h [6]. The following mnemonics are used in the library routine names.

Size	Platform Type	Mnemonic
signed 8-Bit	sint8	s8
signed 16-Bit	sint16	s16
signed 32-Bit	sint32	s32
unsigned 8-Bit	uint8	u8
unsigned 16-Bit	uint16	u16
unsigned 32-Bit	uint32	u32

Table 1: Base Types

As described in [6], the ranges for each of the base types are shown in Table 2.

Base Type	Range
uint8	[0, 255]
sint8	[-128, 127]
uint16	[0, 65535]
sint16	[-32768, 32767]
uint32	[0, 4294967295]
sint32	[-2147483648, 2147483647]

Table 2: Ranges for Base Types

As a convention in the rest of the document:

- mnemonics will be used in the name of the routines (using <InTypeMn1> that means Type Mnemonic for Input 1)
- the real type will be used in the description of the prototypes of the routines (using <InType1> or <OutType>).



8.2 Type definitions

None.

8.3 Comment about rounding

Two types of rounding can be applied:

Results are "rounded off", it means:

•	$0 \le X \le 0.5$	rounded to 0
•	$0.5 \le X \le 1$	rounded to 1
•	-0.5 < X <= 0	rounded to 0
•	-1 < X <= -0.5	rounded to -1

Results are rounded towards zero:

- 0 <= X < 1 rounded to 0
- $-1 < X \le 0$ rounded to 0

8.4 Comment about routines optimization

8.4.1 Optimized with constants

For optimization purpose, in some routines, it is mandatory that an argument of the function "must be constant".

The requirement is that the expression must be fully evaluated at compile time. It may be a constant literal, macro, or arithmetic expression that can be computed at compile time. It may not contain a variable or function call.

For example, the parameters for the radix points are constant expressions so that they may be eliminated after the pre-process phase of compilation. When implemented properly as an inline function or macro, the calculations for the number of shifts necessary are done at compile time, not at run time. There is a ROM/throughput penalty when constant expressions are not used.

8.5 Mathematical routines definitions

8.5.1 Additions

[SWS_Mfx_00002] [

Service name:	Mfx_Add_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>	
Syntax:	<pre><outtype> Mfx_Add_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x01 to 0x18	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	



Parameters (in):	x_value	First argument
Parameters (m).	y_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<outtype> Result of the calculation</outtype>	
Description:	This routine makes an addition between the two arguments.	

()

[SWS_Mfx_00006][

This routine makes an addition between the two arguments:

Return-value = x_value + y_value

J()

[SWS_Mfx_00007][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

1()

[SWS_Mfx_00008] [

Function ID[hex]	Function prototype
0x001	uint8 Mfx_Add_u8u8_u8(uint8 , uint8);
0x002	uint8 Mfx_Add_u8s8_u8(uint8 , sint8);
0x003	sint8 Mfx_Add_u8s8_s8(uint8 , sint8);
0x004	uint8 Mfx_Add_s8s8_u8(sint8 , sint8);
0x005	sint8 Mfx_Add_s8s8_s8(sint8 , sint8);
0x006	uint16 Mfx_Add_u16u16_u16(uint16 , uint16);
0x007	uint16 Mfx_Add_u16s16_u16(uint16 , sint16);
0x008	sint16 Mfx_Add_u16s16_s16(uint16 , sint16);
0x009	uint8 Mfx_Add_s16s16_u8(sint16 , sint16);
0x00A	sint8 Mfx_Add_s16s16_s8(sint16 , sint16);
0x00B	uint16 Mfx_Add_s16s16_u16(sint16 , sint16);
0x00C	sint16 Mfx_Add_s16s16_s16(sint16 , sint16);
0x00D	sint8 Mfx_Add_u32u32_s8(uint32 , uint32);
0x00E	sint16 Mfx_Add_u32u32_s16(uint32 , uint32);
0x00F	uint32 Mfx_Add_u32u32_u32(uint32 , uint32);
0x010	sint32 Mfx_Add_u32u32_s32(uint32 , uint32);
0x011	uint32 Mfx_Add_u32s32_u32(uint32 , sint32);
0x012	sint32 Mfx_Add_u32s32_s32(uint32 , sint32);
0x013	uint32 Mfx_Add_s32s32_u32(sint32 , sint32);
0x014	sint32 Mfx_Add_s32s32_s32(sint32 , sint32);
0x015	uint8 Mfx_Add_s32s32_u8(sint32 , sint32);
0x016	sint8 Mfx_Add_s32s32_s8(sint32 , sint32);
0x017	uint16 Mfx_Add_s32s32_u16(sint32 , sint32);
0x018	sint16 Mfx_Add_s32s32_s16(sint32 , sint32);
0xA01	sint16 Mfx_Add_u32s32_s16(uint32 , sint32);
0xA02	sint8 Mfx_Add_u32s32_s8(uint32 , sint32);
0xA03	uint16 Mfx_Add_u32s32_u16(uint32 , sint32);
0xA04	unit8 Mfx_Add_u32s32_u8(uint32 , sint32);
0xA05	uint16 Mfx_Add_u32u32_u16(uint32 , uint32);



0xA06	uint8 Mfx_Add_u32u32_u8(uint32 , uint32);
0xA07	sint16 Mfx_Add_u16u16_s16(uint16 , uint16);
0xA08	uint8 Mfx_Add_u16u16_u8(uint16 , uint16);
0xA09	uint8 Mfx_Add_u16s16_u8(uint16 , sint16);
0xA0A	sint8 Mfx_Add_u16u16_s8(uint16 , uint16);
0xA0B	sint8 Mfx_Add_u16s16_s8(uint16 , sint16);
0xA0C	sint8 Mfx_Add_u8u8_s8(uint8 , uint8);

]()

8.5.2 Subtractions

[SWS_Mfx_00009] [

Sorvice name:	Mfy Sub alpTypoMp1> alpTypol	Mn2s - OutTynoMns
Service name:	Mfx_Sub_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>	
Syntax:	<outtype> Mfx_Sub_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype>	
	<pre><intype1> x_value,</intype1></pre>	
	<intype2> y_value</intype2>	
)	
Service ID[hex]:	0x20 to 0x40	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Paramatara (in)	x_value	First argument
Parameters (in):	y_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	This routine makes a subtraction between the two arguments.	

] ()

[SWS_Mfx_00010][

This routine makes a subtraction between the two arguments: Return-value = $x_value - y_value$

]()

[SWS_Mfx_00011][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()

[SWS_Mfx_00012] [

Function ID[hex]	Function prototype
0x020	uint8 Mfx_Sub_u8u8_u8(uint8 , uint8);
0x021	sint8 Mfx_Sub_u8u8_s8(uint8 , uint8);
0x022	uint8 Mfx_Sub_u8s8_u8(uint8 , sint8);
0x023	sint8 Mfx_Sub_s8u8_s8(sint8 , uint8);
0x024	sint8 Mfx_Sub_s8s8_s8(sint8 , sint8);
0x025	uint8 Mfx_Sub_u16u16_u8(uint16 , uint16);
0x026	sint8 Mfx_Sub_u16u16_s8(uint16 , uint16);
0x027	uint8 Mfx_Sub_s16s16_u8(sint16 , sint16);
0x028	sint8 Mfx_Sub_s16s16_s8(sint16 , sint16);



0x029 uint8 Mfx_Sub_s32s32_s8(sint32 , sint32); 0x02A sint8 Mfx_Sub_s32s32_s8(sint32 , sint32); 0x02B uint16 Mfx_Sub_u16u16_u16(uint16 , uint16); 0x02C uint16 Mfx_Sub_u16s16_u16(uint16 , sint16); 0x02D sint16 Mfx_Sub_u16s16_s16(sint16 , sint16); 0x02E sint16 Mfx_Sub_u16s16_s16(uint16 , sint16); 0x02F uint16 Mfx_Sub_u16u16_s16(uint16 , sint16); 0x030 sint16 Mfx_Sub_u16u16_s16(uint16 , sint16); 0x030 sint16 Mfx_Sub_u16u16_s16(sint16 , sint16); 0x031 sint16 Mfx_Sub_u316s16_u16(sint32 , uint32); 0x032 uint8 Mfx_Sub_u32s32_u8(sint32 , sint32); 0x033 sint8 Mfx_Sub_u32s32_u8(sint32 , sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32 , uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(sint32 , uint32); 0x036 sint16 Mfx_Sub_u32u32_u16(sint32 , sint32); 0x037 uint16 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x038 sint16 Mfx_Sub_u32s32_u32(uint32 , uint32); 0x039 uint32 Mfx_Sub_u32s32_u32(uint32 , uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32 , uint32); 0x03D sint32 Mfx_Sub_u32s32_s32(uint	0.000	: (0 M(0 L 00 00 0/ : (00 : (00)
0x02B uint16 Mfx_Sub_u16u16_u16(uint16, uint16); 0x02C uint16 Mfx_Sub_u16s16_u16(uint16, sint16); 0x02D sint16 Mfx_Sub_s16u16_s16(uint16, sint16); 0x02E sint16 Mfx_Sub_u16s16_s16(uint16, sint16); 0x02F uint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_s16s16_u16(uint16, uint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_s32u32_u16(sint32, uint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_s32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x031 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x032 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); <td>0x029</td> <td>uint8 Mfx_Sub_s32s32_u8(sint32 , sint32);</td>	0x029	uint8 Mfx_Sub_s32s32_u8(sint32 , sint32);
0x02C uint16 Mfx_Sub_u16s16_u16(uint16, sint16); 0x02D sint16 Mfx_Sub_s16u16_s16(sint16, uint16); 0x02E sint16 Mfx_Sub_u16s16_s16(uint16, sint16); 0x02F uint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s2_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_u16(sint32, sint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_u32u32_u32(uint32, sint32); 0x039 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x037 uint32 Mfx_Sub_s32s32_u32(uint32, uint32); 0x038 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_s32s32_u32(uint32, uint32);		, , , , , , , , , , , , , , , , , , ,
0x02D sint16 Mfx_Sub_s16u16_s16(sint16, uint16); 0x02E sint16 Mfx_Sub_u16s16_s16(uint16, sint16); 0x02F uint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32, uint32); 0x034 uint16 Mfx_Sub_u32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x037 uint16 Mfx_Sub_u32u32_s16(uint32, sint32); 0x038 sint16 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x038 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_us2(uint32, uint32); 0x030 sint32 Mfx_Sub_u32s32_u32(uint32, uint32); <th>0x02B</th> <th></th>	0x02B	
0x02E sint16 Mfx_Sub_u16s16_s16(uint16, sint16); 0x02F uint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_u16u16_s16(uint16, uint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_u32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x038 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_us2(uint32, uint32); 0x031 sint32 Mfx_Sub_u32u32_us2(uint32, uint32); 0x035 sint32 Mfx_Sub_u32s32_us2(uint32, uint32); <th>0x02C</th> <th></th>	0x02C	
0x02F uint16 Mfx_Sub_s16s16_u16(sint16, sint16); 0x030 sint16 Mfx_Sub_u16u16_s16(uint16, uint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_u32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_s16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x030 sint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x041 sint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x045 sint4 Mfx_Sub_u32s32_u3(uint32, uint32);	0x02D	
0x030 sint16 Mfx_Sub_u16u16_s16(uint16, uint16); 0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_u16(sint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, sint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03B uint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03B uint32 Mfx_Sub_u32u32_u32(sint32, uint32); 0x03B uint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03B uint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03B sint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x04B uint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x04B uint32 Mfx_Sub_u32s32_u32(sint32, uint32); <td>0x02E</td> <td></td>	0x02E	
0x031 sint16 Mfx_Sub_s16s16_s16(sint16, sint16); 0x032 uint8 Mfx_Sub_s32u32_u8(sint32, uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x030 uint32 Mfx_Sub_u32s32_u32(uint32, uint32); 0x03A uint32 Mfx_Sub_s32u32_u32(sint32, uint32); 0x03B uint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_u32s32_s32(sint32, uint32); 0x03E sint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x040 sint32 Mfx_Sub_s32s32_u32(sint32, uint32); 0x043 sint16 Mfx_Sub_s32s32_s32(sint32, uint32); 0x044 sint8 Mfx_Sub_s32s32_s16(uint32, sint32); 0x044 sint8 Mfx_Sub_s32s32_s16(uint32, sint32); 0x045 sint16 Mfx_Sub_u32s32_u6(uint32, sint32);	0x02F	
0x032 uint8 Mfx_Sub_s32u32_u8(sint32 , uint32); 0x033 sint8 Mfx_Sub_u32s32_s8(uint32 , sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32 , uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32 , uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32 , uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32 , sint32); 0x038 sint16 Mfx_Sub_s32s32_u32(uint32 , sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32 , uint32); 0x03B uint32 Mfx_Sub_u32u32_u32(sint32 , uint32); 0x03B sint32 Mfx_Sub_u32s32_u32(sint32 , uint32); 0x03B sint32 Mfx_Sub_u32s32_u32(sint32 , uint32); 0x03B sint32 Mfx_Sub_u32s32_u32(sint32 , uint32); 0x03B sint32 Mfx_Sub_us32s32_u32(sint32 , uint32); 0x03F uint32 Mfx_Sub_us32s32_u32(sint32 , uint32); 0x04B sint16 Mfx_Sub_us2s32_u36(sint32 , uint32); 0x04B sint8 Mfx_Sub_us2s32_u16(0x030	
0x033 sint8 Mfx_Sub_u32s32_s8(uint32, sint32); 0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03B uint32 Mfx_Sub_u32u32_u32(sint32, uint32); 0x03B uint32 Mfx_Sub_u32u32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03E sint32 Mfx_Sub_u32s32_u32(sint32, sint32); 0x040 sint32 Mfx_Sub_s32s32_u32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32s32_u32(sint32, uint32); 0x044 sint8 Mfx_Sub_u32s32_u3(uint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u8(uint32, uint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x048 sint8 Mfx_Sub_u16s16_u8(uint16, uint16);	0x031	· · · · · · · · · · · · · · · · · · ·
0x034 uint16 Mfx_Sub_s32u32_u16(sint32, uint32); 0x035 uint16 Mfx_Sub_u32u32_u16(uint32, uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32, uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03B uint32 Mfx_Sub_u32u32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_u32s32_u32(sint32, uint32); 0x03E sint32 Mfx_Sub_u32s32_u32(sint32, sint32); 0x040 sint32 Mfx_Sub_s32s32_u32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32s32_s32(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x044 sint8 Mfx_Sub_u32s32_u16(uint32, sint32); 0x045 sint16 Mfx_Sub_u32s32_u8(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u8(uint32, uint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x048 sint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x049 uint8 Mfx_Sub_u16s16_u8(uint16, uint16); <	0x032	uint8 Mfx_Sub_s32u32_u8(sint32 , uint32);
0x035 uint16 Mfx_Sub_u32u32_u16(uint32 , uint32); 0x036 sint16 Mfx_Sub_u32u32_s16(uint32 , uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32 , sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32 , sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32 , uint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32 , uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32 , uint32); 0x03D sint32 Mfx_Sub_u32s32_s32(uint32 , uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , uint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s16(uint32 , sint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x049 uint8 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_s16u16_s8(sint16	0x033	sint8 Mfx_Sub_u32s32_s8(uint32 , sint32);
0x036 sint16 Mfx_Sub_u32u32_s16(uint32 , uint32); 0x037 uint16 Mfx_Sub_s32s32_u16(sint32 , sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32 , sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32 , sint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32 , uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32 , uint32); 0x03D sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03E sint32 Mfx_Sub_u32s32_u32(sint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , uint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x049 uint8 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04C uint8 Mfx_Sub_s16u16_s8(sint16 ,	0x034	uint16 Mfx_Sub_s32u32_u16(sint32 , uint32);
0x037 uint16 Mfx_Sub_s32s32_u16(sint32, sint32); 0x038 sint16 Mfx_Sub_s32s32_s16(sint32, sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32, uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_u32s32_s32(uint32, sint32); 0x03E sint32 Mfx_Sub_u32s32_u32(sint32, sint32); 0x04F uint32 Mfx_Sub_s32s32_u32(sint32, uint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32, uint32); 0x043 sint16 Mfx_Sub_s32u32_s8(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u8(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x04B uint8 Mfx_Sub_s16u16_u16(sint16, uint16); 0x04C uint8 Mfx_Sub_s16u16_s8(sint16, uint16); 0x04E sint8 Mfx_Sub_s8u_u8(sint8, uint8);	0x035	uint16 Mfx_Sub_u32u32_u16(uint32 , uint32);
0x038 sint16 Mfx_Sub_s32s32_s16(sint32 , sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32 , sint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32 , uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32 , uint32); 0x03D sint32 Mfx_Sub_s32u32_s32(uint32 , uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , uint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u16s16_u8(uint16 , uint16); 0x04B uint8 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04C uint8 Mfx_Sub_s16u16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s8u_u8(sint8 , uint8); 0x04F uint8 Mfx_Sub_s8su_u8(sint8 , sint8); <td>0x036</td> <td>sint16 Mfx_Sub_u32u32_s16(uint32 , uint32);</td>	0x036	sint16 Mfx_Sub_u32u32_s16(uint32 , uint32);
0x038 sint16 Mfx_Sub_s32s32_s16(sint32 , sint32); 0x039 uint32 Mfx_Sub_u32u32_u32(uint32 , uint32); 0x03A uint32 Mfx_Sub_u32s32_u32(uint32 , sint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32 , uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32 , uint32); 0x03D sint32 Mfx_Sub_s32u32_s32(uint32 , uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , uint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u16s16_u8(uint16 , uint16); 0x04B uint8 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04C uint8 Mfx_Sub_s16u16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s8u_u8(sint8 , uint8); 0x04F uint8 Mfx_Sub_s8su_u8(sint8 , sint8); <td>0x037</td> <td>uint16 Mfx_Sub_s32s32_u16(sint32 , sint32);</td>	0x037	uint16 Mfx_Sub_s32s32_u16(sint32 , sint32);
0x03A uint32 Mfx_Sub_u32s32_u32(uint32, sint32); 0x03B uint32 Mfx_Sub_s32u32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_s32u32_s32(sint32, uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32, sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32, sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x04A uint16 Mfx_Sub_u16s16_u8(uint16, uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16, sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16, uint16); 0x04B sint8 Mfx_Sub_s16u16_s8(sint16, uint16); 0x04F uint8 Mfx_Sub_s8us_u8(sint8, uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8, sint8);	0x038	
0x03B uint32 Mfx_Sub_s32u32_u32(sint32, uint32); 0x03C sint32 Mfx_Sub_u32u32_s32(uint32, uint32); 0x03D sint32 Mfx_Sub_s32u32_s32(sint32, uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32, sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32, sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, uint32); 0x048 sint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x049 uint8 Mfx_Sub_u16s16_u8(uint16, uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(uint16, uint16); 0x04C uint8 Mfx_Sub_s16u16_s8(uint16, uint16); 0x04D sint8 Mfx_Sub_s16u16_s8(uint16, uint16); 0x04F uint8 Mfx_Sub_s8us_u8(sint8, uint8); 0xA15 uint8 Mfx_Sub_s8ss_u8(sint8, sint8);	0x039	
0x03C sint32 Mfx_Sub_u32u32_s32(uint32 , uint32); 0x03D sint32 Mfx_Sub_s32u32_s32(sint32 , uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_u16s16_s8(uint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , uint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x03A	uint32 Mfx_Sub_u32s32_u32(uint32 , sint32);
0x03D sint32 Mfx_Sub_s32u32_s32(sint32 , uint32); 0x03E sint32 Mfx_Sub_u32s32_s32(uint32 , sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_s16u16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , sint8);	0x03B	uint32 Mfx_Sub_s32u32_u32(sint32 , uint32);
0x03E sint32 Mfx_Sub_u32s32_s32(uint32, sint32); 0x03F uint32 Mfx_Sub_s32s32_u32(sint32, sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16, uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(uint16, sint16); 0x04C uint8 Mfx_Sub_s16u16_s8(uint16, uint16); 0x04D sint8 Mfx_Sub_s16u16_s8(sint16, uint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8, uint8); 0x04F uint8 Mfx_Sub_s8s8_u8(sint8, sint8);	0x03C	sint32 Mfx_Sub_u32u32_s32(uint32 , uint32);
0x03F uint32 Mfx_Sub_s32s32_u32(sint32 , sint32); 0x040 sint32 Mfx_Sub_s32s32_s32(sint32 , sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32 , uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , uint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0x04F uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x03D	sint32 Mfx_Sub_s32u32_s32(sint32 , uint32);
0x040 sint32 Mfx_Sub_s32s32_s32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16, uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(sint16, uint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16, uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(sint16, uint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8, uint8); 0x04F uint8 Mfx_Sub_s8s8_u8(sint8, sint8);	0x03E	sint32 Mfx_Sub_u32s32_s32(uint32 , sint32);
0x040 sint32 Mfx_Sub_s32s32_s32(sint32, sint32); 0x043 sint16 Mfx_Sub_s32u32_s16(sint32, uint32); 0x044 sint8 Mfx_Sub_s32u32_s8(sint32, uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32, sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32, sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32, sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32, uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32, uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16, uint16); 0x04B uint8 Mfx_Sub_s16u16_u8(sint16, uint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16, uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(sint16, uint16); 0x04E sint8 Mfx_Sub_s8u8_u8(sint8, uint8); 0x04F uint8 Mfx_Sub_s8s8_u8(sint8, sint8);	0x03F	uint32 Mfx_Sub_s32s32_u32(sint32 , sint32);
0x044 sint8 Mfx_Sub_s32u32_s8(sint32 , uint32); 0x045 sint16 Mfx_Sub_u32s32_s16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8us_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8ss_u8(sint8 , sint8);	0x040	
0x045 sint16 Mfx_Sub_u32s32_s16(uint32 , sint32); 0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8us_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8ss_u8(sint8 , sint8);	0x043	sint16 Mfx_Sub_s32u32_s16(sint32 , uint32);
0x046 uint16 Mfx_Sub_u32s32_u16(uint32 , sint32); 0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x044	sint8 Mfx_Sub_s32u32_s8(sint32 , uint32);
0x047 uint8 Mfx_Sub_u32s32_u8(uint32 , sint32); 0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8us_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8ss_u8(sint8 , sint8);	0x045	sint16 Mfx_Sub_u32s32_s16(uint32 , sint32);
0x048 sint8 Mfx_Sub_u32u32_s8(uint32 , uint32); 0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x046	uint16 Mfx_Sub_u32s32_u16(uint32 , sint32);
0x049 uint8 Mfx_Sub_u32u32_u8(uint32 , uint32); 0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x047	uint8 Mfx_Sub_u32s32_u8(uint32 , sint32);
0x04A uint16 Mfx_Sub_s16u16_u16(sint16 , uint16); 0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x048	sint8 Mfx_Sub_u32u32_s8(uint32 , uint32);
0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x049	uint8 Mfx_Sub_u32u32_u8(uint32 , uint32);
0x04B uint8 Mfx_Sub_u16s16_u8(uint16 , sint16); 0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x04A	uint16 Mfx_Sub_s16u16_u16(sint16 , uint16);
0x04C uint8 Mfx_Sub_s16u16_u8(sint16 , uint16); 0x04D sint8 Mfx_Sub_u16s16_s8(uint16 , sint16); 0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x04B	
0x04E sint8 Mfx_Sub_s16u16_s8(sint16 , uint16); 0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x04C	
0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x04D	sint8 Mfx_Sub_u16s16_s8(uint16 , sint16);
0x04F uint8 Mfx_Sub_s8u8_u8(sint8 , uint8); 0xA15 uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);	0x04E	sint8 Mfx_Sub_s16u16_s8(sint16 , uint16);
	0x04F	· · · · · · · · · · · · · · · · · · ·
0xA16 sint8 Mfx_Sub_u8s8_s8(uint8 , sint8);	0xA15	uint8 Mfx_Sub_s8s8_u8(sint8 , sint8);
	0xA16	sint8 Mfx_Sub_u8s8_s8(uint8 , sint8);

]()

8.5.3 Absolute value

[SWS_Mfx_00013] [

Service name:	Mfx_Abs_ <intypemn1>_<outtypemn></outtypemn></intypemn1>	
Syntax:	<pre><outtype> Mfx_Abs_<intypemn1>_<outtypemn>(</outtypemn></intypemn1></outtype></pre>	
Service ID[hex]:	0x50 to 0x57	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	x_value First argument	
Parameters (in-	None	



out):		
Parameters (out):	None	
Return value:	<outtype> Result of the calculation</outtype>	
Description:	This routine computes the absolute value of a signed value.	

]()

[SWS_Mfx_00014][

This routine computes the absolute value of a signed value: Return-value = $| x_value |$

]()

[SWS_Mfx_00015][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS_Mfx_00016] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x050	uint8 Mfx_Abs_s8_u8(sint8);
0x051	sint8 Mfx_Abs_s8_s8(sint8);
0x052	uint8 Mfx_Abs_s32_u8(sint32);
0x053	uint16 Mfx_Abs_s16_u16(sint16);
0x054	sint16 Mfx_Abs_s16_s16(sint16);
0x055	sint16 Mfx_Abs_s32_s16(sint32);
0x056	uint32 Mfx_Abs_s32_u32(sint32);
0x057	sint32 Mfx_Abs_s32_s32(sint32);
0x058	sint8 Mfx_Abs_s32_s8(sint32);
0x059	uint16 Mfx_Abs_s32_u16(sint32);

]()

8.5.4 Absolute value of a difference

[SWS_Mfx_00017] [

Service name:	Mfx_AbsDiff_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>	
Syntax:	<pre><outtype> Mfx_AbsDiff_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x60 to 0x6a	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	x_value	First argument
rarameters (m).	y_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	This routine computes the absolute value of a difference between 2 values.	

I()



[SWS_Mfx_00018][

This routine computes the absolute value of a difference between 2 values:

Return-value = | x_value - y_value |

J()

[SWS_Mfx_00019][

Return-value shall be saturated to boundary values in the event of overflow. I()

[SWS_Mfx_00020] [

Function ID[hex]	Function prototype	
0x060	uint8 Mfx_AbsDiff_u8u8_u8(uint8 , uint8);	
0x061	uint16 Mfx_AbsDiff_u16u16_u16(uint16 , uint16);	
0x062	uint8 Mfx_AbsDiff_s16s16_u8(sint16 , sint16);	
0x063	uint16 Mfx_AbsDiff_s16s16_u16(sint16 , sint16);	
0x064	uint8 Mfx_AbsDiff_u32s32_u8(uint32 , sint32);	
0x065	uint16 Mfx_AbsDiff_u32s32_u16(uint32 , sint32);	
0x066	uint32 Mfx_AbsDiff_u32s32_u32(uint32 , sint32);	
0x067	uint32 Mfx_AbsDiff_u32u32_u32(uint32 , uint32);	
0x068	uint8 Mfx_AbsDiff_s32s32_u8(sint32 , sint32);	
0x069	sint16 Mfx_AbsDiff_s32s32_s16(sint32 , sint32);	
0x06A	sint32 Mfx_AbsDiff_s32s32_s32(sint32 , sint32);	
0x06B	sint8 Mfx_AbsDiff_s32s32_s8(sint32 , sint32);	
0x06C	uint16 Mfx_AbsDiff_s32s32_u16(sint32 , sint32);	
0x06D	uint32 Mfx_AbsDiff_s32s32_u32(sint32 , sint32);	
0x06E	uint16 Mfx_AbsDiff_u32u32_u16(uint32 , uint32);	
0x06F	uint8 Mfx_AbsDiff_u32u32_u8(uint32 , uint32);	
0xA50	sint8 Mfx_Absdiff_u32u32_s8(uint32 , uint32);	
0xA51	sint16 Mfx_Absdiff_u32u32_s16(uint32 , uint32);	
0xA52	sint32 Mfx_Absdiff_u32u32_s32(uint32 , uint32);	
0xA53	sint8 Mfx_Absdiff_u32s32_s8(uint32 , sint32);	
0xA54	sint16 Mfx_Absdiff_u32s32_s16(uint32 , sint32);	
0xA55	sint32 Mfx_Absdiff_u32s32_s32(uint32 , sint32);	
0xA56	uint16 Mfx_AbsDiff_u16s16_u16(uint16 , sint16);	
0xA57	sint16 Mfx_AbsDiff_u16u16_s16(uint16 , uint16);	
0xA58	sint16 Mfx_AbsDiff_u16s16_s16(uint16 , sint16);	
0xA59	sint16 Mfx_AbsDiff_s16s16_s16(sint16 , sint16);	
0xA5A	uint8 Mfx_AbsDiff_u16u16_u8(uint16 , uint16);	
0xA5B	uint8 Mfx_AbsDiff_u16s16_u8(uint16 , sint16);	
0xA5C	sint8 Mfx_AbsDiff_u16u16_s8(uint16 , uint16);	
0xA5D	sint8 Mfx_AbsDiff_u16s16_s8(uint16 , sint16);	
0xA5E	sint8 Mfx_AbsDiff_s16s16_s8(sint16 , sint16);	
0xA5F	uint8 Mfx_AbsDiff_u8s8_u8(uint8 , sint8);	
0xA60	uint8 Mfx_AbsDiff_s8s8_u8(sint8 , sint8);	
0xA61	sint8 Mfx_AbsDiff_u8u8_s8(uint8 , uint8);	
0xA62	sint8 Mfx_AbsDiff_u8s8_s8(uint8 , sint8);	
0xA62	sint8 Mfx_AbsDiff_s8s8_s8(sint8 , sint8);	



8.5.5 Multiplications

[SWS_Mfx_00021] [

Service name:	Mfx_Mul_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>	
Syntax:	<pre><outtype> Mfx_Mul_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x70 to 0x08d	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	x_value	First argument
Parameters (m).	y_value	Second argument
Parameters (in- out):	None	
	None	
Return value:	<outtype> Result of the calculation</outtype>	
Description:	This routine makes a multiplication between the two arguments.	

()

[SWS_Mfx_00022][

This routine makes a multiplication between the two arguments: Return-value = x_value * y_value]()

[SWS_Mfx_00023][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS_Mfx_00024] [

Function ID[hex]	Function prototype
0x070	uint8 Mfx_Mul_u8u8_u8(uint8 , uint8);
0x071	uint8 Mfx_Mul_s8s8_u8(sint8 , sint8);
0x072 (*)	uint8 Mfx_Mul_s8u8_u8(sint8 , uint8);
0x073 (*)	sint8 Mfx_Mul_s8u8_s8(sint8 , uint8);
0x074	sint8 Mfx_Mul_s8s8_s8(sint8 , sint8);
0x075	uint16 Mfx_Mul_u16u16_u16(uint16 , uint16);
0x076 (*)	uint16 Mfx_Mul_s16u16_u16(sint16 , uint16);
0x077 (*)	sint16 Mfx_Mul_s16u16_s16(sint16 , uint16);
0x078 (*)	sint32 Mfx_Mul_s16u16_s32(sint16 , uint16);
0x079	sint32 Mfx_Mul_s16s16_s32(sint16 , sint16);
0x07A (*)	sint32 Mfx_Mul_s32u16_s32(sint32 , uint16);
0x07B	uint32 Mfx_Mul_u16u16_u32(
0x07C	uint32 Mfx_Mul_u32u16_u32(
0x07D	uint16 Mfx_Mul_s16s16_u16(sint16 , sint16);
0x07E	uint8 Mfx_Mul_s16s16_u8(sint16 , sint16);
0x07F	sint8 Mfx_Mul_s16s16_s8(sint16 , sint16);
0x080	sint16 Mfx_Mul_s16s16_s16(sint16 , sint16);
0x081	uint32 Mfx_Mul_u32u32_u32(



0x082	sint32 Mfx_Mul_u32u32_s32(
0x083(*)	uint16 Mfx_Mul_s32u32_u16(sint32 , uint32);
0x084(*)	uint32 Mfx_Mul_s32u32_u32(sint32 , uint32);
0x085(*)	sint32 Mfx_Mul_s32u32_s32(sint32 , uint32);
0x086	uint32 Mfx_Mul_s32s32_u32(sint32 , sint32);
0x087	uint8 Mfx_Mul_s32s32_u8(sint32 , sint32);
0x088	sint8 Mfx_Mul_u32u32_s8(uint32 , uint32);
0x089	sint8 Mfx_Mul_s32s32_s8(sint32 , sint32);
0x08A	sint16 Mfx_Mul_u32u32_s16(uint32 , uint32);
0x08B	sint16 Mfx_Mul_s32s32_s16(sint32 , sint32);
0x08C	uint16 Mfx_Mul_s32s32_u16(sint32 , sint32);
0x08D	sint32 Mfx_Mul_s32s32_s32(sint32 , sint32);
0xA21	sint16 Mfx_Mul_u32s32_s16(uint32 , sint32);
0xA22	sint8 Mfx_Mul_u32s32_s8(uint32 , sint32);
0xA23	uint8 Mfx_Mul_u32s32_u8(uint32 , sint32);
0xA24	uint16 Mfx_Mul_u32u32_u16(uint32 , uint32);
0xA25	uint8 Mfx_Mul_u32u32_u8(uint32 , uint32);
0xA26	uint8 Mfx_Mul_u8s8_u8(uint8 , sint8);
0xA27	sint8 Mfx_Mul_u8s8_s8(uint8 , sint8);
0xA28	uint16 Mfx_Mul_u16s16_u16(uint16 , sint16);
0xA29	sint16 Mfx_Mul_u16s16_s16(uint16 , sint16);
0xA2A	sint32 Mfx_Mul_u16s16_s32(uint16 , sint16);
0xA2B	uint16 Mfx_Mul_u32s32_u16(uint32 , sint32);
0xA2C	uint32 Mfx_Mul_u32s32_u32(
0xA2D	sint32 Mfx_Mul_u32s32_s32(uint32 , sint32);
0xA2E	sint16 Mfx_Mul_u16u16_s16(uint16 , uint16);
0xA2F	uint8 Mfx_Mul_u16u16_u8(uint16 , uint16);
0xA70	uint8 Mfx_Mul_u16s16_u8(uint16 , sint16);
0xA71	sint8 Mfx_Mul_u16u16_s8(uint16 , uint16);
0xA72	sint8 Mfx_Mul_u16s16_s8(uint16 , sint16);
0xA74	sint8 Mfx_Mul_u8u8_s8(uint8 , uint8);

Note:

The function prototypes with the (*) in the above table, do not follow the common requirement that with mixed data type the first parameter is the unsigned one and then comes the signed one. The new added function prototypes are correcting this.

Therefore, all these marked function prototypes will be removed in a major future release.

」(SRS_BSW_00305)

8.5.6 Divisions rounded towards 0

[SWS_Mfx_00025] [

Service name:	Mfx_Div_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>
Syntax:	<outtype> Mfx_Div_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype>
Service ID[hex]:	0x90 to 0x112
Sync/Async:	Synchronous
Reentrancy:	Reentrant



Parameters (in):	x_value	First argument
	y_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	These routines make a division between the two arguments.	

I()

[SWS_Mfx_00026][

These routines make a division between the two arguments: Return-value = x_value / y_value]()

[SWS_Mfx_00027][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()|

[SWS_Mfx_00028][

The result after division by zero is defined by:

- * If $x_value \ge 0$ then the function returns the maximum value of the output type
- * If x_value < 0 then the function returns the minimum value of the output type |()

[SWS_Mfx_00030][

The result is rounded towards 0.

|()

[SWS_Mfx_00031] [

Function ID[hex]	Function prototype
0x090	uint8 Mfx_Div_u8u8_u8(uint8 , uint8);
0x091	uint8 Mfx_Div_s8u8_u8(sint8 , uint8);
0x092	uint8 Mfx_Div_u8s8_u8(uint8 , sint8);
0x093	uint8 Mfx_Div_s8s8_u8(sint8 , sint8);
0x094	sint8 Mfx_Div_u8s8_s8(uint8 , sint8);
0x095	sint8 Mfx_Div_s8u8_s8(sint8 , uint8);
0x096	sint8 Mfx_Div_s8s8_s8(sint8 , sint8);
0x097	uint16 Mfx_Div_u16u16_u16(uint16 , uint16);
0x098	uint16 Mfx_Div_s16u16_u16(sint16 , uint16);
0x099	uint16 Mfx_Div_u16s16_u16(uint16 , sint16);
0x09A	sint16 Mfx_Div_u16s16_s16(uint16 , sint16);
0x09B	sint16 Mfx_Div_s16u16_s16(sint16 , uint16);
0x09C	uint16 Mfx_Div_s16s16_u16(sint16 , sint16);
0x09D	uint8 Mfx_Div_s16s16_u8(sint16 , sint16);
0x09E	sint8 Mfx_Div_s16s16_s8(sint16 , sint16);
0x09F	sint16 Mfx_Div_s16s16_s16(sint16 , sint16);
0x100 (*)	uint16 Mfx_Div_s32u16_u16(sint32 , uint16);
0x101 (*)	sint16 Mfx_Div_s32s16_s16(sint32 , sint16);



0x102	sint16 Mfx_Div_s32u32_s16(sint32 , uint32);
0x103 (*)	uint32 Mfx_Div_u32u16_u32(
0x104 (*)	uint16 Mfx_Div_u32u16_u16(uint32 , uint16);
0x105	uint32 Mfx_Div_u32u32_u32(uint32 , uint32);
0x106	uint32 Mfx_Div_s32u32_u32(sint32 , uint32);
0x107	uint32 Mfx_Div_u32s32_u32(uint32 , sint32);
0x108	sint32 Mfx_Div_u32s32_s32(uint32 , sint32);
0x109	sint32 Mfx_Div_s32u32_s32(sint32 , uint32);
0x10A	uint32 Mfx_Div_s32s32_u32(sint32 , sint32);
0x10B	uint8 Mfx_Div_s32s32_u8(sint32 , sint32);
0x10C	sint8 Mfx_Div_s32s32_s8(sint32 , sint32);
0x10D	uint16 Mfx_Div_s32s32_u16(sint32 , sint32);
0x10E	sint16 Mfx_Div_s32s32_s16(sint32 , sint32);
0x10F	sint32 Mfx_Div_s32s32_s32(sint32 , sint32);
0x110	sint8 Mfx_Div_u32u32_s8(uint32 , uint32);
0x111	sint16 Mfx_Div_u32u32_s16(uint32 , uint32);
0x112	sint32 Mfx_Div_u32u32_s32(uint32 , uint32);
0x113	sint8 Mfx_Div_s32u32_s8(sint32 , uint32);
0x114	uint16 Mfx_Div_s32u32_u16(sint32 , uint32);
0x115	uint8 Mfx_Div_s32u32_u8(sint32 , uint32);
0x116	sint16 Mfx_Div_u32s32_s16(uint32 , sint32);
0x117	sint8 Mfx_Div_u32s32_s8(uint32 , sint32);
0x118	uint16 Mfx_Div_u32s32_u16(uint32 , sint32);
0x119	uint8 Mfx_Div_u32s32_u8(uint32 , sint32);
0x11A	uint16 Mfx_Div_u32u32_u16(uint32 , uint32);
0x11B	uint8 Mfx_Div_u32u32_u8(uint32 , uint32);
0x11C	sint16 Mfx_Div_u16u16_s16(uint16 , uint16);
0x11D	uint8 Mfx_Div_u16u16_u8(uint16 , uint16);
0x11E	uint8 Mfx_Div_u16s16_u8(uint16 , sint16);
0x11F	uint8 Mfx_Div_s16u16_u8(sint16 , uint16);
0xA80	sint8 Mfx_Div_u16u16_s8(uint16 , uint16);
0xA81	sint8 Mfx_Div_u16s16_s8(uint16 , sint16);
0xA82	sint8 Mfx_Div_s16u16_s8(sint16 , uint16);
0xA83	sint8 Mfx_Div_u8u8_s8(uint8 , uint8);

Note: The function prototypes with the (*) in the above table, are already covered by the 32 bit services function prototypes (e;g; Mfx_Div_s32u16_u16 --> Mfx_Div_s32u32_u16). Therefore, these functions will be removed in a major future release] ()



8.5.7 Divisions rounded off

[SWS_Mfx_00032] [

Service name:	Mfx_RDiv_ <intypemn1><intypei< th=""><th>Mn2>_<outtypemn></outtypemn></th></intypei<></intypemn1>	Mn2>_ <outtypemn></outtypemn>
Syntax:	<pre><outtype> Mfx_RDiv_<intyp< th=""><th>eMn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></th></intyp<></outtype></pre>	eMn1> <intypemn2>_<outtypemn>(</outtypemn></intypemn2>
Service ID[hex]:	0x120 to 0x142	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Paramatara (in)	x_value	First argument
Parameters (in):	y_value	Second argument
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	These routines make a division be	etween the two arguments.

1 ()

[SWS_Mfx_00033][

These routines make a division between the two arguments:

Return-value = x_value / y_value

]()

[SWS_Mfx_00034][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS Mfx 00035][

The result after division by zero is defined by:

- * If x value ≥ 0 then the function returns the maximum value of the output type
- * If x_value < 0 then the function returns the minimum value of the output type |()

[SWS_Mfx_00037][

The result is rounded off.

I()

[SWS_Mfx_00038] [

Function ID[hex]	Function prototype : RDiv
0x120	uint8 Mfx_RDiv_u8u8_u8(uint8 , uint8);
0x121	uint8 Mfx_RDiv_s8u8_u8(sint8 , uint8);
0x122	uint8 Mfx_RDiv_u8s8_u8(uint8 , sint8);
0x123	uint8 Mfx_RDiv_s8s8_u8(sint8 , sint8);
0x124	sint8 Mfx_RDiv_u8s8_s8(uint8 , sint8);
0x125	sint8 Mfx_RDiv_s8u8_s8(sint8 , uint8);
0x126	sint8 Mfx_RDiv_s8s8_s8(sint8 , sint8);



0x127	uint16 Mfx_RDiv_u16u16_u16(uint16 , uint16);
0x128	uint16 Mfx_RDiv_s16u16_u16(sint16 , uint16);
0x129	uint16 Mfx_RDiv_u16s16_u16(uint16 , sint16);
0x128	sint16 Mfx_RDiv_u16s16_s16(uint16 , sint16);
0x12A 0x12B	· · · · · · · · · · · · · · · · · · ·
	sint16 Mfx_RDiv_s16u16_s16(sint16 , uint16);
0x12C	uint16 Mfx_RDiv_s16s16_u16(sint16 , sint16);
0x12D	uint8 Mfx_RDiv_s16s16_u8(sint16 , sint16);
0x12E	sint8 Mfx_RDiv_s16s16_s8(sint16 , sint16);
0x12F	sint16 Mfx_RDiv_s16s16_s16(sint16 , sint16);
0x130 (*)	uint16 Mfx_RDiv_s32u16_u16(sint32 , uint16);
0x131 (*)	sint16 Mfx_RDiv_s32s16_s16(sint32 , sint16);
0x132	sint16 Mfx_RDiv_s32u32_s16(sint32 , uint32);
0x133 (*)	uint32 Mfx_RDiv_u32u16_u32(uint32 , uint16);
0x134 (*)	uint16 Mfx_RDiv_u32u16_u16(uint32 , uint16);
0x135	uint32 Mfx_RDiv_u32u32_u32(uint32 , uint32);
0x136	uint32 Mfx_RDiv_s32u32_u32(sint32 , uint32);
0x137	uint32 Mfx_RDiv_u32s32_u32(uint32 , sint32);
0x138	sint32 Mfx_RDiv_u32s32_s32(uint32 , sint32);
0x139	sint32 Mfx_RDiv_s32u32_s32(sint32 , uint32);
0x13A	uint32 Mfx_RDiv_s32s32_u32(sint32 , sint32);
0x13B	uint8 Mfx_RDiv_s32s32_u8(sint32 , sint32);
0x13C	sint8 Mfx_RDiv_s32s32_s8(sint32 , sint32);
0x13D	uint16 Mfx_RDiv_s32s32_u16(sint32 , sint32);
0x13E	sint16 Mfx_RDiv_s32s32_s16(sint32 , sint32);
0x13F	sint32 Mfx_RDiv_s32s32_s32(sint32 , sint32);
0x140	sint8 Mfx_RDiv_u32u32_s8(uint32 , uint32);
0x141	sint16 Mfx_RDiv_u32u32_s16(uint32 , uint32);
0x142	sint32 Mfx_RDiv_u32u32_s32(uint32 , uint32);
0x143	sint8 Mfx_RDiv_s32u32_s8(sint32 , uint32);
0x144	uint16 Mfx_RDiv_s32u32_u16(sint32 , uint32);
0x145	uint8 Mfx_RDiv_s32u32_u8(sint32 , uint32);
0x146	sint16 Mfx_RDiv_u32s32_s16(uint32 , sint32);
0x140	sint8 Mfx_RDiv_u32s32_s8(uint32 , sint32);
0x148	uint16 Mfx_RDiv_u32s32_u16(uint32 , sint32);
0x148	uint8 Mfx_RDiv_u32s32_u16(uint32 , sint32);
0x149 0x14A	uint16 Mfx_RDiv_u32u32_u16(uint32 , uint32);
0x14A 0x14B	uint8 Mfx_RDiv_u32u32_u8(uint32 , uint32);
0x14B 0x14C	sint16 Mfx_RDiv_u16u16_s16(uint16 , uint16);
0x14C	uint8 Mfx_RDiv_u16u16_u8(uint16 , uint16);
0x14E	uint8 Mfx_RDiv_u16s16_u8(uint16 , uint16);
0x14F	uint8 Mfx_RDiv_s16u16_u8(sint16 , uint16);
0xA70	sint8 Mfx_RDiv_u16u16_s8(uint16 , uint16);
0xA71	sint8 Mfx_RDiv_u16s16_s8(uint16 , sint16);
0xA72	sint8 Mfx_RDiv_s16u16_s8(sint16 , uint16);
0xA73	sint8 Mfx_RDiv_u8u8_s8(uint8 , uint8);

Note: The function prototypes with the (*) in the above table, are already covered by the 32 bit services function prototypes (e;g; Mfx_RDiv_s32u16_u16 --> Mfx_RDiv_s32u32_u16). Therefore, these functions will be removed in a major future release.

]()



8.5.8 Combinations of multiplication and division rounded towards 0

[SWS_Mfx_00039] [

Service name:	Mfx_MulDiv_ <intypemn1><int< th=""><th>ypeMn2><intypemn3>_<outtypemn></outtypemn></intypemn3></th></int<></intypemn1>	ypeMn2> <intypemn3>_<outtypemn></outtypemn></intypemn3>
Syntax:	<pre><outtype> Mfx_MulDiv_<intypemn1><</intypemn1></outtype></pre>	InTypeMn2> <intypemn3>_<outtypemn>(</outtypemn></intypemn3>
Service ID[hex]:	0x150 to 0x162	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
	x_value	First argument
Parameters (in):	y_value	Second argument
	z_value	Third argument
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	These routines make a multiplic the third argument.	cation between the two arguments and a division by

I()

[SWS_Mfx_00040][

These routines make a multiplication between the two arguments and a division by the third argument:

Return-value = x_value * y_value / z_value |()

[SWS_Mfx_00041][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

I()

[SWS_Mfx_00042][

The result after division by zero is defined by:

- * If $x_value y_value \ge 0$ then the function returns the maximum value of the output type
- * If x_value*y_value < 0 then the function returns the minimum value of the output type

|()

[SWS_Mfx_00044][

The result is rounded towards 0. I()

[SWS_Mfx_00045] [

Function ID[hex]	Function prototype : Div
0x150	uint16 Mfx_MulDiv_s32s32s32_u16(sint32 , sint32 , sint32);



0x151	sint16 Mfx_MulDiv_s32s32s32_s16(sint32 , sint32 , sint32);
0x152	uint16 Mfx_MulDiv_u32u32u16_u16(uint32 , uint32 , uint16);
0x153	sint16 Mfx_MulDiv_s32s32s16_s16(sint32 , sint32 , sint16);
0x154	uint16 Mfx_MulDiv_s16u16s16_u16(sint16 , uint16 , sint16);
0x155	uint16 Mfx_MulDiv_s16u16u16_u16(sint16 , uint16 , uint16);
0x156	uint16 Mfx_MulDiv_u16u16u16_u16(uint16 , uint16 , uint16);
0x157	sint16 Mfx_MulDiv_s16u16s16_s16(sint16 , uint16 , sint16);
0x158	sint16 Mfx_MulDiv_s16s16u16_s16(sint16 , sint16 , uint16);
0x159	sint16 Mfx_MulDiv_s16u16u16_s16(sint16 , uint16 , uint16);
0x15A	sint16 Mfx_MulDiv_s16s16s16_s16(sint16 , sint16 , sint16);
0x15B	uint32 Mfx_MulDiv_u32u32u32_u32(uint32 , uint32 , uint32);
0x15C	uint32 Mfx_MulDiv_u32u32s32_u32(uint32 , uint32 , sint32);
0x15D	uint32 Mfx_MulDiv_u32s32u32_u32(uint32 , sint32 , uint32);
0x15E	uint32 Mfx_MulDiv_u32s32s32_u32(uint32 , sint32 , sint32);
0x15F	sint32 Mfx_MulDiv_s32s32u32_s32(sint32 , sint32 , uint32);
0x160	sint32 Mfx_MulDiv_s32u32s32_s32(sint32 , uint32 , sint32);
0x161	sint32 Mfx_MulDiv_s32u32u32_s32(sint32 , uint32 , uint32);
0x162	sint32 Mfx_MulDiv_s32s32s32_s32(sint32 , sint32 , sint32);
0x163	uint16 Mfx_MulDiv_u32u32u32_u16(uint32 , uint32 , uint32);
0x164	uint16 Mfx_MulDiv_u16s16s16_u16(uint16 , sint16 , sint16);
0x165	uint16 Mfx_MulDiv_u16s16u16_u16(uint16 , sint16 , uint16);
0x166	sint16 Mfx_MulDiv_u16s16s16_s16(uint16 , sint16 , sint16);
0x167	sint16 Mfx_MulDiv_u16s16u16_s16(uint16 , sint16 , uint16);
0x168	sint32 Mfx_MulDiv_u32s32s32_s32(uint32 , sint32 , sint32);
0x169	sint32 Mfx_MulDiv_u32s32u32_s32(uint32 , sint32 , uint32);

Note: The redundancy due to commutativity will be reduced in the next version] ()

8.5.9 Combinations of multiplication and division rounded off

[SWS_Mfx_00046] [

Mfx_RMulDiv_ <intypemn1><ir< th=""><th>nTvpeMn2><intvpemn3> <outtvpemn></outtvpemn></intvpemn3></th></ir<></intypemn1>	nTvpeMn2> <intvpemn3> <outtvpemn></outtvpemn></intvpemn3>	
	Mfx_RMulDiv_ <intypemn1><intypemn2><intypemn3>_<outtypemn></outtypemn></intypemn3></intypemn2></intypemn1>	
<pre><outtype> Mfx_RMulDiv_<intypemn1><intypemn2><intypemn3>_<outtypemn>(</outtypemn></intypemn3></intypemn2></intypemn1></outtype></pre>		
0x170 to 0x182		
Synchronous		
Reentrant		
x_value	First argument	
y_value	Second argument	
z_value	Third argument	
None		
None		
<outtype> Result of the calculation</outtype>		
These routines make a multiplication between the two arguments and a division by the third argument.		
	Mfx_RMulDiv_ <intypemn1></intypemn1>	

] ()



[SWS_Mfx_00047][

These routines make a multiplication between the two arguments and a division by the third argument:

Return-value = x_value * y_value / z_value |()

[SWS_Mfx_00048][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()|

[SWS_Mfx_00049][

The result after division by zero is defined by:

- * If x_value*y_value ≥ 0 then the function returns the maximum value of the output type
- * If x_value*y_value < 0 then the function returns the minimum value of the output type |()

[SWS_Mfx_00051][

The result is rounded off.

(()

[SWS_Mfx_00052] [

Function ID[hex]	Function prototype : RDiv
0x170	uint16 Mfx_RMulDiv_s32s32s32_u16(sint32 , sint32 , sint32);
0x171	sint16 Mfx_RMulDiv_s32s32s32_s16(sint32 , sint32 , sint32);
0x172	uint16 Mfx_RMulDiv_u32u32u16_u16(uint32 , uint32 , uint16);
0x173	sint16 Mfx_RMulDiv_s32s32s16_s16(sint32 , sint32 , sint16);
0x174	uint16 Mfx_RMulDiv_s16u16s16_u16(sint16 , uint16 , sint16);
0x175	uint16 Mfx_RMulDiv_s16u16u16_u16(sint16 , uint16 , uint16);
0x176	uint16 Mfx_RMulDiv_u16u16u16_u16(uint16 , uint16 , uint16);
0x177	sint16 Mfx_RMulDiv_s16u16s16_s16(sint16 , uint16 , sint16);
0x178	sint16 Mfx_RMulDiv_s16s16u16_s16(sint16 , sint16 , uint16);
0x179	sint16 Mfx_RMulDiv_s16u16u16_s16(sint16 , uint16 , uint16);
0x17A	sint16 Mfx_RMulDiv_s16s16s16_s16(sint16 , sint16 , sint16);
0x17B	uint32 Mfx_RMulDiv_u32u32u32_u32(uint32 , uint32 , uint32);
0x17C	uint32 Mfx_RMulDiv_u32u32s32_u32(uint32 , uint32 , sint32);
0x17D	uint32 Mfx_RMulDiv_u32s32u32_u32(uint32 , sint32 , uint32);
0x17E	uint32 Mfx_RMulDiv_u32s32s32_u32(uint32 , sint32 , sint32);
0x17F	sint32 Mfx_RMulDiv_s32s32u32_s32(sint32 , sint32 , uint32);
0x180	sint32 Mfx_RMulDiv_s32u32s32_s32(sint32 , uint32 , sint32);
0x181	sint32 Mfx_RMulDiv_s32u32u32_s32(sint32 , uint32 , uint32);
0x182	sint32 Mfx_RMulDiv_s32s32s32_s32(sint32 , sint32 , sint32);
0x183	uint16 Mfx_RMulDiv_u32u32u32_u16(uint32 , uint32 , uint32);
0x184	uint16 Mfx_RMulDiv_u16s16s16_u16(uint16 , sint16 , sint16);
0x185	uint16 Mfx_RMulDiv_u16s16u16_u16(uint16 , sint16 , uint16);
0x186	sint16 Mfx_RMulDiv_u16s16s16_s16(uint16 , sint16 , sint16);
0x187	sint16 Mfx_RMulDiv_u16s16u16_s16(uint16 , sint16 , uint16);



0x188	sint32 Mfx_RMulDiv_u32s32s32_s32(uint32 , sint32 , sint32);
0x189	sint32 Mfx_RMulDiv_u32s32u32_s32(

Note: The redundancy due to commutativity will be reduced in the next version] ()

8.5.10 Combinations of multiplication and shift right

[SWS_Mfx_00053] [

Service name:	Mfx_MulShRight_ <intypemn1></intypemn1>	<intypemn2>_<outtypemn></outtypemn></intypemn2>
Syntax:	<pre><outtype> Mfx_MulShRight_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x190 to 0x205	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
	x_value	First argument
Parameters (in):	y_value	Second argument
	shift	Third argument
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	This routine makes a multiplication between the two arguments and applies a shift right defined by the third argument.	

I()

[SWS_Mfx_00054][

This routine makes a multiplication between the two arguments and applies a shift right defined by the third argument:

Return-value = (x_value * y_value) >> shift]()

[SWS_Mfx_00055][

We precise that for the shift right of a negative number, we always keep the bit of sign.

1()

[SWS_Mfx_00056][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS_Mfx_00057] [

Function ID[hex]	Function prototype	Associated maximum shift
0x190	uint8 Mfx_MulShRight_s16s16u8_u8(sint16 , sint16 , uint8);	30
0x191	sint8 Mfx_MulShRight_s16s16u8_s8(sint16 , sint16 , uint8);	30



0x192	sint16 Mfx_MulShRight_s16s16u8_s16(sint16 , sint16 , uint8);	30
0x193	uint16 Mfx_MulShRight_s16s16u8_u16(sint16 , sint16 , uint8);	30
0x194	uint8 Mfx_MulShRight_u32s32u8_u8(uint32 , sint32 , uint8);	63
0x195	sint8 Mfx_MulShRight_u32s32u8_s8(uint32 , sint32 , uint8);	63
0x196	uint16 Mfx_MulShRight_u32s32u8_u16(uint32 , sint32 , uint8);	63
0x197	sint16 Mfx_MulShRight_u32s32u8_s16(uint32 , sint32 , uint8);	63
0x198	uint32 Mfx_MulShRight_u32s32u8_u32(uint32 , sint32 , uint8);	63
0x199	sint32 Mfx_MulShRight_u32s32u8_s32(uint32 , sint32 , uint8);	63
0x19A	sint8 Mfx_MulShRight_s32s32u8_s8(sint32 , sint32 , uint8);	62
0x19B	uint8 Mfx_MulShRight_s32s32u8_u8(sint32 , sint32 , uint8);	62
0x19C	sint16 Mfx_MulShRight_s32s32u8_s16(sint32 , sint32 , uint8);	62
0x19D	uint16 Mfx_MulShRight_s32s32u8_u16(sint32 , sint32 , uint8);	62
0x19E	uint32 Mfx_MulShRight_s32s32u8_u32(sint32 , sint32 , uint8);	62
0x19F	sint32 Mfx_MulShRight_s32s32u8_s32(sint32 , sint32 , uint8);	62
0x200	uint8 Mfx_MulShRight_u32u32u8_u8(uint32 , uint32 , uint8);	63
0x201	sint8 Mfx_MulShRight_u32u32u8_s8(uint32 , uint32 , uint8);	63
0x202	uint16 Mfx_MulShRight_u32u32u8_u16(uint32 , uint32 , uint8);	63
0x203	sint16 Mfx_MulShRight_u32u32u8_s16(uint32 , uint32 , uint8);	63
0x204	uint32 Mfx_MulShRight_u32u32u8_u32(uint32 , uint32 , uint8);	63
0x205	sint32 Mfx_MulShRight_u32u32u8_s32(uint32 , uint32 , uint8);	63

If you want to see an example of the use of these functions, see 8.7.1 .

]()

8.5.11 Combinations of division and shift left

[SWS_Mfx_00058] [

Service name:	Mfx_DivShLeft_ <intypemn1><i< th=""><th>nTypeMn2>u8_<outtypemn></outtypemn></th></i<></intypemn1>	nTypeMn2>u8_ <outtypemn></outtypemn>
Syntax:	<pre></pre> <pre><outtype> Mfx_DivShLeft_<intypemn1><intypemn2>u8_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x210 to 0x218	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
	x_value	First argument
Parameters (in):	y_value	Second argument
	shift	Third argument
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	Result of the calculation
Description:	This routine applies a shift left defined by the third argument to the first argument, and then makes a division by the second argument.	

] ()



[SWS_Mfx_00059][

This routine applies a shift left defined by the third argument to the first argument, and then makes a division by the second argument:

Return-value = (x_value << shift) / y_value |()

[SWS_Mfx_00060][

We precise that for the shift left of a negative number, we always keep the bit of sign. J()

[SWS_Mfx_00061][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

1()

[SWS_Mfx_00062][

The result after division by zero is defined by:

- * If $x_value \ge 0$ then the function returns the maximum value of the output type
- * If x_value < 0 then the function returns the minimum value of the output type |()

[SWS_Mfx_00064] [

Function ID[hex]	Function prototype	Associated maximum shift
0x210	uint8 Mfx_DivShLeft_u8u8u8_u8(uint8 , uint8 , uint8);	7
0x211	uint8 Mfx_DivShLeft_u16u16u8_u8(uint16 , uint16 , uint8);	15
0x212	uint16 Mfx_DivShLeft_u16u16u8_u16(uint16 , uint16 , uint8);	15
0x213	sint16 Mfx_DivShLeft_s16s16u8_s16(sint16 , sint16 , uint8);	15
0x214	sint16 Mfx_DivShLeft_s16u16u8_s16(sint16 , uint16 , uint8);	15
0x215	uint16 Mfx_DivShLeft_u32u32u8_u16(uint32 , uint32 , uint8);	31
0x216	uint32 Mfx_DivShLeft_u32u32u8_u32(uint32 , uint32 , uint8);	31
0x217	sint32 Mfx_DivShLeft_s32s32u8_s32(sint32 , sint32 , uint8);	31
0x218	sint32 Mfx_DivShLeft_s32u32u8_s32(sint32 , uint32 , uint8);	31
0xA30	uint8 Mfx_DivShLeft_u32s32u8_u8(uint32 , sint32 , uint8);	31
0xA31	sint8 Mfx_DivShLeft_u32s32u8_s8(uint32 , sint32 , uint8);	31
0xA32	uint16 Mfx_DivShLeft_u32s32u8_u16(uint32 , sint32 , uint8);	31
0xA33	sint16 Mfx_DivShLeft_u32s32u8_s16(uint32 , sint32 , uint8);	31
0xA34	uint32 Mfx_DivShLeft_u32s32u8_u32(uint32 , sint32 , uint8);	31
0xA35	sint32 Mfx_DivShLeft_u32s32u8_s32(uint32 , sint32 , uint8);	31
0xA36	sint8 Mfx_DivShLeft_s32s32u8_s8(sint32 , sint32 , uint8);	31
0xA37	uint8 Mfx_DivShLeft_s32s32u8_u8(sint32 , sint32 , uint8);	31
0xA38	sint16 Mfx_DivShLeft_s32s32u8_s16(sint32 , sint32 , uint8);	31
0xA39	uint16 Mfx_DivShLeft_s32s32u8_u16(sint32 , sint32 , uint8);	31
0xA3A	uint32 Mfx_DivShLeft_s32s32u8_u32(sint32 , sint32 , uint8);	31
0xA3B	uint8 Mfx_DivShLeft_u32u32u8_u8(uint32 , uint32 , uint8);	31
0xA3C	sint8 Mfx_DivShLeft_u32u32u8_s8(uint32 , uint32 , uint8);	31
0xA3D	sint16 Mfx_DivShLeft_u32u32u8_s16(uint32 , uint32 , uint8);	31
0xA3E	sint32 Mfx_DivShLeft_u32u32u8_s32(uint32 , uint32 , uint8);	31



0xA3F	uint8 Mfx_DivShLeft_s32u32u8_u8(sint32 , uint32 , uint8);	31
0xA40	sint8 Mfx_DivShLeft_s32u32u8_s8(sint32 , uint32 , uint8);	31
0xA41	uint16 Mfx_DivShLeft_s32u32u8_u16(sint32 , uint32 , uint8);	31
0xA42	sint16 Mfx_DivShLeft_s32u32u8_s16(sint32 , uint32 , uint8);	31
0xA43	uint32 Mfx_DivShLeft_s32u32u8_u32(sint32 , uint32 , uint8);	31

If you want to see an example of the use of these functions, see 8.7.2.

]()

8.5.12 Modulo

[SWS Mfx 00065] [

Service name:	Mfx_Mod_ <typemn></typemn>		
Syntax:	<type> Mfx Mod <typemn>(</typemn></type>		
	<type> x value,</type>		
	<type> y_</type>	value	
)		
Service ID[hex]:	0x220 to 0x225		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Doromotoro (in)	x_value	First argument	
Parameters (in):	y_value	Second argument	
Parameters (in-	None		
out):			
Parameters (out):	None		
Return value:	<type></type>	Result of the calculation	
Description:	This routine retur	This routine returns the remainder of the division x_value / y_value if y_value is	
•	not zero.	_ ,_ ,_	

I()

[SWS_Mfx_00066][

If y_value is zero, the result is zero.

1()

[SWS_Mfx_00068][

In other cases, Return-value = x_value mod y_value |()

[SWS_Mfx_00069][

The sign of the remainder is the same than the sign of x_value .]()

[SWS_Mfx_00070][

Function ID[hex]	Function prototype
0x220	uint8 Mfx_Mod_u8(uint8 , uint8);
0x221	sint8 Mfx_Mod_s8(sint8 , sint8);
0x222	uint16 Mfx_Mod_u16(uint16 , uint16);
0x223	sint16 Mfx_Mod_s16(sint16 , sint16);
0x224	uint32 Mfx_Mod_u32(uint32 , uint32);
0x225	sint32 Mfx_Mod_s32(sint32 , sint32);
0x226	uint8 Mfx_Mod_u32u32_u8(uint32 , uint32)



0x227	sint8 Mfx_Mod_s32s32_s8(sint32 , sint32)
0x228	uint16 Mfx_Mod_u32u32_u16(uint32 , uint32)
0x229	sint16 Mfx_Mod_s32s32_s16(sint32 , sint32)

8.5.13 Limiting

[SWS_Mfx_00073] [

2442_INIX_00013]		
Mfx_Limit_ <typemn></typemn>		
<type> Mfx Limit <typemn>(</typemn></type>		
<type> value,</type>		
<type< th=""><th>e> min value,</th></type<>	e> min value,	
<type< th=""><th>e> max_value</th></type<>	e> max_value	
)	_	
0x230 to 0x235		
Synchronous		
Reentrant		
value	input value.	
min_value	Lower Bound. min_value shall not be strictly greater than max_value.	
max_value	Upper Bound. max_value shall not be strictly lower than min_value.	
None		
None		
<type></type>	Result of the calculation	
This routine limits the input value between Lower Bound and Upper Bound.		
	Mfx_Limit_ <type> Mf <type< p=""> <type <="" <type="" p=""> Ox230 to 0x Synchronou Reentrant value min_value max_value None <type></type></type></type<></type>	

] ()

[SWS_Mfx_00074][

Return-value = min_value if value < min_value |()

[SWS_Mfx_00075][

Return-value = max_value if value > max_value | ()

[SWS_Mfx_00076][

Return-value = value in the other cases J()

[SWS_Mfx_00079][

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x230	uint8 Mfx_Limit_u8(uint8 , uint8, uint8);
0x231	sint8 Mfx_Limit_s8(sint8 , sint8, sint8);
0x232	uint16 Mfx_Limit_u16(uint16 , uint16, uint16);
0x233	sint16 Mfx_Limit_s16(sint16 , sint16, sint16);
0x234	uint32 Mfx_Limit_u32(uint32 , uint32, uint32);
0x235	sint32 Mfx_Limit_s32(sint32 , sint32, sint32);

| ()

8.5.14 Limitations with only one value for minimum and maximum

[SWS_Mfx_00082] [



Service name:	Mfx_Minmax_ <typemn></typemn>	
Syntax:	<type> Mfx Minmax <typemn>(</typemn></type>	
	<type> value,</type>	
	<type> minmax_value</type>	
)	
Service ID[hex]:	0x240 to 0x245	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Doromotoro (in)	value	First argument
Parameters (in):	minmax_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<type></type>	Result of the calculation
Description:	The routine limits a value to a minimum or a maximum that depends on the sign of the minmax_value.	

1 ()

The result value is:

[SWS_Mfx_00083][

minmax_value if minmax_value ≥ 0 and value > minmax_value]()

[SWS_Mfx_00084][

minmax_value if minmax_value < 0 and value < minmax_value |()

[SWS_Mfx_00085][

value in the other cases |()

[SWS_Mfx_00086] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x240	uint8 Mfx_Minmax_u8(uint8 , uint8);
0x241	sint8 Mfx_Minmax_s8(sint8 , sint8);
0x242	uint16 Mfx_Minmax_u16(uint16 , uint16);
0x243	sint16 Mfx_Minmax_s16(sint16 , sint16);
0x244	uint32 Mfx_Minmax_u32(uint32 , uint32);
0x245	sint32 Mfx_Minmax_s32(sint32 , sint32);

]()

8.5.15 Minimum and maximum

[SWS_Mfx_00090] [

Sorvice name:	Mfw Min TypoMps	
Service name:	Mfx_Min_ <typemn></typemn>	
Syntax:	<type> Mfx_Min_<typemn>(</typemn></type>	
	<type> x_value,</type>	
	<type> y_value</type>	
Service ID[hex]:	0x250 to 0x255	



Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	x_value	First argument
	y_value	Second argument
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	<type></type>	Result of the calculation
Description:	This routine returns the minimum between two values.	

[SWS_Mfx_00091][

Return-value = x_value if x_value < y_value | ()

[SWS_Mfx_00092][

Return-value = y_value in the other case |()

[SWS_Mfx_00093] [

2M2_MIX_000a3]			
Service name:	Mfx_Max_ <typemn></typemn>		
Syntax:	<type> Mfx_Max_<</type>	<type> Mfx Max <typemn>(</typemn></type>	
	<type> x val</type>	ue,	
	<type> y val</type>	ue	
)		
Service ID[hex]:	0x256 to 0x25b		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	x_value	First argument	
Parameters (m).	y_value	Second argument	
Parameters (in-	None		
out):			
Parameters (out):	None		
Return value:	<type></type>	Result of the calculation	
Description:	This routine returns the maximum between two values.		

1 ()

[SWS_Mfx_00094][

Return-value = x_value if x_value > y_value | ()

[SWS_Mfx_00095][

Return-value = y_value in the other case |()

[SWS_Mfx_00096] [

Here is the list of implemented functions.



Function ID[hex]	Function prototype
0x250	uint8 Mfx_Min_u8(uint8 , uint8);
0x251	sint8 Mfx_Min_s8(sint8 , sint8);
0x252	uint16 Mfx_Min_u16(uint16 , uint16);
0x253	sint16 Mfx_Min_s16(sint16 , sint16);
0x254	uint32 Mfx_Min_u32(uint32 , uint32);
0x255	sint32 Mfx_Min_s32(sint32 , sint32);
0x256	uint8 Mfx_Max_u8(uint8 , uint8);
0x257	sint8 Mfx_Max_s8(sint8 , sint8);
0x258	uint16 Mfx_Max_u16(uint16 , uint16);
0x259	sint16 Mfx_Max_s16(sint16 , sint16);
0x25A	uint32 Mfx_Max_u32(uint32 , uint32);
0x25B	sint32 Mfx_Max_s32(sint32 , sint32);

8.6 2ⁿ Scaled Integer Math Functions

For all the following functions, upper case letters will be used for operands, and lower case letters will be used for radix.

For example:

- "x" is the operand, "a" is the parameter that represents its radix,
- "C" is the result, "c" is the parameter for its radix.

A Radix will always be a signed integer on 16 bits (sint16). For that reason, the mnemonic will not appear in the name of the functions in order to have shorter names.

For all operations, the valid range is given for information. Indeed, operations with parameters outside of the valid range will be saturated within the range of the output type. It can help for optimization purpose.

8.6.1 Conversion

8.6.1.1 16-Bit to 8-Bit 2ⁿ Scaled Integer Conversion

[SWS_Mfx_00100] [

Service name:	Mfx_ConvertP2_ <in< th=""><th colspan="2">Mfx_ConvertP2_<intypemn>_<outtypemn></outtypemn></intypemn></th></in<>	Mfx_ConvertP2_ <intypemn>_<outtypemn></outtypemn></intypemn>	
Syntax:	<pre><outtype> Mfx_ConvertP2_<intypemn>_<outtypemn>(</outtypemn></intypemn></outtype></pre>		
Service ID[hex]:	0x260 to 0x261		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	x Int	teger value of the fixed-point operand.	
		adix point position of the fixed point operand. ust be a constant expression.	
	Mı	adix point position of the fixed point result. lust be a constant expression. alid range: -15 ≤ (c - a) ≤ 7	



Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype> =2^(c-a) * x</outtype>	
Description:	The routine converts a scaled 16-bit integer to a scaled 8-bit integer.	

[SWS_Mfx_00101][

The function returns the integer value of the fixed point conversion (C), determined by $C = 2^{(c-a)} x$.

[SWS_Mfx_00102][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS_Mfx_00103][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00104][

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x260	uint8 Mfx_ConvertP2_u16_u8(uint16 x, sint16 a, sint16 c)
0x261	sint8 Mfx_ConvertP2_s16_s8(sint16 x, sint16 a, sint16 c)

] ()

8.6.1.2 8-Bit to 16-Bit 2ⁿ Scaled Integer Conversion

[SWS_Mfx_00106] [

Service name:	Mfx_ConvertP2_	Mfx_ConvertP2_ <intypemn>_<outtypemn></outtypemn></intypemn>		
Syntax:	<pre><outtype> Mfx_ConvertP2_<intypemn>_<outtypemn>(</outtypemn></intypemn></outtype></pre>			
Service ID[hex]:	0x26a to 0x26b			
Sync/Async:	Synchronous			
Reentrancy:	Reentrant			
	х	Integer value of the fixed-point operand.		
Parameters (in):		Radix point position of the fixed point operand. Must be a constant expression.		
rarameters (m).		Radix point position of the fixed point result. Must be a constant expression. Valid range: -7 ≤ (c - a) ≤ 15		
Parameters (in- out):	None			
	None			
Return value:	<outtype></outtype>	=2^(c-a) * x		
Description:	The routine converts a scaled 8-bit integer to a scaled 16-bit integer.			

] ()



[SWS_Mfx_00107][

The function returns the integer value of the fixed point conversion (C), determined by $C = 2^{(c-a)} x$.

[SWS_Mfx_00108][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()

[SWS_Mfx_00109][

If it is necessary to round the result of this equation, it is rounded toward zero. J()

[SWS_Mfx_00110] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x26A	uint16 Mfx_ConvertP2_u8_u16(uint8 x, sint16 a, sint16 c)
0x26B	sint16 Mfx_ConvertP2_s8_s16 (sint8 x, sint16 a, sint16 c)

| ()

8.6.1.3 32-Bit to 16-Bit 2ⁿ Scaled Integer Conversion

[SWS Mfx 00112] [

<u>[3W3_WIIX_UUTT</u>	~]		
Service name:	Mfx_ConvertP2_	Mfx_ConvertP2_ <intypemn>_<outtypemn></outtypemn></intypemn>	
Syntax:	<pre><outtype> Mfx_ConvertP2_<intypemn>_<outtypemn>(</outtypemn></intypemn></outtype></pre>		
Service ID[hex]:	0x270 to 0x271		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	x a c	Integer value of the fixed-point operand. Radix point position of the fixed point operand. Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: -31 ≤ (c - a) ≤ 15	
Parameters (in- out):	None		
Parameters (out):	None		
Return value:	<outtype></outtype>	=2^(c-a) * x	
Description:	The routine converts a scaled 32-bit integer to a scaled 16-bit integer.		

| () |

[SWS_Mfx_00113][

The function returns the integer value of the fixed point conversion (C), determined by $C = 2^{(c-a)} * x$.

]()

[SWS_Mfx_00114][



Return-value shall be saturated to boundary values in the event of negative or positive overflow.

I()

[SWS_Mfx_00115][

If it is necessary to round the result of this equation, it is rounded toward zero.]()

[SWS_Mfx_00116] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x270	uint16 Mfx_ConvertP2_u32_u16 (uint32 x, sint16 a, sint16 c)
0x271	sint16 Mfx_ConvertP2_s32_s16 (sint32 x, sint16 a, sint16 c)

1 ()

8.6.1.4 16-Bit to 32-Bit 2ⁿ Scaled Integer Conversion

[SWS_Mfx_00118] [

[0110 _IIIIX_0011	~]		
Service name:	Mfx_ConvertP2_ <intypemn>_<outtypemn></outtypemn></intypemn>		
Syntax:	<pre><outtype> Mfx_ConvertP2_<intypemn>_<outtypemn>(</outtypemn></intypemn></outtype></pre>		
	sint16 a, sint16 c		
Service ID[hex]:	0x27a to 0x27b		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
	х	Integer value of the fixed-point operand.	
Parameters (in):	a	Radix point position of the fixed point operand. Must be a constant expression.	
	С	Radix point position of the fixed point result. Must be a constant expression. Valid range: -15 ≤ (c - a) ≤ 31	
Parameters (in- out):	None		
Parameters (out):	None		
Return value:	<outtype></outtype>	=2^(c-a) * x	
Description:	The routine converts a scaled 16-bit integer to a scaled 32-bit integer.		

I()

[SWS_Mfx_00119][

The function returns the integer value of the fixed point conversion (C), determined by $C = 2^{(c-a)} x$.

[SWS_Mfx_00120][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

1()

[SWS_Mfx_00121][

If it is necessary to round the result of this equation, it is rounded toward zero.



|()

[SWS_Mfx_00122] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x27A	uint32 Mfx_ConvertP2_u16_u32(uint16 x, sint16 a, sint16 c)
0x27B	sint32 Mfx_ConvertP2_s16_s32(sint16 x, sint16 a, sint16 c)

1 ()

8.6.2 Multiplication

8.6.2.1 16-Bit Multiplication of 2ⁿ Scaled Integer

[SWS Mfx 00124] [

<u>[3883_BHIX_UU12</u>	·~」		
Service name:	Mfx_MulP2_<	InTypeMn1> <intypemn2>_<outtypemn></outtypemn></intypemn2>	
Syntax:	<pre><outtype> Mfx_MulP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>		
Service ID[hex]:	0x280 to 0x28	5	
Sync/Async:	Synchronous		
Reentrancy:	Reentrant	Reentrant	
Parameters (in):	y a b	Integer value of the fixed point operand. Integer value of the fixed point operand. Radix point position of the first fixed point operand. Must be a constant expression. Radix point position of the second fixed point operand. Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: -31 ≤ (c - b - a) ≤ 15	
Parameters (in- out):	None		
Parameters (out):	None	Tarrest and the second	
Return value:	<outtype></outtype>	$= 2^{(c-b-a)} * [x * y]$	
Description:	The routine motors.	ultiplies two 16-bit integers with scaling factors set by input parame-	

] ()

[SWS_Mfx_00125][

The function returns the integer value of the fixed point multiplication (C), determined by $C = 2^{(c-b-a)} [x * y]$.

[SWS_Mfx_00126][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()|

[SWS_Mfx_00127][



If it is necessary to round the result of this equation, it is rounded toward zero.]()

[SWS_Mfx_00128] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x280	uint16 Mfx_MulP2_u16u16_u16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x281	uint16 Mfx_MulP2_u16s16_u16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x282	uint16 Mfx_MulP2_s16s16_u16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x283	sint16 Mfx_MulP2_u16u16_s16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x284	sint16 Mfx_MulP2_u16s16_s16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x285	sint16 Mfx_MulP2_s16s16_s16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)

] ()

8.6.2.2 32-Bit Multiplication of 2ⁿ Scaled Integer

[SWS_Mfx_00130] [

Service name:	Mfx_MulP2_ <l< th=""><th>nTypeMn1><intypemn2>_<outtypemn></outtypemn></intypemn2></th></l<>	nTypeMn1> <intypemn2>_<outtypemn></outtypemn></intypemn2>
Syntax:	<pre><outtype> Mfx_MulP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x290 to 0x295	5
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):	x y a b	Integer value of the fixed point operand. Integer value of the fixed point operand. Radix point position of the first fixed point operand. Must be a constant expression. Radix point position of the second fixed point operand. Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: -63 ≤ (c - b - a) ≤ 31
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	= 2^(c-b-a) * [x * y]
Description:	The routine multiplies two 32-bit integers with scaling factors set by input parameters.	

l ()

[SWS Mfx 00131][

The function returns the integer value of the fixed point multiplication (C), determined by $C = 2^{(c-b-a)} * [x * y]$.

[SWS_Mfx_00132][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.



|()

[SWS_Mfx_00133][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00134] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x290	uint32 Mfx_MulP2_u32u32_u32(uint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x291	uint32 Mfx_MulP2_u32s32_u32(uint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x292	uint32 Mfx_MulP2_s32s32_u32(sint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x293	sint32 Mfx_MulP2_u32u32_s32(uint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x294	sint32 Mfx_MulP2_u32s32_s32(uint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x295	sint32 Mfx_MulP2_s32s32_s32(sint32 x, sint32 y, sint16 a, sint16 b, sint16 c)

| ()

8.6.3 Division

8.6.3.1 16-Bit Division of 2ⁿ Scaled Integer

[SWS_Mfx_00136] [

Service name:	Mfx_DivP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>			
Syntax:	<pre><outtype> Mfx_DivP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>			
Service ID[hex]:	0x300 to 0x307	7		
Sync/Async:	Synchronous			
Reentrancy:	Reentrant	Reentrant		
	х	Integer value of the fixed point operand.		
	у	Integer value of the fixed point operand.		
	а	Radix point position of the first fixed point operand. Must be a constant expression.		
Parameters (in):	b	Radix point position of the second fixed point operand. Must be a constant expression.		
	С	Radix point position of the fixed point result. Must be a constant expression. Valid range: -15 ≤ (c + b - a) ≤ 31		
Parameters (in-	None None			
out):				
Parameters (out):	None			
Return value:	<outtype></outtype>	= [2^(c+b-a) * x] / y		
Description:	The routine div ters.	ides two 16-bit integers with scaling factors set by input parame-		

1 ()

[SWS_Mfx_00137][

The function returns the integer value of the fixed point quotient (C), determined by $C = [2^{(c+b-a)} * x] / y$.

1()



[SWS_Mfx_00138][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()|

[SWS_Mfx_00139][

If the divisor, y, is zero, the result is defined by:

- * If $x \ge 0$ then the function returns the maximum value of the output type
- * If x < 0 then the function returns the minimum value of the output type I()

[SWS_Mfx_00141][

If it is necessary to round the result of this equation, it is rounded toward zero.]()

[SWS_Mfx_00142] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x300	uint16 Mfx_DivP2_u16u16_u16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x301	uint16 Mfx_DivP2_u16s16_u16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x302	uint16 Mfx_DivP2_s16u16_u16(sint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x303	uint16 Mfx_DivP2_s16s16_u16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x304	sint16 Mfx_DivP2_u16u16_s16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x305	sint16 Mfx_DivP2_u16s16_s16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x306	sint16 Mfx_DivP2_s16u16_s16(sint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x307	sint16 Mfx_DivP2_s16s16_s16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)

] ()

8.6.3.2 32-Bit Division of 2ⁿ Scaled Integer

[SWS_Mfx_00144] [

Service name:	Mfx_DivP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>		
Syntax:	<pre><outtype> Mfx_DivP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>		
Service ID[hex]:	0x310 to 0x317		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
	x I	nteger value of the fixed point operand.	
	y I	nteger value of the fixed point operand.	
Parameters (in):		Radix point position of the first fixed point operand. Must be a constant expression.	
		Radix point position of the second fixed point operand. Must be a constant expression.	
		Radix point position of the fixed point result. Must be a constant expression.	



	Valid range: -31 ≤ (c + b - a) ≤ 63	
Parameters (in-	None	
out):		
Parameters (out):	None	
Return value:	$<$ OutType> = $[2^(c+b-a) * x] / y$	
Description:	The routine divides two 32-bit integers with scaling factors set by input parame-	
	ters.	

[SWS_Mfx_00145][

The function returns the integer value of the fixed point quotient (C), determined by $C = [2^{(c+b-a)} * x] / y$.

|()|

[SWS_Mfx_00146][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()

[SWS_Mfx_00147][

If the divisor, y, is zero, the result is defined by:

- * If $x \ge 0$ then the function returns the maximum value of the output type
- * If x < 0 then the function returns the minimum value of the output type I()

[SWS_Mfx_00149][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00150] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x310	uint32 Mfx_DivP2_u32u32_u32(uint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x311	uint32 Mfx_DivP2_u32s32_u32(uint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x312	uint32 Mfx_DivP2_s32u32_u32(sint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x313	uint32 Mfx_DivP2_s32s32_u32(sint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x314	sint32 Mfx_DivP2_u32u32_s32(uint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x315	sint32 Mfx_DivP2_u32s32_s32(uint32 x, sint32 y, sint16 a, sint16 b, sint16 c)
0x316	sint32 Mfx_DivP2_s32u32_s32(sint32 x, uint32 y, sint16 a, sint16 b, sint16 c)
0x317	sint32 Mfx_DivP2_s32s32_s32(sint32 x, sint32 y, sint16 a, sint16 b, sint16 c)

1 ()

8.6.4 Addition

8.6.4.1 16-Bit Addition of 2ⁿ Scaled Integer

[SWS_Mfx_00152] [

	<u> </u>		
Service name:	Mfx_AddP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>		
Syntax:	<pre><outtype> Mfx_AddP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>		
	<intype1> x,</intype1>		
	<intype2> y,</intype2>		



	sint16	a,	
	sint16 b,		
	sint16	С	
)		
Service ID[hex]:	0x320 to 0x325	5	
Sync/Async:	Synchronous		
Reentrancy:	Reentrant	Reentrant	
	х	Integer value of the fixed point operand.	
	У	Integer value of the fixed point operand.	
	а	Radix point position of the first fixed point operand.	
		Must be a constant expression.	
	b	Radix point position of the second fixed point operand.	
Parameters (in):		Must be a constant expression.	
	С	Radix point position of the fixed point result.	
		Must be a constant expression.	
		Valid range: 0 ≤ a - b ≤ 15	
		$(c - b) \le 15$, $(a - c) \le 15$, $a \ge b$	
		$(c - a) \le 15$, $(b - c) \le 15$, $a < b$	
Parameters (in-	None		
out):			
Parameters (out):	None		
Return value:	<outtype></outtype>	$a \ge b$: $2^{(c-a)} * [x + (y * 2^{(a-b)})],$	
Return value.		a < b: 2^(c-b) * [(x * 2^(b-a)) + y]	
Description:	The routine ad	ds two 16-bit integers with scaling factors set by input parameters.	

I ()

[SWS_Mfx_00153][

The function returns the integer value of the fixed point sum (C), determined by $a \ge b$: $C = 2^(c-a) * [x + (y * 2^(a-b))],$ a < b: $C = 2^(c-b) * [(x * 2^(b-a)) + y].$

[SWS_Mfx_00154][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

(()

[SWS_Mfx_00155][

If it is necessary to round the result of this equation, it is rounded toward zero.]()

[SWS_Mfx_00156] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x320	uint16 Mfx_AddP2_u16u16_u16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x321	uint16 Mfx_AddP2_u16s16_u16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x322	uint16 Mfx_AddP2_s16s16_u16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x323	sint16 Mfx_AddP2_u16u16_s16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x324	sint16 Mfx_AddP2_u16s16_s16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x325	sint16 Mfx_AddP2_s16s16_s16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)

] ()



8.6.4.2 32-Bit Addition of 2ⁿ Scaled Integer

[SWS_Mfx_00158] [

Service name:	Mfx AddP2 <i< th=""><th>nTypeMn1><intypemn2>_<outtypemn></outtypemn></intypemn2></th></i<>	nTypeMn1> <intypemn2>_<outtypemn></outtypemn></intypemn2>	
Syntax:	<pre><outtype> Mfx_AddP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>		
Service ID[hex]:	0x330 to 0x335	5	
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	x y a b	Integer value of the fixed point operand. Integer value of the fixed point operand. Radix point position of the first fixed point operand. Must be a constant expression. Radix point position of the second fixed point operand. Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: $0 \le a - b \le 31$ $(c - b) \le 31$, $(a - c) \le 31$, $a \ge b$ $(c - a) \le 31$, $(b - c) \le 31$, $a \le b$	
Parameters (in- out):	None		
Parameters (out):	None		
Return value:	<outtype></outtype>	$a \ge b$: 2^(c-a) * [x + (y * 2^(a-b))], a < b: 2^(c-b) * [(x * 2^(b-a)) + y]	
Description:	The routine ad	ds two 32-bit integers with scaling factors set by input parameters.	

1 ()

[SWS_Mfx_00159][

The function returns the integer value of the fixed point sum (C), determined by $a \ge b$: $C = 2^(c-a) * [x + (y * 2^(a-b))],$ a < b: $C = 2^(c-b) * [(x * 2^(b-a)) + y]$

[SWS_Mfx_00160][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

]()

[SWS Mfx 00161][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00162] [

Here is the list of implemented functions.



Function ID[hex]	Function prototype
0x330	uint32 Mfx_AddP2_u32u32_u32(uint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x331	uint32 Mfx_AddP2_u32s32_u32(uint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x332	uint32 Mfx_AddP2_s32s32_u32(sint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x333	sint32 Mfx_AddP2_u32u32_s32(uint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x334	sint32 Mfx_AddP2_u32s32_s32(uint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x335	sint32 Mfx_AddP2_s32s32_s32(sint32 x, sint32 y, sint32 a, sint32 b, sint32 c)

8.6.5 Subtraction

8.6.5.1 16-Bit Subtraction of 2ⁿ Scaled Integer

[SWS Mfx 00164] [

SWS_WIX_UUTE				
Service name:	Mfx_SubP2_<	Mfx_SubP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>		
Syntax:	<pre><outtype> Mfx_SubP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>			
Service ID[hex]:	0x340 to 0x34	7		
Sync/Async:	Synchronous	Synchronous		
Reentrancy:	Reentrant			
Parameters (in):	x y a b	Integer value of the fixed point operand. Integer value of the fixed point operand. Radix point position of the first fixed point operand. Must be a constant expression. Radix point position of the second fixed point operand. Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: $0 \le a - b \le 15$ $(c - b) \le 15$, $(a - c) \le 15$, $a \ge b$ $(c - a) \le 15$, $(b - c) \le 15$, $a < b$		
Parameters (in- out):	None			
Parameters (out):		None		
Return value:	<outtype></outtype>	$a \ge b$: $2^{(c-a)} * [x - (y * 2^{(a-b)})]$ $a < b$: $2^{(c-b)} * [(x * 2^{(b-a)}) - y]$		
Description:	The routine su ters.	The routine subtracts two 16-bit integers with scaling factors set by input parame-		

] ()

[SWS_Mfx_00165][

The function returns the integer value of the fixed point difference (C), determined by $a \ge b$: $C = 2^(c-a) * [x - (y * 2^(a-b))],$ a < b: $C = 2^(c-b) * [(x * 2^(b-a)) - y]$

[SWS_Mfx_00166][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

|()



[SWS_Mfx_00167][

If it is necessary to round the result of this equation, it is rounded toward zero. J()

[SWS_Mfx_00168] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x340	uint16 Mfx_SubP2_u16u16_u16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x341	uint16 Mfx_SubP2_u16s16_u16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x342	uint16 Mfx_SubP2_s16u16_u16(sint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x343	uint16 Mfx_SubP2_s16s16_u16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x344	sint16 Mfx_SubP2_u16u16_s16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x345	sint16 Mfx_SubP2_u16s16_s16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x346	sint16 Mfx_SubP2_s16u16_s16(sint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x347	sint16 Mfx_SubP2_s16s16_s16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)

]()

8.6.5.2 32-Bit Subtraction of 2ⁿ Scaled Integer

[SWS_Mfx_00170] [

Service name:	Mfx_SubP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>		
Syntax:	<pre><outtype> Mfx_SubP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>		
Service ID[hex]:	0x350 to 0x35	7	
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	x y a b	Integer value of the fixed point operand. Integer value of the fixed point operand. Radix point position of the first fixed point operand. Must be a constant expression. Radix point position of the second fixed point operand.	
	c	Must be a constant expression. Radix point position of the fixed point result. Must be a constant expression. Valid range: $0 \le a - b \le 31$ $(c - b) \le 31$, $(a - c) \le 31$, $a \ge b$ $(c - a) \le 31$, $(b - c) \le 31$, $a < b$	
Parameters (in- out):	None		
Parameters (out):	None		
Return value:	<outtype></outtype>	a ≥ b: 2^(c-a) * [x - (y * 2^(a-b))] a < b: 2^(c-b) * [(x * 2^(b-a)) - y]	
Description:	The routine subtracts two 32-bit integers with scaling factors set by input parameters.		

] ()



[SWS_Mfx_00171][

The function returns the integer value of the fixed point difference (C), determined by $a \ge b$: $C = 2^(c-a) * [x - (y * 2^(a-b))],$ a < b: $C = 2^(c-b) * [(x * 2^(b-a)) - y].$

[SWS_Mfx_00172][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

(()

[SWS_Mfx_00173][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00174] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x350	uint32 Mfx_SubP2_u32u32_u32(uint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x351	uint32 Mfx_SubP2_u32s32_u32(uint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x352	uint32 Mfx_SubP2_s32u32_u32(sint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x353	uint32 Mfx_SubP2_s32s32_u32(sint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x354	sint32 Mfx_SubP2_u32u32_s32(uint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x355	sint32 Mfx_SubP2_u32s32_s32(uint32 x, sint32 y, sint32 a, sint32 b, sint32 c)
0x356	sint32 Mfx_SubP2_s32u32_s32(sint32 x, uint32 y, sint32 a, sint32 b, sint32 c)
0x357	sint32 Mfx_SubP2_s32s32_s32(sint32 x, sint32 y, sint32 a, sint32 b, sint32 c)

] ()

8.6.6 Absolute Difference of 2ⁿ Scaled Integer

[SWS_Mfx_00176] [

Service name:	Mfx_AbsDiffP2_ <intypemn1><intypemn2>_<outtypemn></outtypemn></intypemn2></intypemn1>	
Syntax:	<pre><outtype> Mfx_AbsDiffP2_<intypemn1><intypemn2>_<outtypemn>(</outtypemn></intypemn2></intypemn1></outtype></pre>	
Service ID[hex]:	0x360 to 0x365	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
	x Integer value of the fixed point operand.	
	y Integer value of the fixed point operand.	
Parameters (in):	a Radix point position of the first fixed point operand. Must be a constant expression.	
	Radix point position of the second fixed point operand. Must be a constant expression.	
	Radix point position of the fixed point result. Must be a constant expression. Valid range: $0 \le a - b \le 15$	



		$(c - b) \le 15$, $(a - c) \le 15$, $a \ge b$ $(c - a) \le 15$, $(b - c) \le 15$, $a < b$
	None	
out):		
Parameters (out):	None	
Return value:	<outtype></outtype>	a ≥ b: 2^(c-a) * x - (y * 2^(a-b)) a < b: 2^(c-b) * (x * 2^(b-a)) - y
Neturii varue.		a < b: 2^(c-b) * (x * 2^(b-a)) - y
Description:	The routine subtracts and takes the absolute value of two 16-bit integers with scal-	
	ing factors set by input parameters.	

1 ()

[SWS_Mfx_00177][

The function returns the integer value of the fixed point absolute difference (C), determined by

```
a \ge b: C = 2^{(c-a)} | |x - (y | 2^{(a-b)})|,

a < b: C = 2^{(c-b)} | |(x | 2^{(b-a)}) - y|.

|()
```

[SWS_Mfx_00178][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

(()

[SWS_Mfx_00179][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00180] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
0x360	uint16 Mfx_AbsDiffP2_u16u16_u16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x361	uint16 Mfx_AbsDiffP2_u16s16_u16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x362	uint16 Mfx_AbsDiffP2_s16s16_u16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x363	sint16 Mfx_AbsDiffP2_u16u16_s16(uint16 x, uint16 y, sint16 a, sint16 b, sint16 c)
0x364	sint16 Mfx_AbsDiffP2_u16s16_s16(uint16 x, sint16 y, sint16 a, sint16 b, sint16 c)
0x365	sint16 Mfx_AbsDiffP2_s16s16_s16(sint16 x, sint16 y, sint16 a, sint16 b, sint16 c)

| ()

8.6.7 Absolute Value

8.6.7.1 16-Bit Absolute Value of 2ⁿ Scaled Integer

[SWS_Mfx_00182] [

<u>[</u>	4 (
Service name:	Mfx_AbsP2_s16_ <outtypemn></outtypemn>		
Syntax:	<outtype> Mfx_AbsP2_s16_<outtypemn>(</outtypemn></outtype>		
Service ID[hex]:	0x370 to 0x371		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		



Parameters (in):	X	Integer value of the fixed point operand.
		Radix point position of the first fixed point operand. Must be a constant expression.
		Radix point position of the fixed point result. Must be a constant expression. Valid range: -15 ≤ (c - a) ≤ 15
Parameters (in- out):	None	
Parameters (out):	None	
Return value:	<outtype></outtype>	2^(c-a) * x
Description:	The routine takes the absolute value of a 16-bit integer with scaling factors set by input parameters.	

[SWS_Mfx_00183][

The function returns the integer value of the fixed point absolute value (C), determined by $C = 2^{(c-a)} |x|$.

]()

[SWS_Mfx_00184][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

(()

[SWS_Mfx_00185][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00186] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype
	7 0
	10
0x370	uint16 Mfx_AbsP2_s16_u16(sint16 x, sint16 a, sint16 c)
0x371	sint16 Mfx_AbsP2_s16_s16(sint16 x, sint16 a, sint16 c)

1 ()

8.6.7.2 32-Bit Absolute Value of 2n Scaled Integer

[SWS Mfx 00188] [

2110_IIIIX_00100]		
Service name:	Mfx_AbsP2_s32_ <outtypemn></outtypemn>	
Syntax:	<pre><outtype> Mfx_AbsP2_s32_<outtypemn>(</outtypemn></outtype></pre>	
Service ID[hex]:	0x37a to 0x37b	
Sync/Async:	Synchronous	
Reentrancy:	Reentrant	
Parameters (in):		Integer value of the fixed point operand.
	a	Radix point position of the first fixed point operand.



		Must be a constant expression.	
		Radix point position of the fixed point result. Must be a constant expression. Valid range: -31 ≤ (c - a) ≤ 31	
Parameters (in- out):	None		
Parameters (out):	None		
Return value:	<outtype></outtype>	2^(c-a) * x	
Description:	The routine takes the absolute value of a 32-bit integer with scaling factors set by input parameters.		

| () |

[SWS_Mfx_00189][

The function returns the integer value of the fixed point absolute value (C), determined by $C = 2^{(c-a)} * |x|$.

(()

[SWS_Mfx_00190][

Return-value shall be saturated to boundary values in the event of negative or positive overflow.

(()

[SWS_Mfx_00191][

If it is necessary to round the result of this equation, it is rounded toward zero. I()

[SWS_Mfx_00192] [

Here is the list of implemented functions.

Function ID[hex]	Function prototype	
0x37A	uint32 Mfx_AbsP2_s32_u32(sint32 x, sint16 a, sint16 c)	
0x37B	sint32 Mfx_AbsP2_s32_s32(sint32 x, sint16 a, sint16 c)	

1 ()

8.7 Examples of use of functions

8.7.1 Combinations of multiplication and shift right

The function that multiplies an argument by a factor of a given range can be interpreted as the combination of multiplication and shift right.

If we consider the factor that is a power of two: 2ⁿ¹

If we consider the maximum of the type used to code the factor: 2ⁿ²-1

Then, the shift right we shall apply to the result of the multiplication is given by : (n2-n1)

For example, we multiply a s8 value (argument1) by a factor of 1 (2^0) coded with an u8 $(Max(u8)=2^8-1)$.

The physical range of the factor is [0, 0.996]

The result is:

Mfx_MulShRight_s16s16u8_s8(argument1, factor, 8)



8.7.2 Combinations of division and shift left

In the domain of power train, the function that divides two arguments to compute a factor of a given range can be interpreted as the combination of division and shift left.

If we consider the factor that is a power of two: 2ⁿ¹

If we consider the maximum of the type used to code the result (factor): 2ⁿ²-1

Then, the shift left we shall apply to the result of the division is given by : (n2-n1)

For example, we divide two u16 values (argument1 and argument2) to obtain a factor of 1 (2^{0}) coded with an u16 (Max(u16)= 2^{16} -1).

The physical range of the result is [0, 0.999985]

The result is:

Mfx_DivShLeft_u16u16u8_u16(argument1, argument2, 16)

8.8 Version API

8.8.1 Mfx GetVersionInfo

[SWS Mfx 00215] [

OWO_MIX_00210]			
Service name:	Mfx_GetVersionInfo		
Syntax:	void Mfx GetVersionInfo(
	Std_VersionInfoType* versioninfo		
)		
Service ID[hex]:	0x400		
Sync/Async:	Synchronous		
Reentrancy:	Reentrant		
Parameters (in):	None		
Parameters (in-	None		
out):			
Parameters (out):	versioninfo Pointer to where to store the version information of this module.		
	Format according [BSW00321]		
Return value:	None		
Description:	Returns the version information of this library.		

I (SRS BSW 00407, SRS BSW 00003, SRS BSW 00318, SRS BSW 00321)

The version information of a BSW module generally contains:

Module Id

Vendor Id

Vendor specific version numbers (SRS_BSW_00407).

[SWS_Mfx_00216] [

If source code for caller and callee of Mfx_ GetVersionInfo is available, the Mfx library should realize Mfx_ GetVersionInfo as a macro defined in the module's header file.] (SRS_BSW_00407, SRS_BSW_00411)

8.9 Call-back notifications



None.

8.10 Scheduled functions

The MFX library does not have scheduled functions.

8.11 Expected Interfaces

None.

8.11.1 Mandatory Interfaces

None.

8.11.2 Optional Interfaces

None.

8.11.3 Configurable interfaces

None.



9 Sequence diagrams

Not applicable.



10 Configuration specification

10.1 Published Information

[SWS_Mfx_00214] The standardized common published parameters as required by SRS_BSW_00402 in the SRS General on Basic Software Modules [3] shall be published

within the header file of this module and need to be provided in the BSW Module Description. The according module abbreviation can be found in the List of Basic Software Modules [1].] (SRS_BSW_00402, SRS_BSW_00374, SRS_BSW_00379)

Additional module-specific published parameters are listed below if applicable.

10.2 Configuration option

[SWS_Mfx_00218] The MFX library shall not have any configuration options that may affect the functional behavior of the routines. I.e. for a given set of input parameters, the outputs shall be always the same. For example, the returned value in case of error shall not be configurable. (SRS_LIBS_00001)

However, a library vendor is allowed to add specific configuration options concerning library implementation, e.g. for resources consumption optimization.



11 Not applicable requirements

[SWS_Mfx_00222] [These requirements are not applicable to this specification.] ()