

Project Report

Adaptive Cruise Control

Compliance Design of Automotive Systems
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Chapter 1

Introduction

Chapter 2

Project organization

2.1 Model based design

2.2 Team organization

Chapter 3

Model explanation & design

As anticipated before, the aim of this project is to design an Adaptive Cruise Control: to design it following the MBD, we of course need a plant that models the vehicle longitudinal dynamics. In Fig 3.1 it is possible to see the whole plant model.

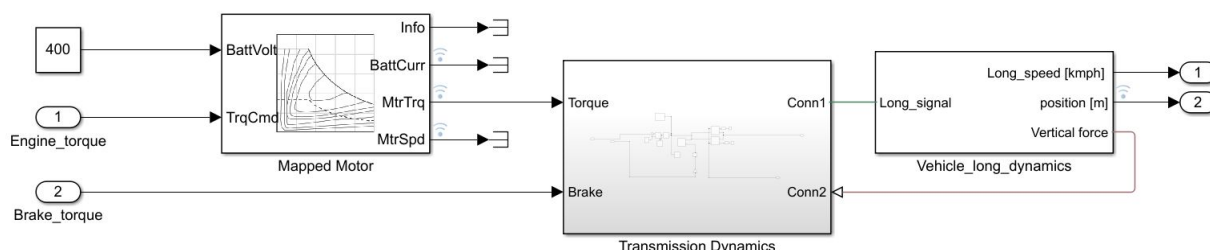


Figure 3.1: Plant

Going deeper, to model the longitudinal dynamics of the vehicle we picked from the Simscape Driveline library the “vehicle longitudinal dynamics” block.

We wanted to make some more detailed simulation so we decided to model the driveline system.

The vehicle dynamics block accepts as input the long speed of the vehicle, we associated it to the long speed of the CoG of the wheels.

The Simscape wheels block models the behaviour of a the wheel in terms of inertia, rolling resistance and slip. The angular speed of the wheel is proportional to the torque given as input.

The torque is the output of a mapped motor block, of course modified by a gear ratio block that models the one-speed transmission of the electric vehicle.

When a negative torque is asked, the brake block provides a negative torque at the wheel input.

With this plant model we wanted to model the entire driveline from the ECU torque request to the wheels motion.

Chapter 4

Project development

4.1 Requirements

4.1.1 Software requirements

In order to be able to interact with the developed model, some software tools are needed:

- Matlab R2019b;
- Simulink;
- Simscape;
- Simscape Driveline.

4.1.2 High level requirements

The goal of the overall control system is to extend the mission of the simpler Cruise Control: regulate the speed to the desired one and keep it. In addition respect this goal, the Adaptive Cruise Control adapts the speed of the vehicle respect the velocity of the following one, being able to correctly maintain the safety distance dictated by the Highway Code when it needed (that is to say, when the current distance between the vehicles is lower than the required safety distance):

$$d_s = d_{min} + \frac{1}{k}v^2$$

4.1.3 Low level requirements

The designed system is intended to work on an electric vehicle, considering also the state of charge of the battery: particularly, the overall behaviour of the control action will be more or less aggressive depending on the state of charge level.

Thanks to the presence of a sensor (typically a radar installed in the front of the car), the distance respect the following vehicle is continuously monitored: when this distance falls below the threshold dictated by the Highway Code minus a safety margin quantity (one meter as dictated by requirement, to be sure to be able to brake in the correct timings), the control must act on the brakes. Then, during the acceleration phase, the system needs to be designed as following: when the first vehicle accelerates, the control

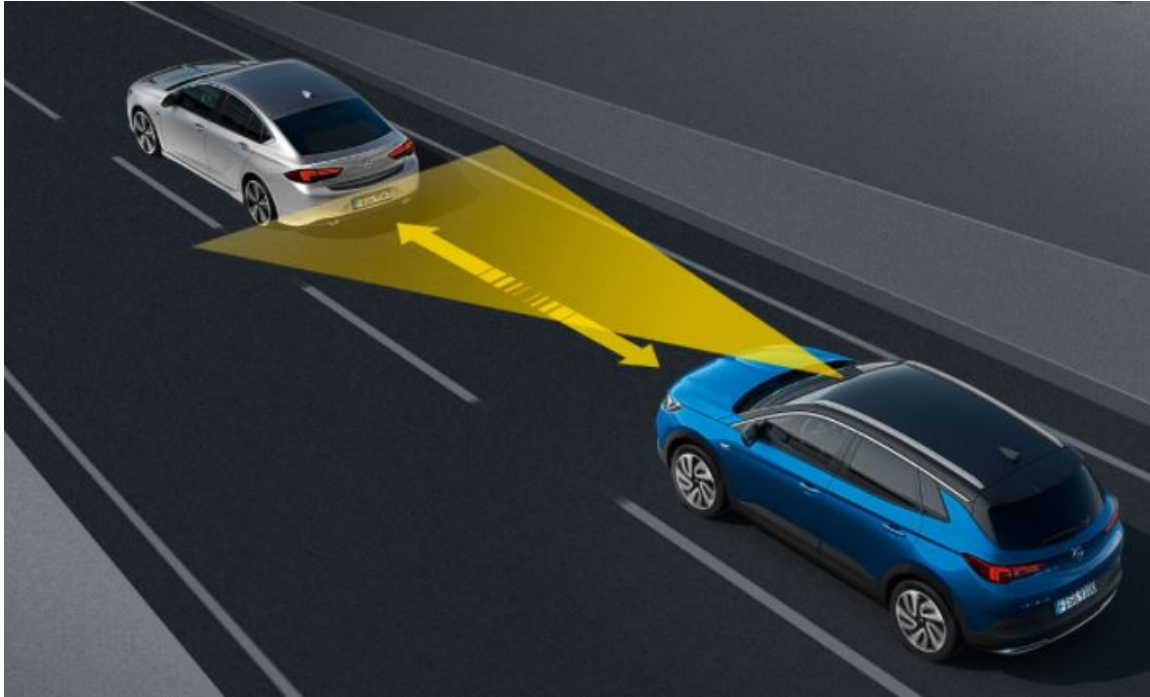


Figure 4.1: ACC



Figure 4.2: Front radar

must operate so that the vehicle speed becomes the one desired if it is possible, that is to say if the two vehicles are far enough. Otherwise, the system must adequate the vehicle speed to the one needed to keep the safety distance.

4.2 Unity & Integration testing

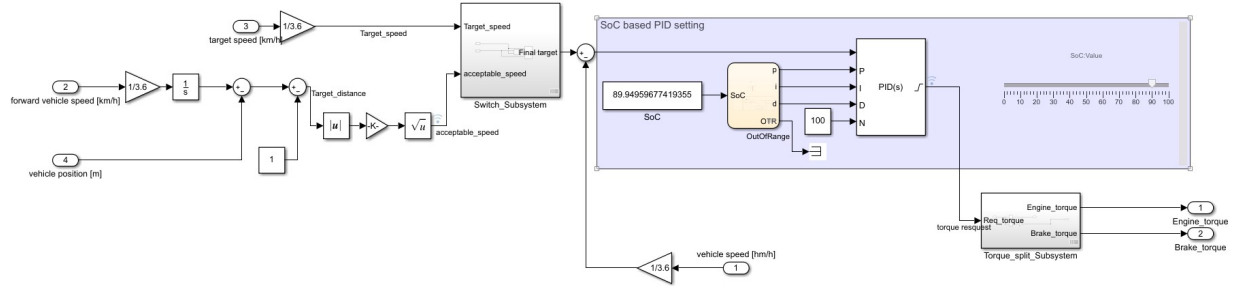


Figure 4.3: An overview of the controller block

Our controller is composed by 4 main blocks: switch_subsys, SoC handler, torque_split and the PID regulator. While it is impossible to test the PID behaviour without a model to be controlled, it is necessary to test separately the other blocks. We tested the units using the tool Simulink Test. We first built an harness for each unity to isolate it from the rest of the model, then we built the input signals in such a way to stimulate the unity in all possible conditions. We also built the baseline signals to tell the tester what output we expected when feeding the unity with a given input.

4.2.1 Switch subsystem

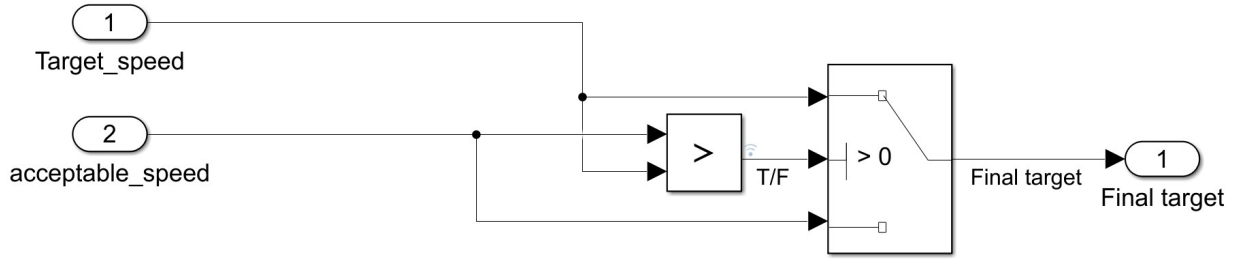


Figure 4.4: An overview of the switch subsystem

This system is composed of a switch commanded by a majority operator fed with the same signals that have to be switched. We wanted to be sure that this small unity would have behaved as expected. In Fig 4.5 we can see the inputs that we used to test the unit: we wanted to test all the possible conditions for the 'greater than' block. To better visualize which signal was let pass by the switch we tried to differentiate as much as possible the two inputs. As we can see [FIGURE] the simulink test tool is able to generate

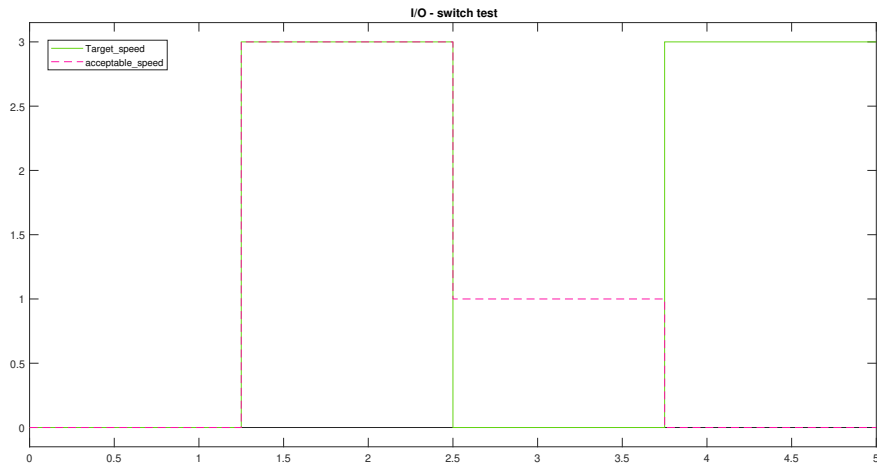


Figure 4.5: Inputs of the test: acceptable speed is the one set by the user, target speed is the one of the care ahead

a document reporting the configuration of the test, to guarantee to the customer (we can call customer whoever will need to use our subsystem and will need it being tested) that the test was performed accordingly to his needs. This feature also gives the opportunity to an external part to perform the exact same test and verify that the results are coherent. In the same way the tool can generate a report regarding the output of the test, here in Fig. 4.6 we can see that the test produces successful results, infact the output of the test coincides precisely with our baseline signal.

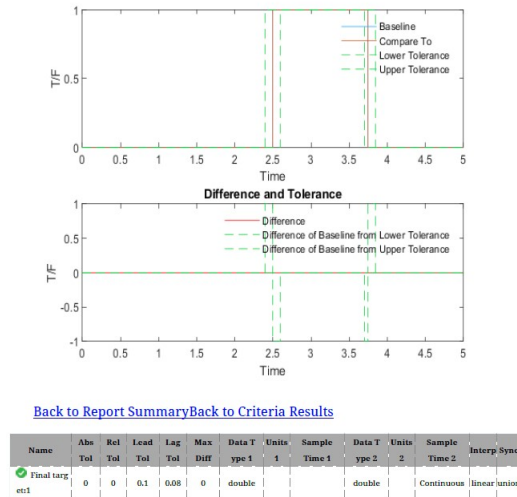


Figure 4.6: Automatically generated test reports

4.2.2 Torque split

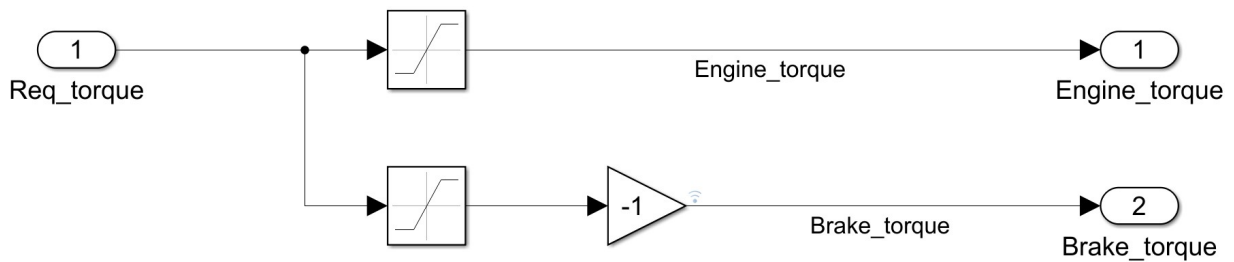


Figure 4.7: An overview of the torque split subsystem

This unit's goal is to split the signal coming from the PID, asking for torque. The PID asks for a negative or positive torque, we have to be able to translate the negative torque in a signal to be sent to the brake module. This unit's goal is to divide the torque signal into two signals depending on the sign of the PID output. To test this unit we just had to verify that when given a positive input it was transferred to output assigned to the engine and when given a negative input it was transferred to the brake output. At the same time the output assigned to the opposite sign has to be zero. In Fig. 4.8 you can see how the test input was designed and you can see that the output corresponds exactly to the baseline signal, meaning a successful test.

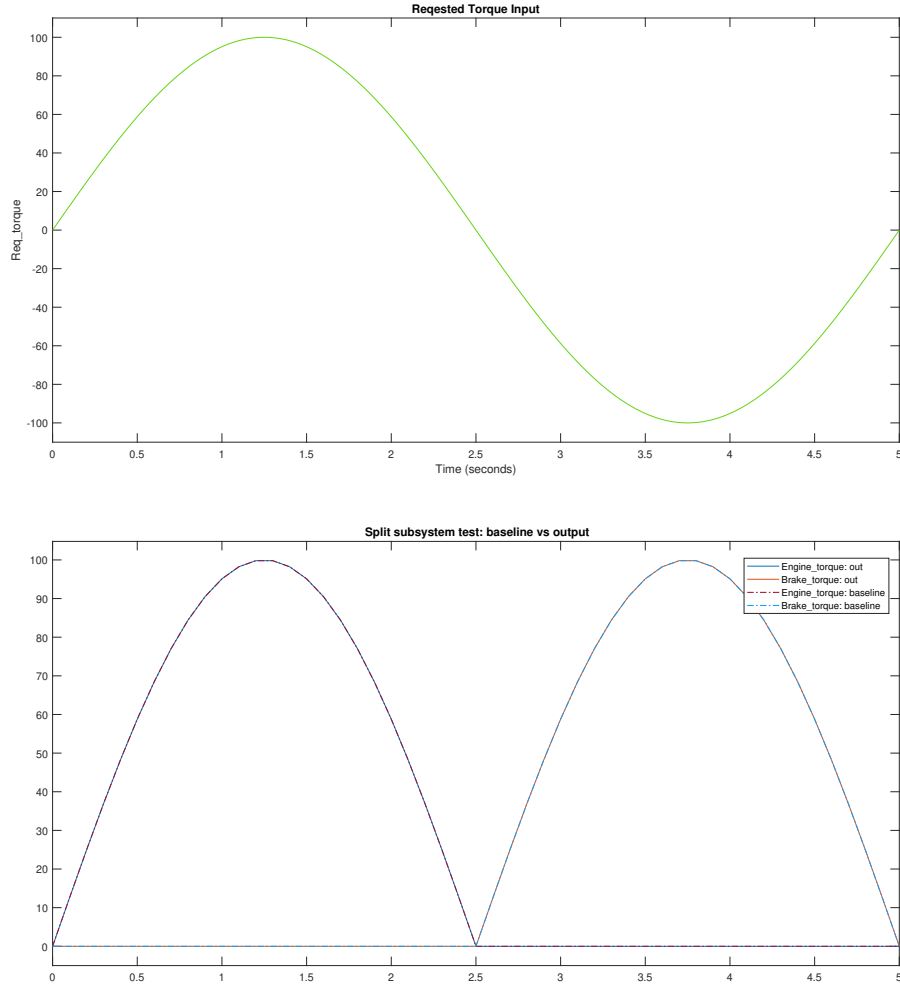


Figure 4.8: Test inputs (up) Outputs vs baseline (down)

4.2.3 SoC handler

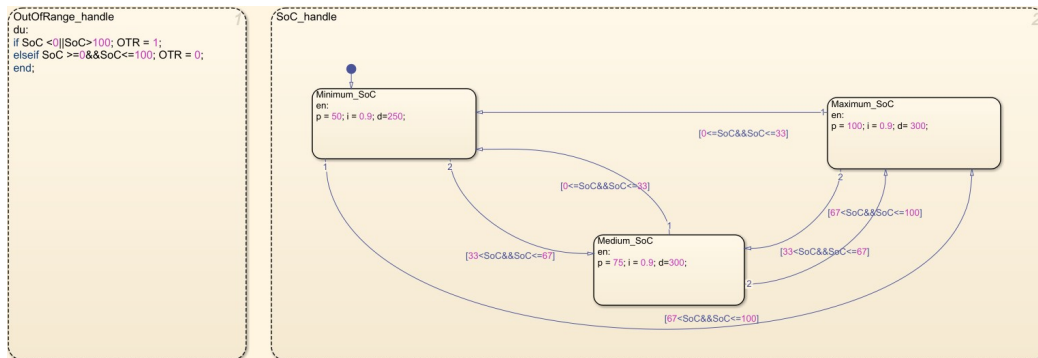


Figure 4.9: An overview of the SoC handler chart

This unit is a FSM, so a slightly more complicated test is due. The goal of this unit is to manage the aggressiveness of the control depending on the SoC of the vehicle's battery pack. We want a less aggressive control if the car has less autonomy left. We divided the interval of possible SoC values in three segments: minimum, medium, maximum. Each

segment corresponds to a different state. We have three states, since from each state you can go to the other two we have in total 6 transitions. We have only one input: SoC signal. In the input signal, which you can see in Fig.4.10, we changed the signal in order to stimulate all the possible transitions. To be more robust against unexpected behaviour we wanted an additional output for the FSM. The SoC signal is expected to never trespass the 0% and 100% bounds, so we created a signal “OutOfRange” that rises when this condition is verified. The FSM is designed to check constantly the OTR signal while performing the ordinary tasks, so a parallel state is created in the FSM. We also tested

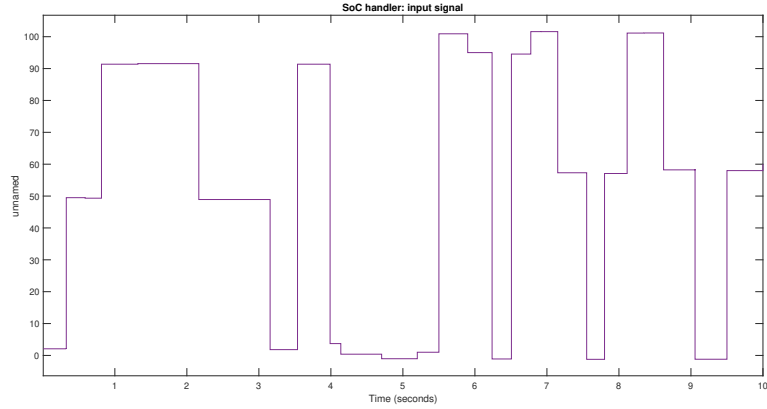


Figure 4.10: Input fed to the SoC handler

the behaviour of this signal. In Fig.4.10 you can deduce from the input signal (second half of the signal) that we wanted to test that regardless the state in which we were and the previous value of the SoC signal, the OTR signal would raise when expected to. In Fig.4.11 you can see that the output signal matches the baseline, meaning a successful test.

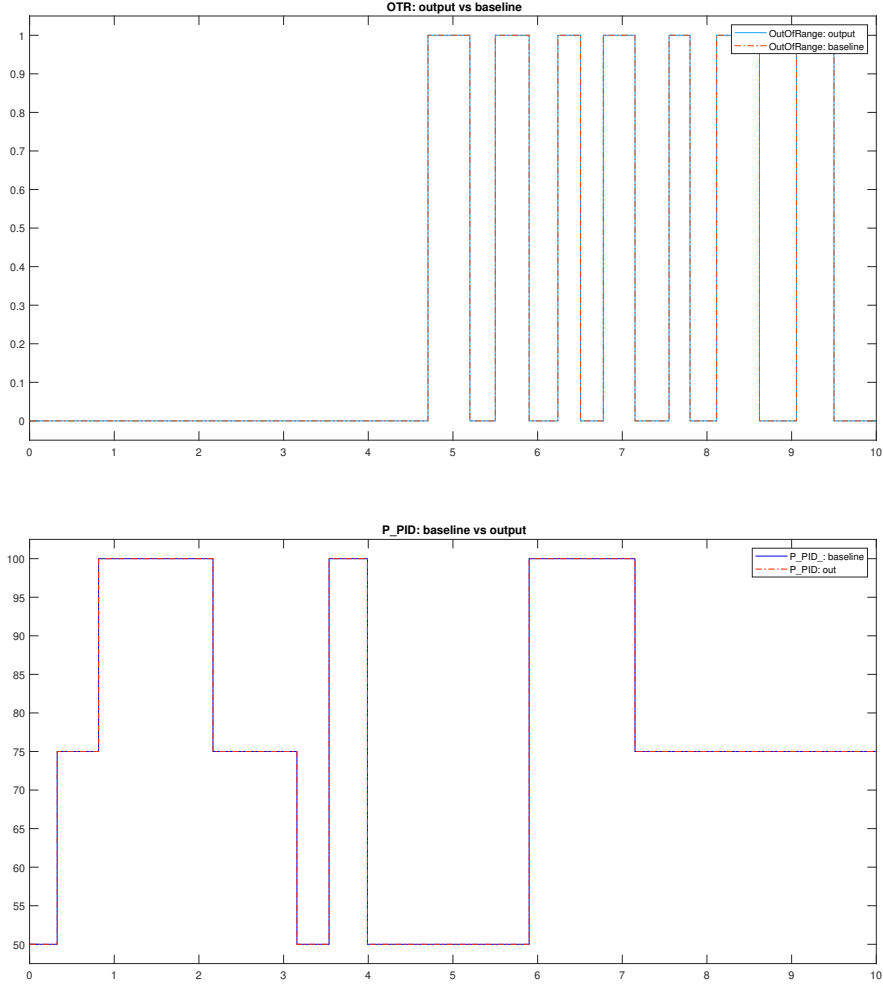


Figure 4.11: OTR output vs baseline (up) PID::P output vs baseline (down)

4.2.4 Integration test

All these units cooperate in the same controller module, so an integration test would be necessary. In our specific case these three units do not have in common any input or output, meaning that there is no possible integration test to be performed. All these units are meant to condition the PID behaviour, so in order to perform an integration test the PID would have to be involved. Since we assume the PID block itself already tested by MATLAB there is no point in testing the PID behaviour when not regulating a plant model. According to the previous affirmations we decided to let the integration test coincide with the MIL test, in order to test the behaviour of the whole controller.

4.3 MIL

4.4 SIL

4.5 PIL

Chapter 5

Conclusion