

# **Test Specification Report**

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# Table of Contents

<a href="#">1. Switch test</a> .....	2
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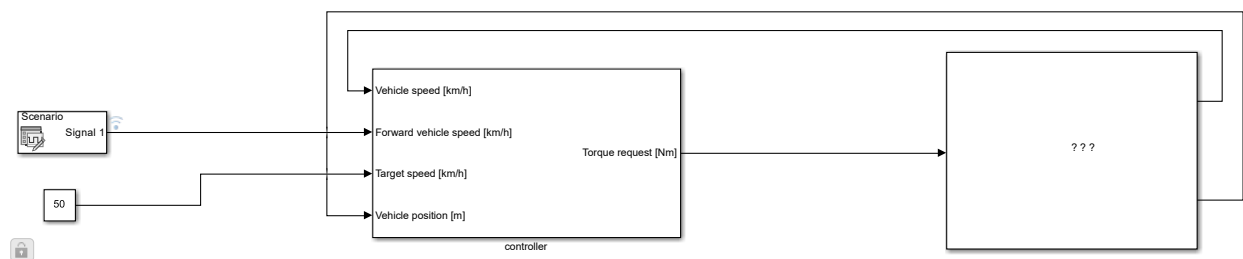
# 1. Switch\_test

## Test Details

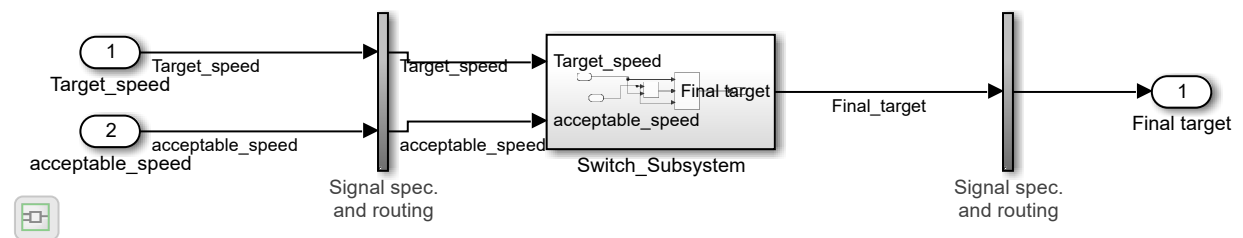
Releases	Current (2019b)
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## System Under Test

Model Name: Bozza\_longitudinal\_rev2\_19b



Harness Name: Bozza\_longitudinal\_rev2\_19b\_Harness1



## Simulation Settings Overrides

Simulation Mode	Model Settings
Stop Time	5

## External Inputs

Name	File Path	Status
Test_switch-controller_input.mat (Active)	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Ciao (3)\Test_switch_controller_input.mat	Successfully mapped inputs.

## Logged Signals

Name	Source	Port Index	Plot Index
Signal Set 1			
• Final_target	Bozza_longitudinal_rev2_19b_Harness-1/Switch_Subsystem	1	

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Baseline Criteria

Signal Name	Abs Tol	Rel Tol	Leading Tol	Lagging Tol
Baseline_switch_controller.mat (Active)	0	0	0.10000-0000000-00001	0.08000-0000000-000002
• Final_target (Active)	0	0	0.10000-0000000-00001	0.08000-0000000-000002
• T/F (Active)	0	0	0.10000-0000000-00001	0.08000-0000000-000002
• Final target:1 (Active)	0	0	0.10000-0000000-00001	0.08000-0000000-000002