Report Generated by Test Manager

Title: **Test_baseline_01**
Author: **Alessandro Barro, Nicolas Bruscol i, Viviana Ceccarelli, Manuel Cintu ra, LeonardoChiacchiararelli, Luc ia Mariani**
Date: **11-Apr-2021 23:44:23**

Test Environment

Platform: PCWIN64
MATLAB: (R2019b)

Summary

Name	Outcome	Duration (Seconds)
[Results: 2021-Apr-11 23:43:17](#)	✅	5.321
📄 [Switch_test](#)	✅	5.321

Results: 2021-Apr-11 23:43:17

Result Type:	Result Set
Parent:	None
Start Time:	11-Apr-2021 23:43:17
End Time:	11-Apr-2021 23:43:22
Outcome:	Total: 1, Passed: 1

Back to Report Summary

Switch_test

Test Result Information

|	
Result Type:	Test Case Result
Parent:	Results: 2021-Apr-11 23:43:17
Start Time:	11-Apr-2021 23:43:17
End Time:	11-Apr-2021 23:43:22
Outcome:	Passed

Test Case Information

|	
Name:	Switch_test
Type:	Baseline Test
Baseline Name:	Baseline_switch_controller.mat
Baseline File:	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Ciao (3)\Baseline_switch_controller.mat

Baseline Comparison

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync	Link to Plot
✅ Final_target	0	0	0.1	0.08	1.5	double			double		Continuous	linear	union	Link
✅ T/F	0	0	0.1	0.08	0	boolean			boolean		Continuous	zoh	union	Link
✅ Final target:1	0	0	0.1	0.08	0	double			double		Continuous	linear	union	Link

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✅ Final_target	0	0	0.1	0.08	1.5	double			double		Continuous	linear	union

Difference and Tolerance

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✅ T/F	0	0	0.1	0.08	0	boolean			boolean		Continuous	zoh	union

Difference and Tolerance
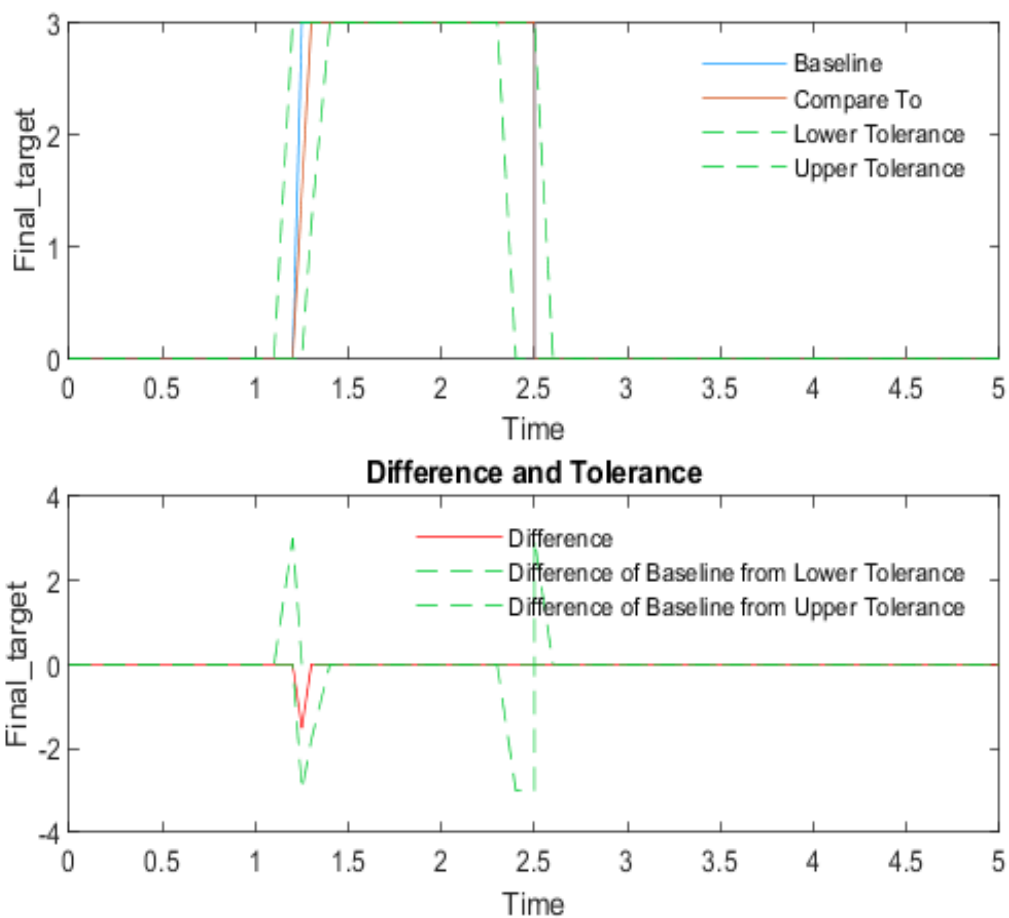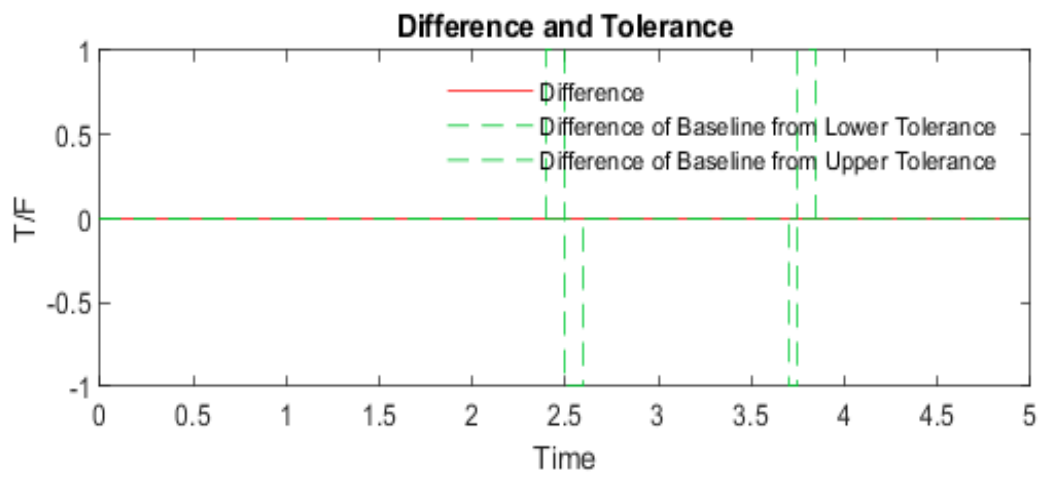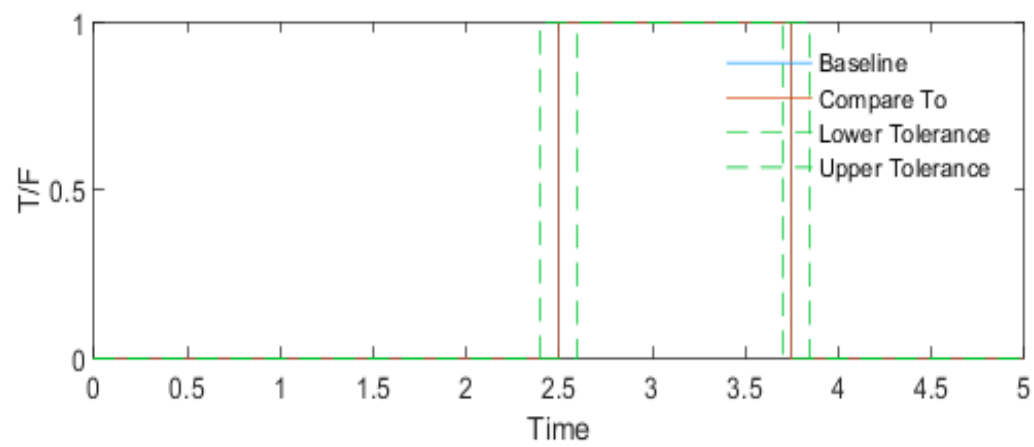
[Back to Report Summary](#)[Back to Criteria Results](#)

Name	Abs Tol	Rel Tol	Lead Tol	Lag Tol	Max Diff	Data Type 1	Units 1	Sample Time 1	Data Type 2	Units 2	Sample Time 2	Interp	Sync
✅ Final target:1	0	0	0.1	0.08	0	double			double		Continuous	linear	union

## Baseline_switch_controller.mat

Baseline Information

Baseline Name: Baseline_switch_controller.mat
Baseline File: D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Ciao (3)\Baseline_switch_controller.mat

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
Final_target	double			linear	union	Link

T/F	boolean			zoh	union	Link
Final target:1	double			linear	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
Final_target	double			linear	union
T/F	boolean			zoh	union
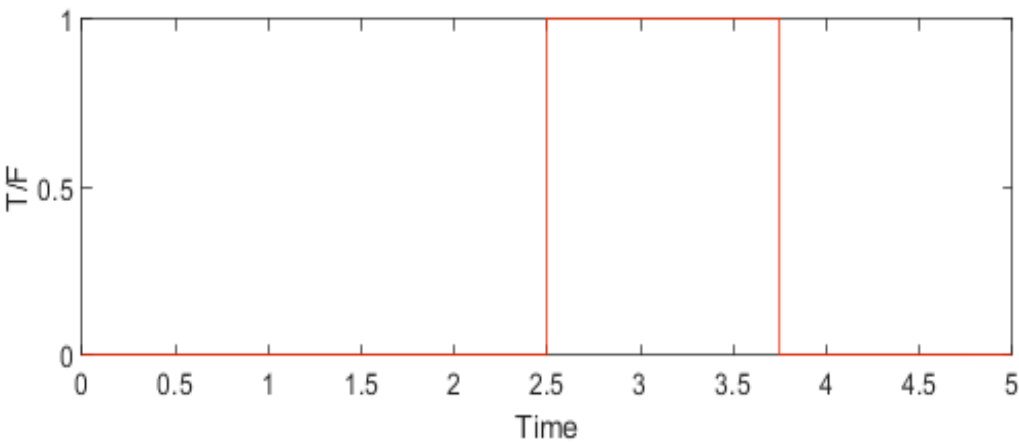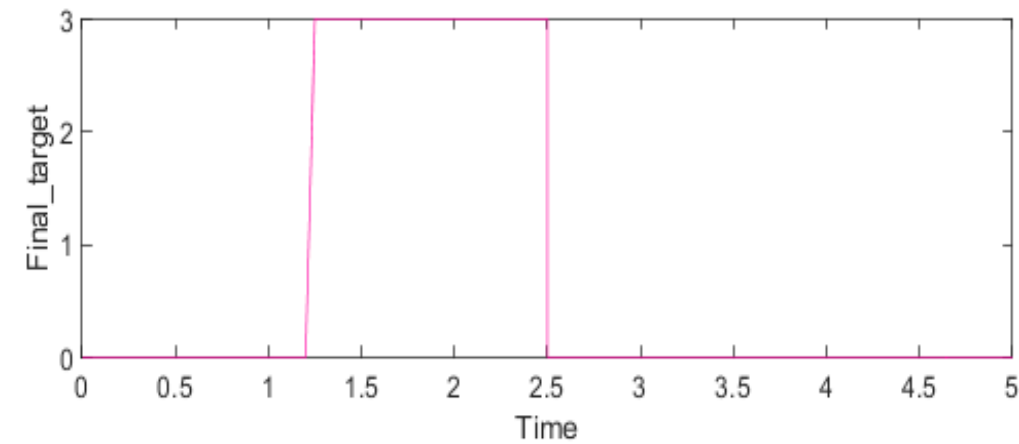
[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
Final target:1	double			linear	union
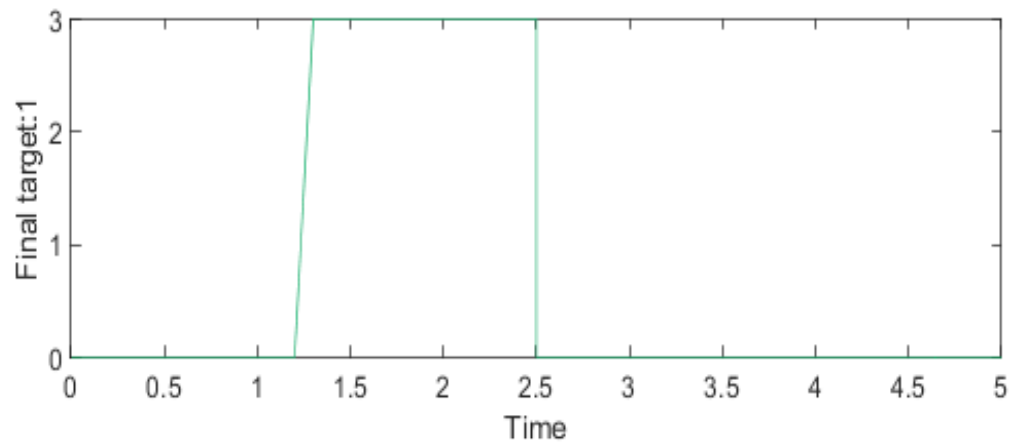
[Back to Report Summary](#)[Back to Signal Summary](#)
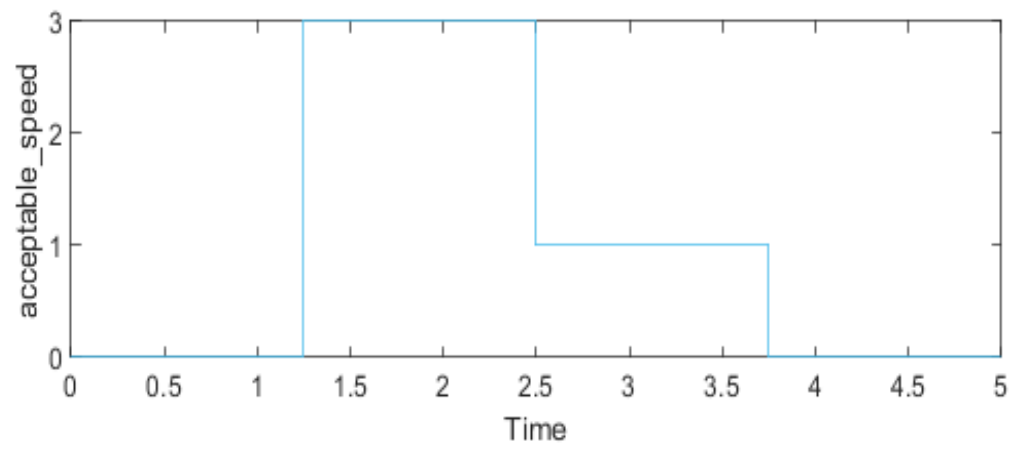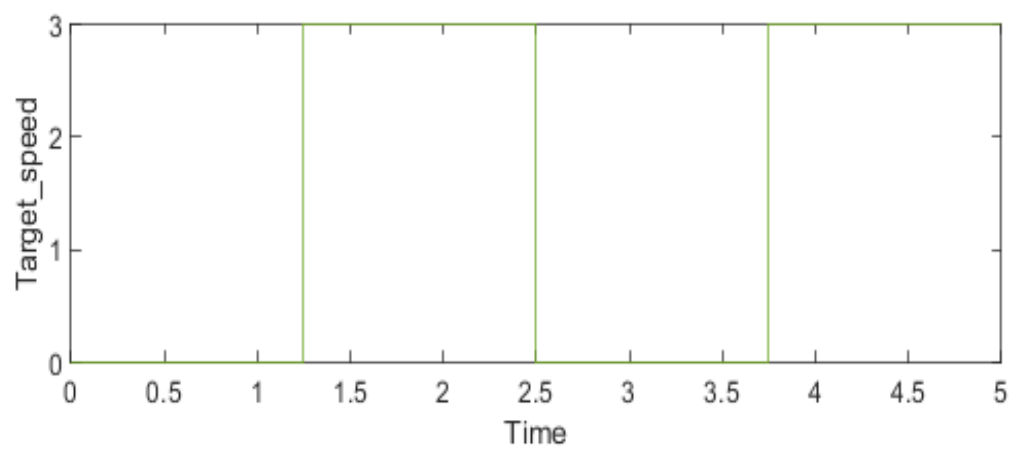
Input Data

Input Information

External Input Name:	Test_switch_controller_input.mat
External Input File:	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Ciao (3)\Test_switch_controller_input.mat

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
Target_speed	double			zoh	union	Link
acceptable_speed	double			zoh	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
Target_speed	double			zoh	union
acceptable_speed	double			zoh	union

Back to Report SummaryBack to Signal Summary

Simulation

System Under Test Information

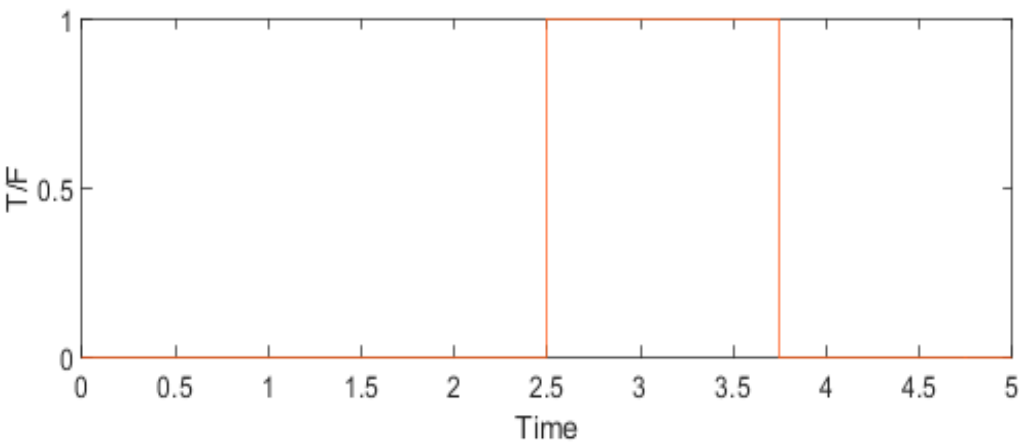|	
Model:	Bozza_longitudinal_rev2_19b
Harness:	Bozza_longitudinal_rev2_19b_Harness1
Harness Owner:	Bozza_longitudinal_rev2_19b/controller/Switch_Subsystem
Simulation Mode:	normal
Override SIL or PIL Mode:	0
Configuration Set:	Configuration6
External Input Name:	Test_switch_controller_input.mat
External Input File:	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Ciao (3)\Test_switch_controller_input.mat
Start Time:	0
Stop Time:	5
Checksum:	1217138011 3439934359 2320854378 3628123934
Simulink Version:	10.0
Model Version:	1.4
Model Author:	Leo
Date:	Sun Apr 11 23:34:13 2021
User ID:	Leo
Model Path:	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Git\adaptive-cruise-control\Model\Bozza_longitudinal_rev2_19b.slx
Machine Name:	PC-LEO
Solver Name:	VariableStepDiscrete
Solver Type:	Variable-Step
Max Step Size:	0.10000000000000001
Simulation Start Time:	2021-04-11 23:43:18
Simulation Stop Time:	2021-04-11 23:43:18
Platform:	PCWIN64

Simulation Output

Name	Data Type	Units	Sample Time	Interp	Sync	Link to Plot
Final target:1	double		Continuous	linear	union	Link
Final_target	double		Continuous	linear	union	Link

Name	Data Type	Units	Sample Time	Interp	Sync
T/F	boolean		Continuous	zoh	union	Link
Final target:1	double		Continuous	linear	union
Final_target	double		Continuous	linear	union
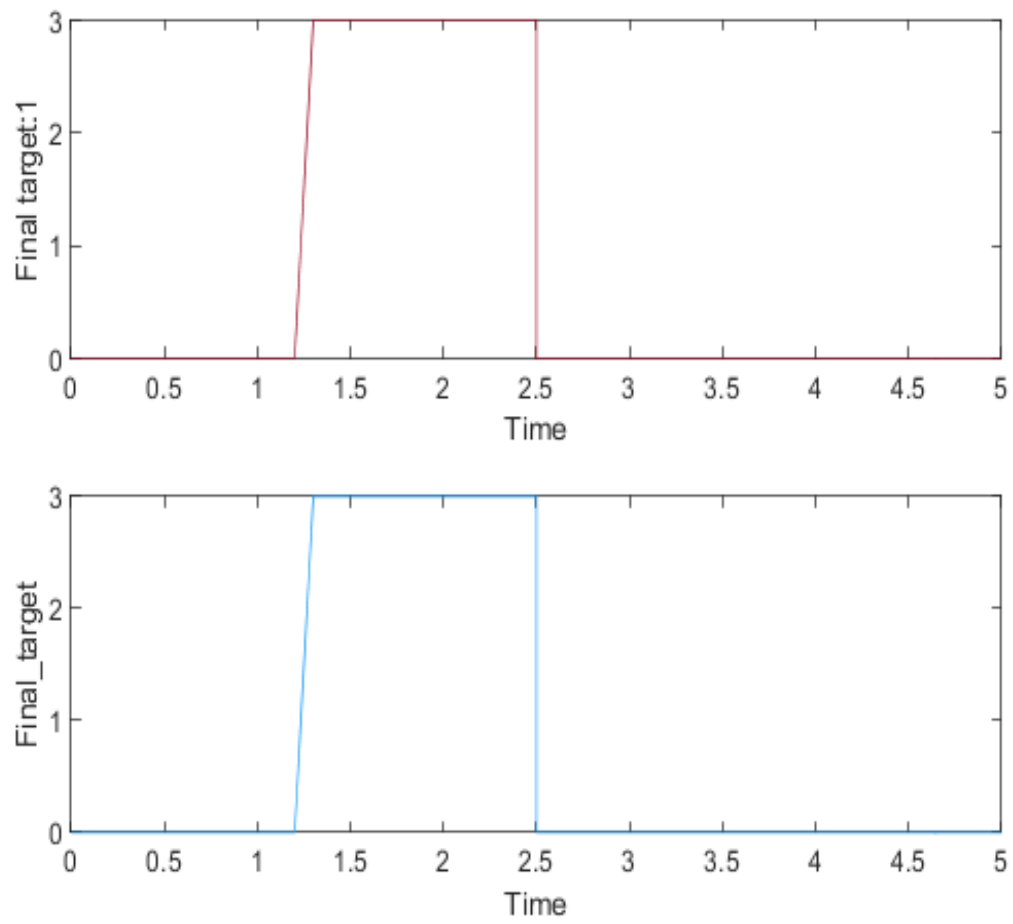
[Back to Report Summary](#)[Back to Signal Summary](#)

Name	Data Type	Units	Sample Time	Interp	Sync
T/F	boolean		Continuous	zoh	union

Back to Report Summary[Back to Signal Summary](Back to Signal Summary)