

# **Test Specification Report**

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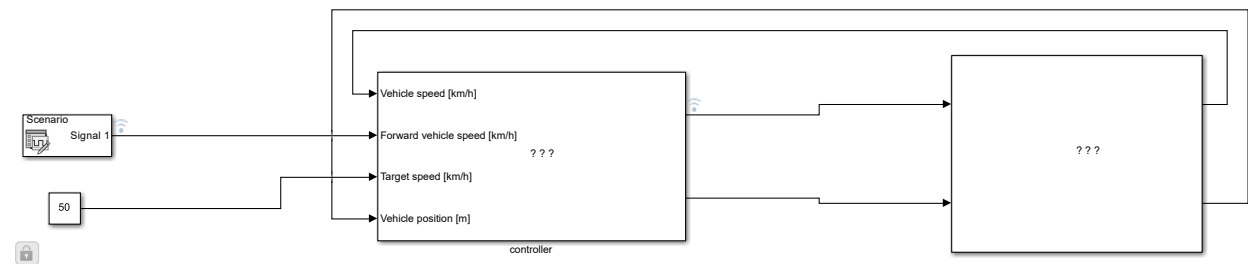
# 1. SoC\_handle\_FSM\_BaselineTest

## Test Details

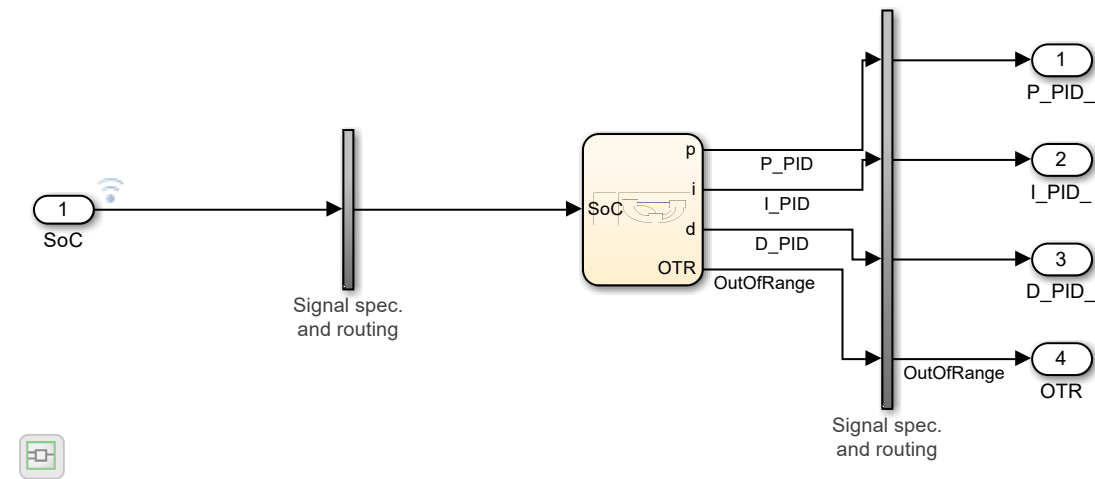
Releases	Current (2019b)
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## System Under Test

Model Name: Bozza\_longitudinal\_rev2\_19b



Harness Name: Bozza\_longitudinal\_rev2\_19b\_Harness6



## Simulation Settings Overrides

Simulation Mode	Model Settings
Stop Time	10

## External Inputs

Name	File Path	Status
Test_chart_rev2.-mat (Active)	D:\Dati\Università\MAGISTRALE\SecondYear\Compliance Design\Project\Git\adaptive-cruise-control\Test\FSM_unit_test\Test_chart_rev2.mat	Successfully mapped inputs.

## Configuration Settings Overrides

Configuration settings	Do not override model settings
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## Baseline Criteria

Signal Name	Abs Tol	Rel Tol	Leading Tol	Lagging Tol
Baseline_FSM_Testing_rev2.mat	0	0	0	0
• D_PID	0	0	0	0
• I_PID	0	0	0	0
• OutOfRange	0	0	0	0
• P_PID	0	0	0	0
Baseline_FSM_Testing_rev3_captured.-mat	0	0	0	0
• D_PID	0	0	0	0
• I_PID	0	0	0	0
• OutOfRange	0	0	0	0
• P_PID	0	0	0	0
• P_PID_:1	0	0	0	0

# 1. SoC\_handle\_FSM\_BaselineTest

Signal Name	Abs Tol	Rel Tol	Leading Tol	Lagging Tol
• I_PID_:1	0	0	0	0
• D_PID_:1	0	0	0	0
• OutOfRange	0	0	0	0
Baseline_FSM_Testing_rev4_captured.-mat	0	0	0	0.14999-9999999-99999
• D_PID	0	0	0	0.14999-9999999-99999
• I_PID	0	0	0	0.14999-9999999-99999
• OutOfRange	0	0	0	0.14999-9999999-99999
• P_PID	0	0	0	0.14999-9999999-99999
• P_PID_:1	0	0	0	0.14999-9999999-99999
• I_PID_:1	0	0	0	0.14999-9999999-99999
• D_PID_:1	0	0	0	0.14999-9999999-99999
• OutOfRange	0	0	0	0.14999-9999999-99999

## 1. SoC\_handle\_FSM\_BaselineTest

Signal Name	Abs Tol	Rel Tol	Leading Tol	Lagging Tol
Baseline_FSM_Testing_rev5_captured.-mat	0	0	0	0
• P_PID_:1 (Active)	0	0	0	0
• I_PID_:1 (Active)	0	0	0	0
• D_PID_:1 (Active)	0	0	0	0
• OutOfRange (Active)	0	0	0	0
Baseline_FSM_Testing_rev6_captured.-mat (Active)	0	0	0	0
• SoC:1 (Active)	0	0	0	0
• P_PID_:1 (Active)	0	0	0	0
• I_PID_:1 (Active)	0	0	0	0
• D_PID_:1 (Active)	0	0	0	0
• OutOfRange (Active)	0	0	0	0