Lindsey Bieda and Joe Frambach Reduction and Parallel Problems 10.31.2011

17. Consider the following problem. The input is an undirected graph G and an integer k. The problem is to determine if G contains a clique of size k **AND** an independent set of size k. Recall that a clique is a collection of mutually adjacent vertices, and an independent set is a collection of mutually nonadjacent vertices. Show by reduction that if this problem has a polynomial time algorithm then the clique problem has a polynomial time algorithm.

$\mathbf{CLIQUE} \leq \mathbf{CLIQUE_AND_INDSET}$

Program CLIQUE:

Read graph G, int k. G' = G + k nodes not connected to any other nodes in G. Return CLIQUE_AND_INDSET(G',k).

Since G will contain an independent set of size k as it is added to the original graph, CLIQUE_AND_INDSET will always find an independent set of size k. The only aspect that is determined by the original input is if there is a clique of size k, which is the desired functionality.

18. Consider the following problem. The input is an undirected graph G and an integer k. The problem is to determine if G contains a clique of size k **OR** an independent set of size k. Show by reduction that if this problem has a polynomial time algorithm then the clique problem has a polynomial time algorithm.

$\mathbf{CLIQUE} \leq \mathbf{CLIQUE_OR_INDSET}$

Program CLIQUE:

Read graph G, int k.

G' = G + a new node connected to all other nodes in G.

Return CLIQUE_OR_INDSET(G', k+1).

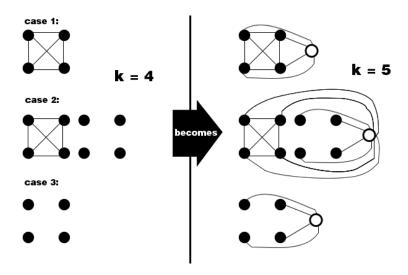


Figure 1: Construction of G'

Figure 1 shows how the construction of the new G' and by increasing the value of k we ensure that CLIQUE_OR_INDSET(G',k+1) will return true, if and only if G contains a clique of size k. This is the case since we eliminate the independent set by adding the new node and increase the size of the possibly existing clique with the addition of this new node.

- 22. Consider the following problem. The input is a graph G = (V, E), a subset R of vertices of G, and a positive integer k. The problem is to determine if there is a subset U of V such that
 - 1. All the vertices in R are contained in U, and
 - 2. the number of vertices in U is at most k, and
 - 3. for every pair of vertices x and y in R, one can walk from x to y in G only traversing vertices that are in U.

Show that this problem is NP-hard using a reduction from Vertex Cover. Recall that the input for the vertex cover problem is a graph H and an integer ℓ , and the problem is to determine whether H has a vertex cover of size ℓ or not. A vertex cover S is a collection of vertices with the property that every edge is incident on at least one vertex in S.

- 1. Consider the problem of computing the AND of n bits.
 - Give an algorithm that runs in time $O(\log n)$ using n processors on an EREW PRAM. What is the efficiency of this algorithm?

The parallel algorithm works as follows:

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AND(x_1, \ldots, x_n, p)
If p = 1:
p returns the value it has
Else:
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and(AND $(x_1, \ldots, x_{n/2}, p/2)$), AND $(x_{n/2+1}, \ldots, x_n, p/2)$), where lowercase and refers to performing the and of the two returned values.

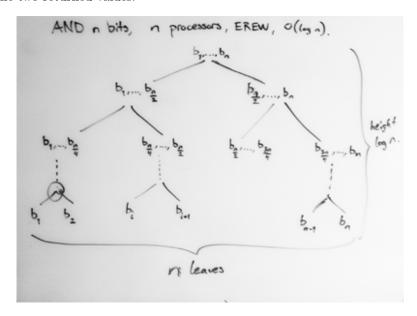


Figure 2: Construction of the tree

We end up with the above call tree which demonstrates that this algorithm performs in $O(\log n)$ time. The efficiency is: n/(2n-1)

• Give an algorithm that runs in time $O(\log n)$ using $n/\log n$ processors on an EREW PRAM. What is the efficiency of this algorithm?

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The parallel algorithm works as follows: \operatorname{AND}(x_1,\ldots,x_n,p) If p=1:

p performs the following:

For each bit:

if bit = 0: return 0

return 1

Else:
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and(AND $(x_1, \ldots, x_{n/2}, p/2)$), AND $(x_{n/2+1}, \ldots, x_n, p/2)$), where lowercase and refers to performing the and of the two returned values.

The tree structure is very similar to the one above, however, the height is now $\log(\log n)$ and there are

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n/(\log n) leaves. Each leaf takes \log(n) time to process the data. The efficiency is: n/(n+(n/\log n)-1)
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 \bullet Give an algorithm that runs in time O(1) using n processors on a CRCW PRAM.

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\begin{aligned} \text{Answer} &= 1 \\ \text{For processor} \ i &= 1 \text{ to } n \text{:} \\ processor_i \text{ runs:} \\ \text{If } b_i &= 0 \text{:} \\ \text{Anwser} &= 0 \text{.} \\ \text{return Answer.} \end{aligned}
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Each processor takes one bit and if it is a 0 they write a 0 to the answer memory location.