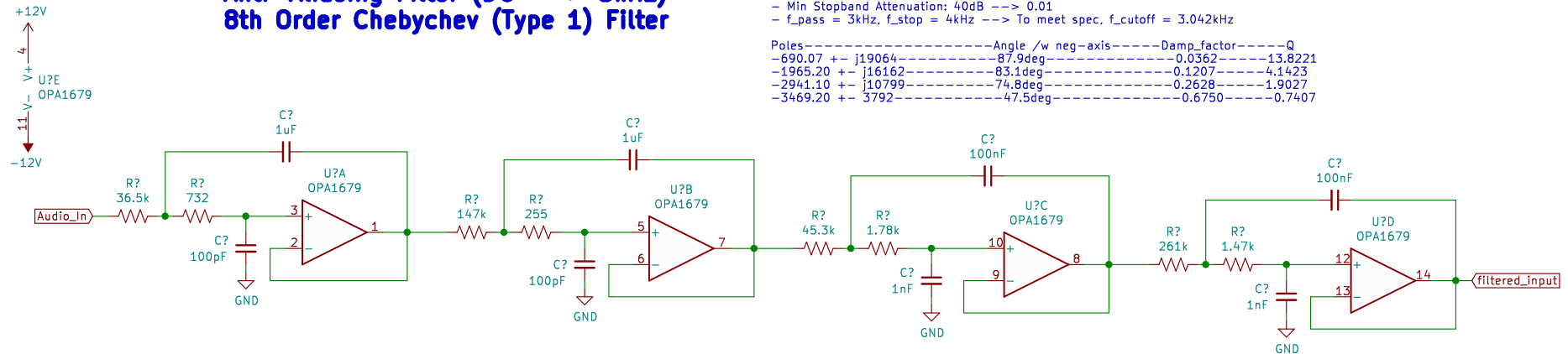
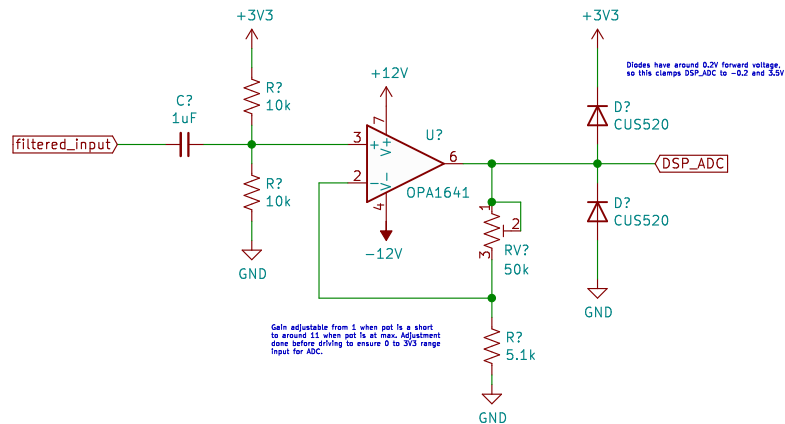


Anti-Aliasing Filter (DC --> 3kHz) 8th Order Chebyshev (Type 1) Filter

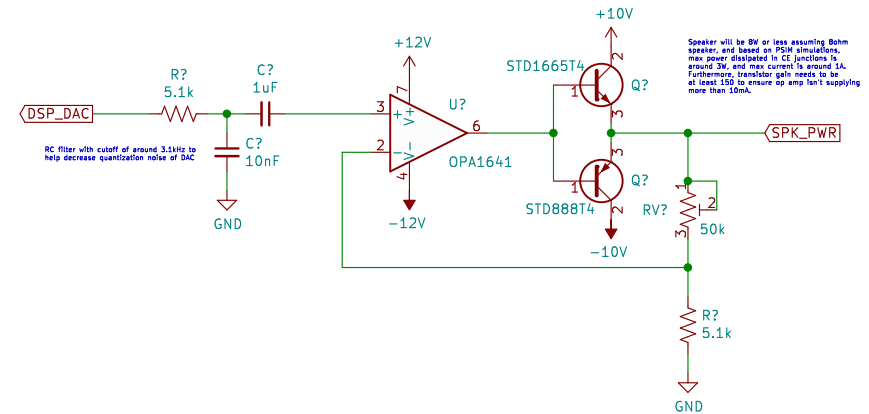


INPUT TO DSP'S ADC

Blocking Cap --> Add DC offset --> Adjustable gain to cover 0 to 3.3V range --> Clamping Diodes



POWER AMPLIFIER STAGE (CLASS B)



POWER

Sheet: Board Power



File: power.sch

Author: Abdullah Almosalami

North Dakota State University

Sheet: /

File: DSP_Audio_Board.sch

Title: DSP Audio Development Board

Size: A4 Date: 2020-11-06

KiCad E.D.A. kicad (5.1.7)-1

Rev: 1.0

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