Time)	100 ns 20	Olns :	300 ns 4	00 ns	500 lns 600	0 lns 70	0 hs 800 hs	3
LILIIC clk=0									
pc[31:0]=00000000	(00000000 X0+	<u> </u>	Vn+ Vnnnnn+ Vn+ Vn+		V0+ V0+ V0+ V0+ V0+	- X0+ X0+ X0+ X0+ X0+	Vn+ Vn+ Vn+ Vn+ Vn+	X0+ X0+ X0+ X0+ X0+ X	<u> </u>
<pre>instr_fetched[31:0]=xxxxxxxx</pre>		\(\frac{\frac}\firi}}}}}{\frac{\frac{\frac{\frac{\frac{\frac{\frac{\frac					<u> </u>		\(\lambda + \l
pcsel[1:0]=00			<u> </u>					7	
	(00			Ϋ́το	X00	X10 X00		X11 X00	
cpu_data_inp[31:0]=xxxxxxxx	xxxxxxx								
USB_FAKE									
data[7:0]=uu	uu								
rdata[31:0]=00000000	00000+XFF0000)FF							
decode, reg fetch									
instr_valid[31:0]=00000000	00000000	X3+ X3+ X4+ X3C1A0+ X3+							
regs_a[31:0]=00000000	0000000			+ (+)(0+ (+)(00000000	(+)(0101FF+ X0+	(00000000 \undersity) \(\undersity \undersi	(uuuuu+ (0+ (uu	uuuu+ (0+ (+)(0000000	0 (+
regs_b[31:0]=00000000	0000000	\uuuuuuuu \(\frac{1+}{1+}\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	0000+ (uuuuuuuu)	008000+ \(00000000	/+/+X0+ /uuuuu	uuu (0+ (uut	uuuuuu (0+ (uu	uuu+(00000000	
eq_fwd_a[31:0]=00000000	0000000	(uuuuu+ (00000+ (+ (1	+ / + / 00000+ / u+ / 0+	+ \(+ \(0 + \) + \(0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	(+)(010+)(0101F	+ X0+ Xu+ X00000000	(uuuuu+ (0+)(uu	uuuu+ (0+ (+) 0000000	0 (+
eq_fwd_b[31:0]=00000000	0000000	/uuuuu+ /1+ /10000+ /+/+	X0+ X+Xuuuuuuuu X	008+ \(1+ \) \(\) \(00000000 \)	(+)(+)(+)(uuuuu	uuu (0+ (uut	uuuuuu (0+ (uu	uuu+ (00000000	
exec									
operation[31:0]=0	(O)(1	X27 X22 X0 X27	X22 X0 X27 X22	2 (27)(22)(0)(1	X0 X1 X17				
ex_displ32[31:0]=uuuuuuuu	uuuuuuuu (00	000+\(0+ \(0+ \(0+ \(00000+ \)	X0+ X0+ X00000+ X0+	+ X0+ X0+ X0+ X00000-	+X0+X0+XF+X0+X00	000018			
alu_inp_a[31:0]=uuuuuuuu	uuuuuuuu (00	000000 X1+ Xu+ X00000+	X0+ Xu+ X00000+ X0+	+ X0+ X1+ Xu+ X00000	000 X0+ X0101F	FD8			
alu_inp_b[31:0]=uuuuuuuu	uuuuuuuu (00	000+\(0+ \(\chi 0+ \(\chi 1+ \(\chi 00000+ \)	X0+ X0+ X00000+ X0+	+ X0+ X0+ X1+ X00000	000 XF+ X0+ X00	000018			
result[31:0]=uuuuuuuu	uuuuuuuu (00	000+X1+ X1+ Xu+ X00800+	X0+ Xu+ X01010+ X0+	+ X1+ X1+ Xu+ X00000-	+ \u+ \(0+ \(0+ \(0+ \(0)+ \(0)+ \(0)= \(0	.01FFF0			
ex_a_c[4:0]=XXX	XXX (0	X26	χ29	X26 X0	X31 X0 X29 X0				
memory			,						
b_sel[3:0]=0000	(0000				У11	11			
wr=1					ì				
d_aval=1									
d_addr[31:0]=xxxxxxxx	xxxxxxx				You	.01FFF4			
data_inp[31:0]=xxxxxxxx	xxxxxxx				Λ				
add_mp[01 0] mmmmm									