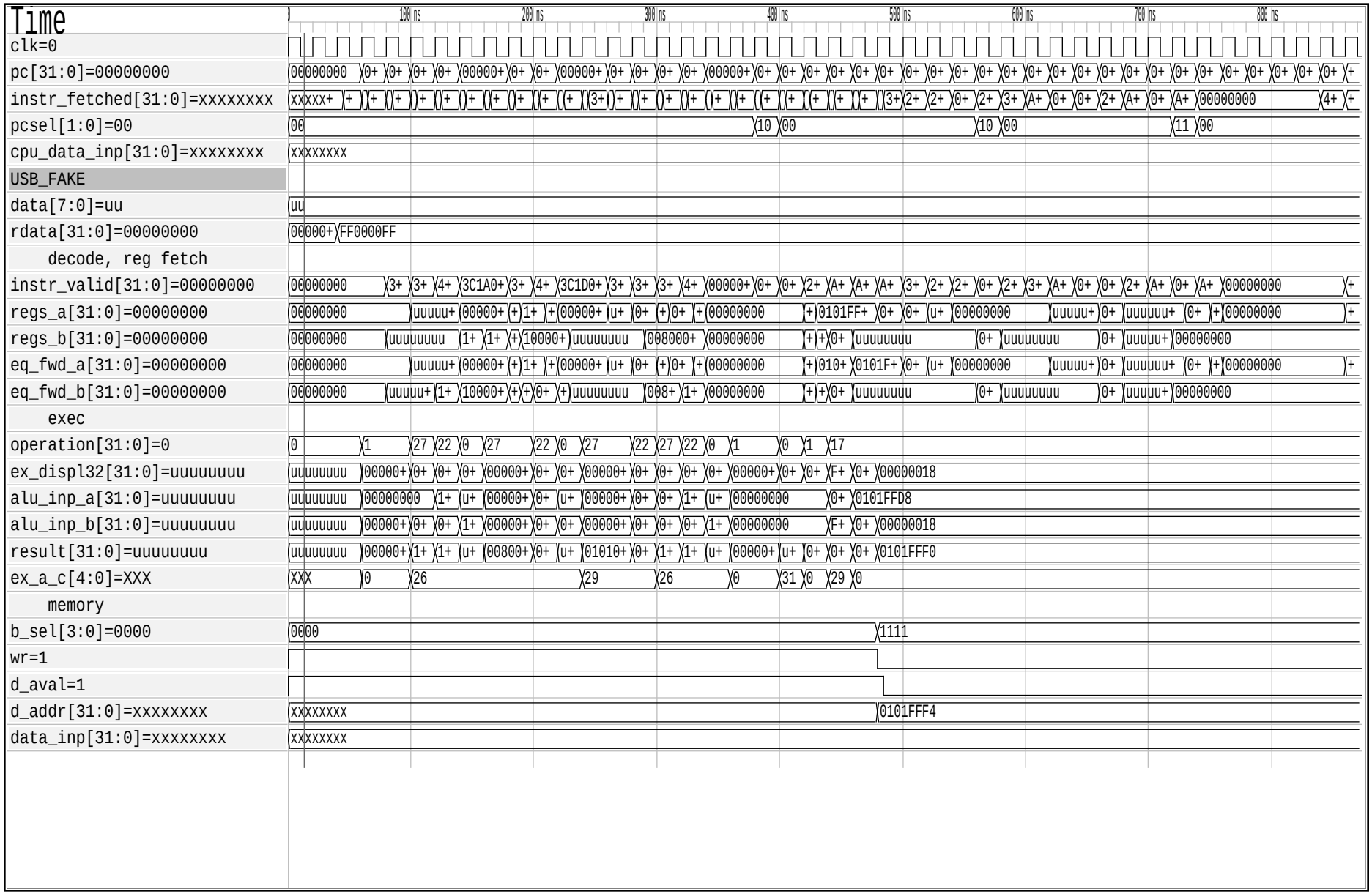
Time
0
100 ns
200 ns
300 ns
400 ns
500 ns
600 ns
700 ns
800 ns
clk=0
pc[31:0]=00000000
instr_fetched[31:0]=xxxxxxxx
pcsel[1:0]=00
cpu_data_inp[31:0]=xxxxxxxx
USB_FAKE
data[7:0]=uu
rdata[31:0]=00000000
decode, reg fetch
instr_valid[31:0]=00000000
regs_a[31:0]=00000000
regs_b[31:0]=00000000
eq_fwd_a[31:0]=00000000
eq_fwd_b[31:0]=00000000
exec
operation[31:0]=0
ex_displ32[31:0]=uuuuuuuu
alu_inp_a[31:0]=uuuuuuuu
alu_inp_b[31:0]=uuuuuuuu
result[31:0]=uuuuuuuu
ex_a_c[4:0]=XXX
memory
b_sel[3:0]=0000
wr=1
d_aval=1
d_addr[31:0]=xxxxxxxx
data_inp[31:0]=xxxxxxxx