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REVISION	DESCRIPTION	DATE	APPROVED
v1.0	Initial version	2023/09/18	Siavash Taher Parvar

RELEASED

Blender

SCHEMATIC STATUS: **RELEASED**

v1.0
2023/09/18
VARIANT: N/A

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TEMPLATE NOTES

Set Project Parameters

- 1- Go to View -> Pge Preview Setting
- 2- Set Parameters based on the following Info
- COMMENT 1: Project Version
- COMMENT 2: Document Status
- COMMENT 3: Doc. Approval Eng.
- COMMENT 4: BOM Ref. DOC.
- COMMENT 5: PCB Ref. DOC.
- COMMENT 6: GBR Ref. DOC.
- COMMENT 7: ASM Ref. DOC.
- COMMENT 8: Variant Name
- COMMENT 9: Revision Description

Symbols and Lables

Mark Not Fitted Components as --> **DNF**
Differential Signal Example
Net Class Example

SCHEMATIC STATUS:

- DRAFT - Very Early Stage of Schematic
- PRELIMINARY - Close to Final Schematic
- CHECKED - There Should Not Be Any Mistakes
- RELEASED - A Board with This Schematic Has Been Produced

DESIGN CONSIDERATION

INFO:
Example text for informational design notes.

CAUTIONARY:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

3D Preview TOP


3D Preview BOTTOM

Board Statistics Stackup Info

SHOULD BE ADDED
AFTER RELEASE

SHOULD BE ADDED
AFTER RELEASE

SHOULD BE ADDED
AFTER RELEASE

APPROVALS		DATE	PROJECT:			 Mend0z0
ENG: Siavash Taher Parvar		2023/09/18	Blender			
DSN: Siavash Taher Parvar		2023/09/18				
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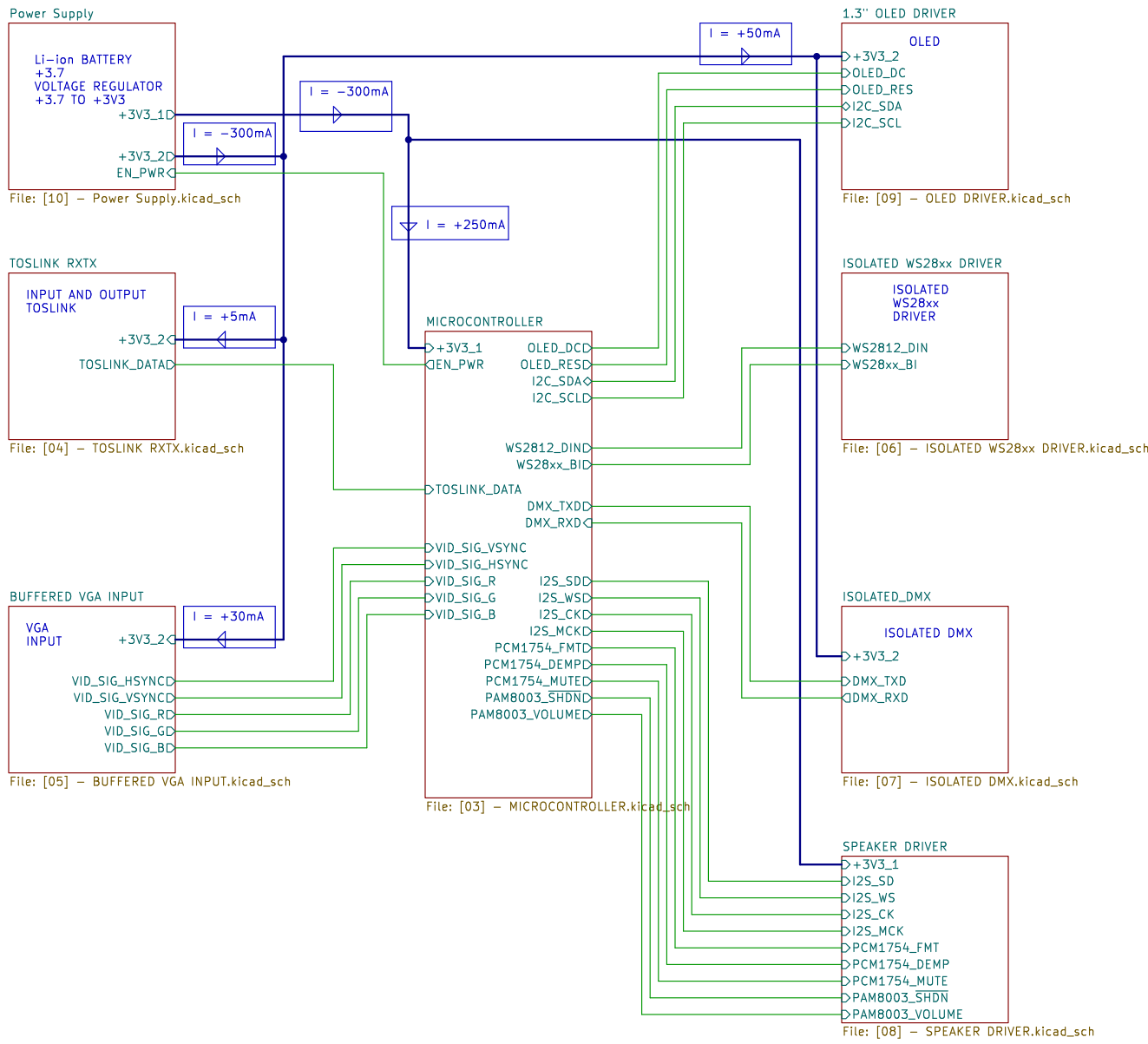



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BLOCK DIAGRAM

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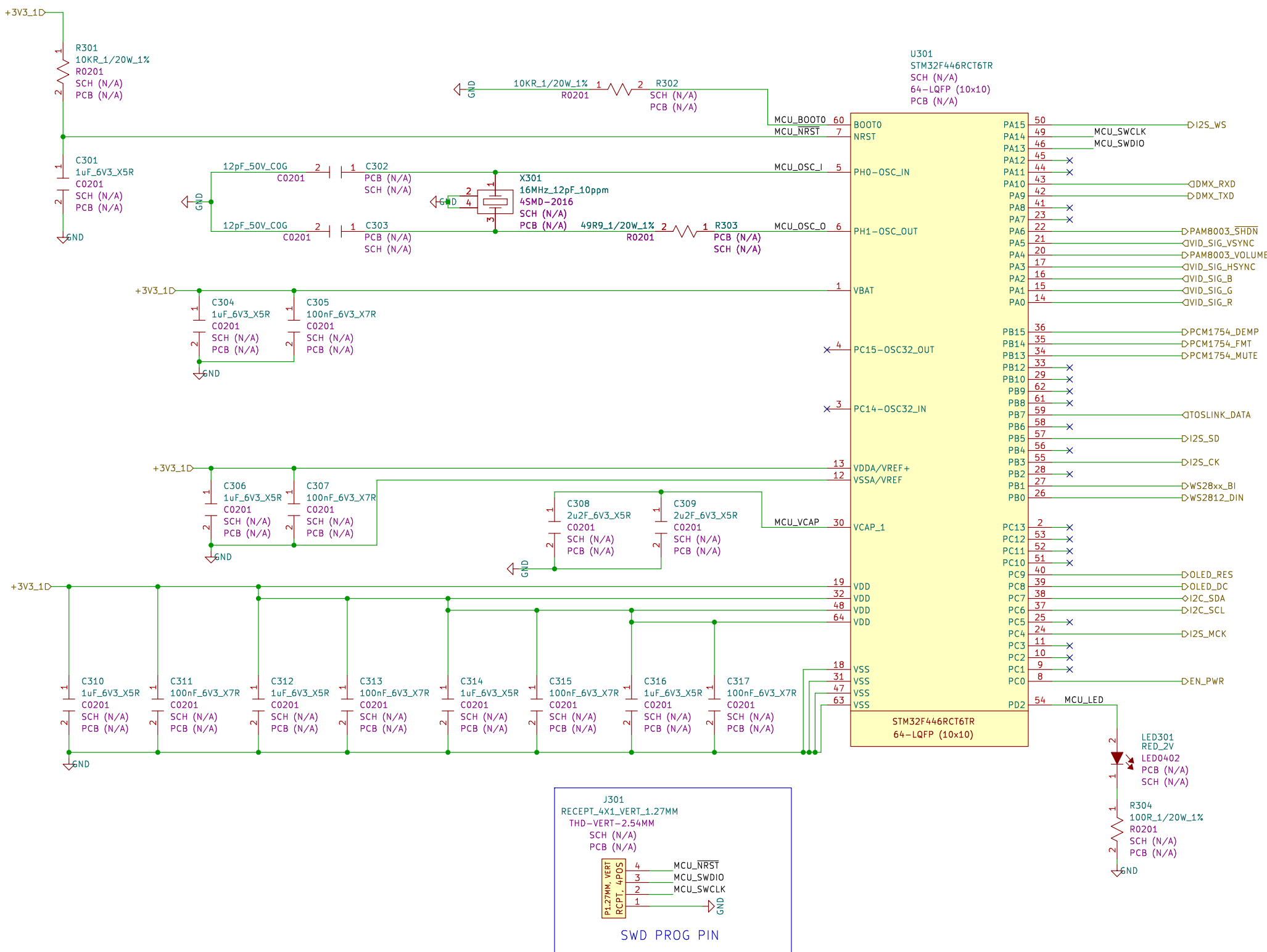
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MICROCONTROLLER

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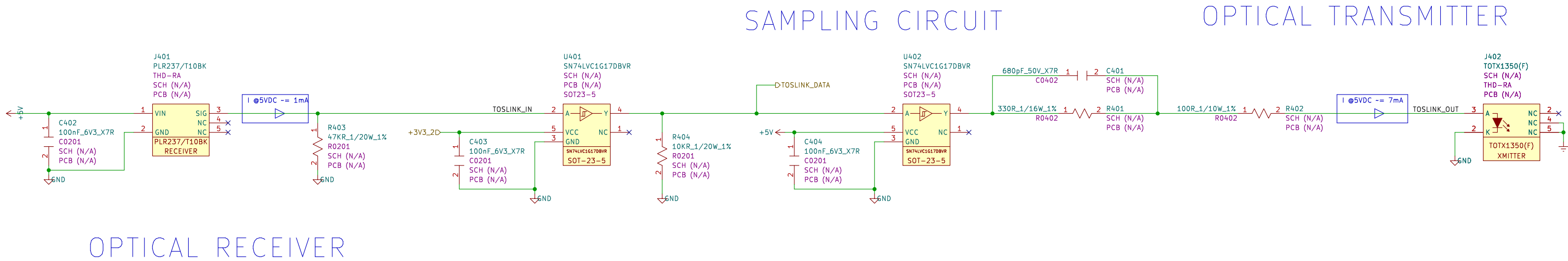
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TOSLINK RXTX

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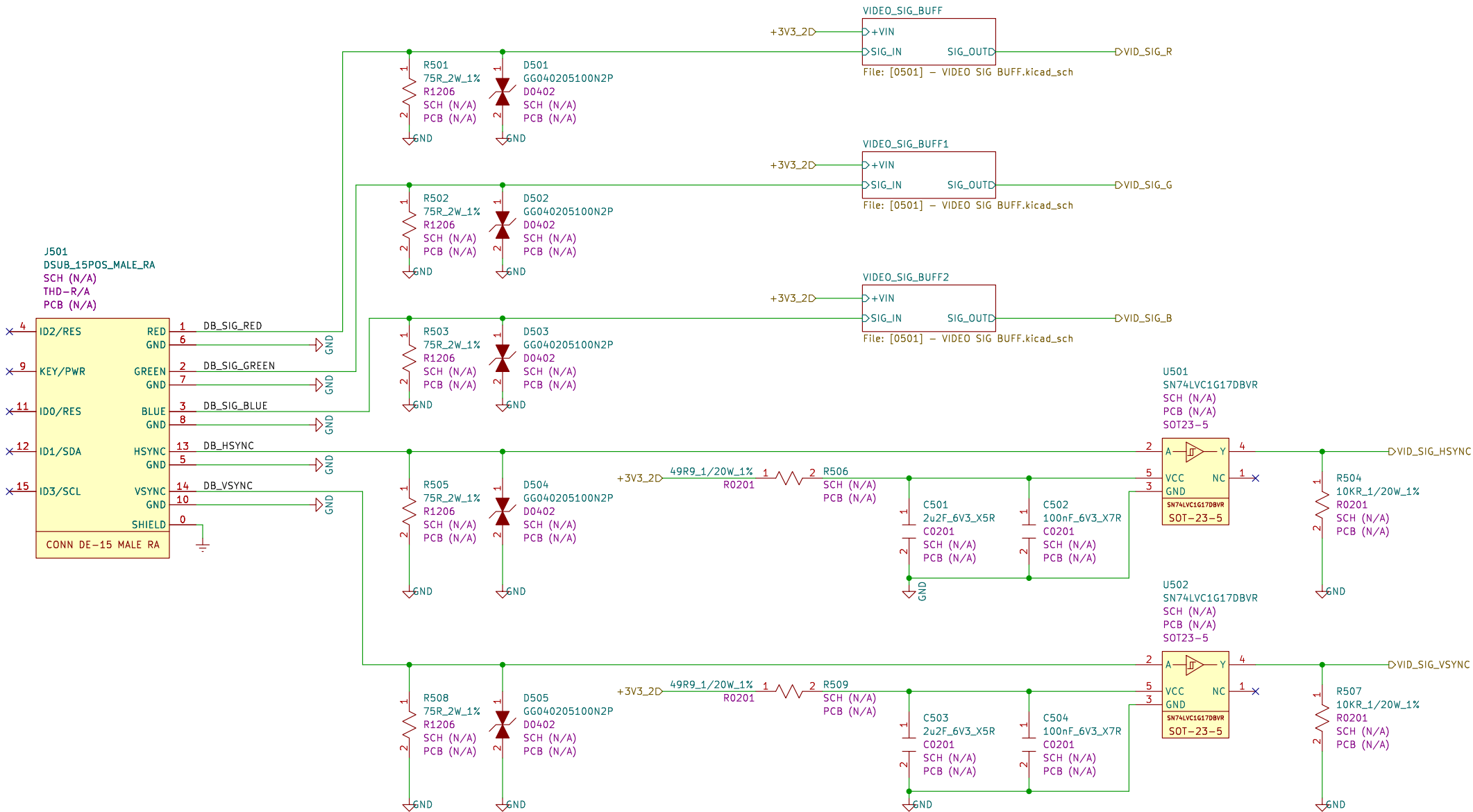
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BUFFERED VGA INPUT

RELEASED

- NOTE:
- Pinout Info
- 1: Red Video (To monitor from video card)
 - 2: Green Video (To monitor from video card)
 - 3: Blue Video (To monitor from video card)
 - 4: Monitor ID 2 (To video card from monitor)
 - 5: TTL Ground (Monitor self-test, used for testing purposes only)
 - 6: Red Analog Ground
 - 7: Green Analog Ground
 - 8: Blue Analog Ground
 - 9: Key (Plugged hole, not used for electronic signals)
 - 10: Sync Ground (For both sync pins)
 - 11: Monitor ID 0 (To video card from monitor)
 - 12: Monitor ID 1 (To video card from monitor)
 - 13: Horizontal Sync (To monitor from video card)
 - 14: Vertical Sync (To monitor from video card)
 - 15: Monitor ID 3 (To video card from monitor)
- NOTE:
- The most important pins on a monitor's connector are the synchronization, or sync pins. There are three: Horizontal, vertical, and ground. The sending of a horizontal sync pulse indicates the end of a horizontal line, and the sending of a vertical sync pulse indicates the end of a vertical screen frame.



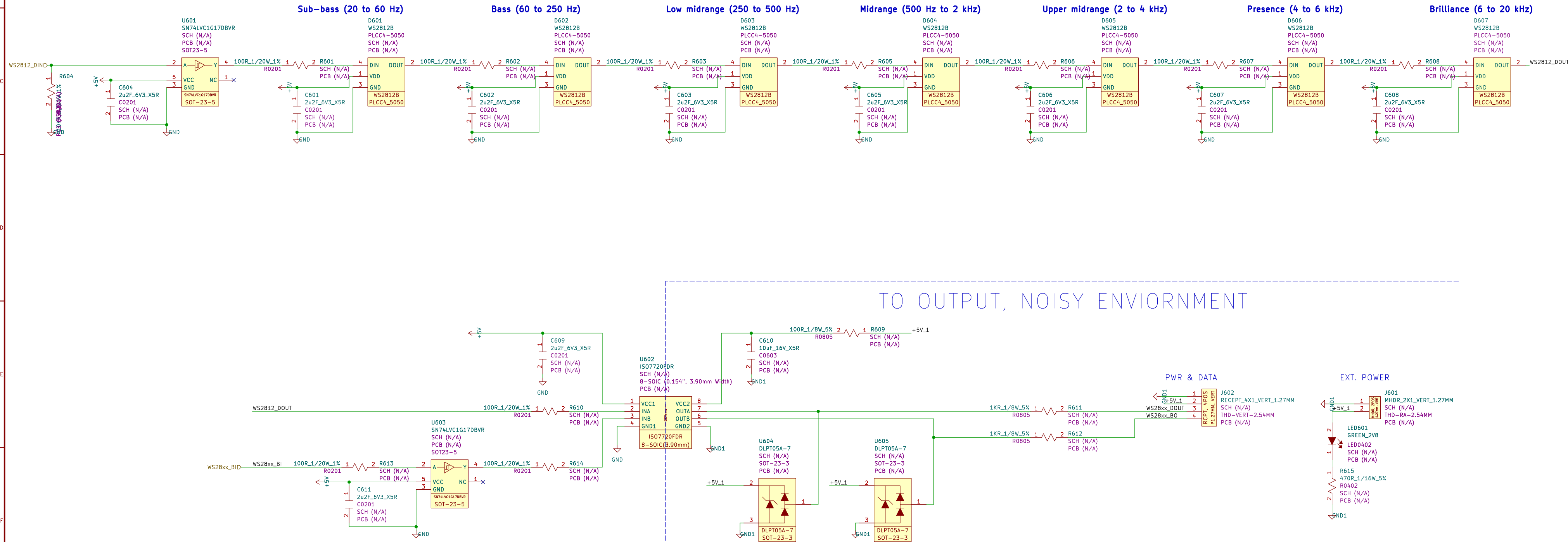
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ISOLATED WS28xx DRIVER

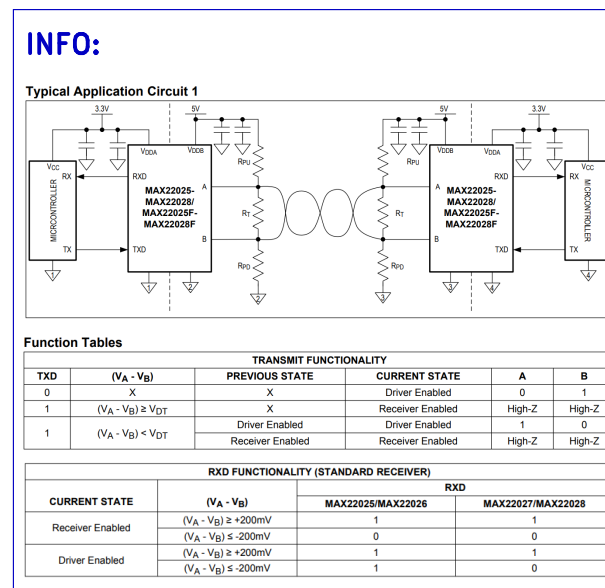
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ISOLATED_DMXX

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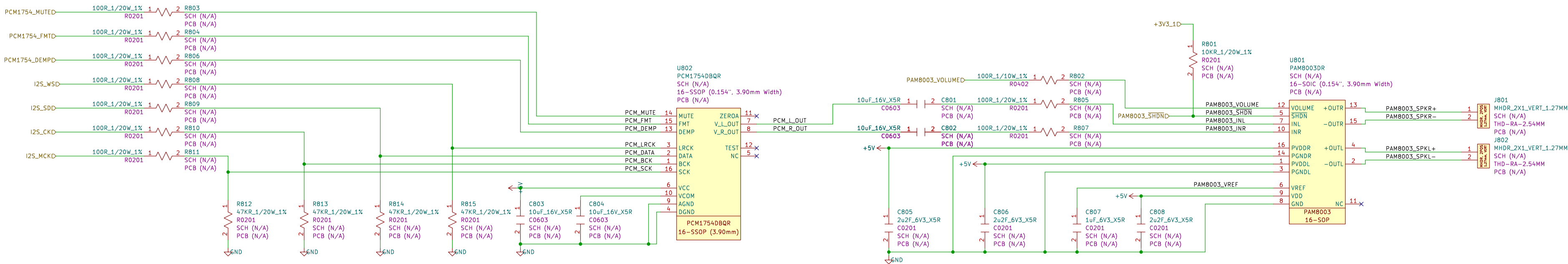
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SPEAKER DRIVER

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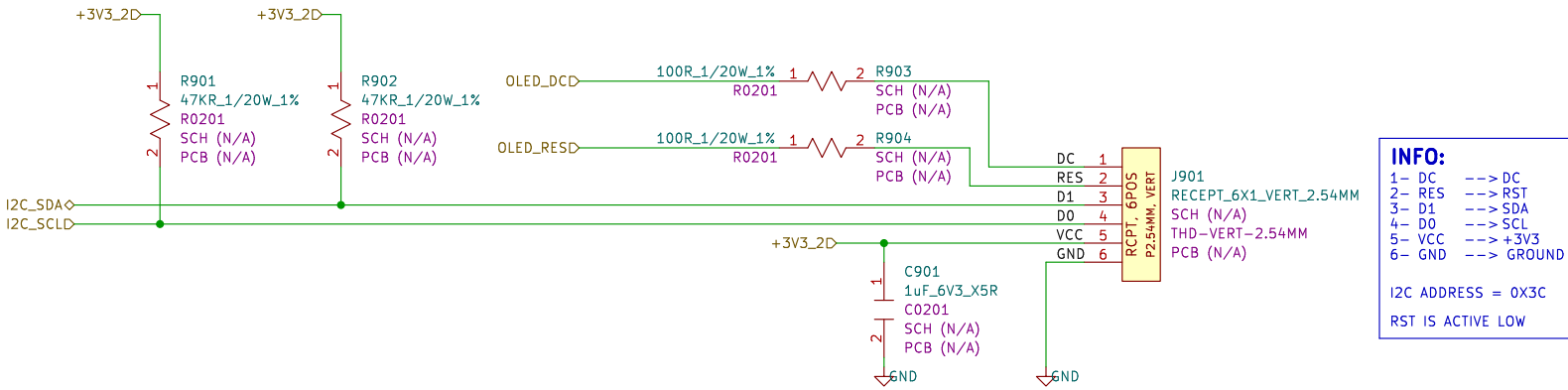
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1.3" OLED DRIVER

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DSN: Siavash Taher Parvar		2023/09/18				
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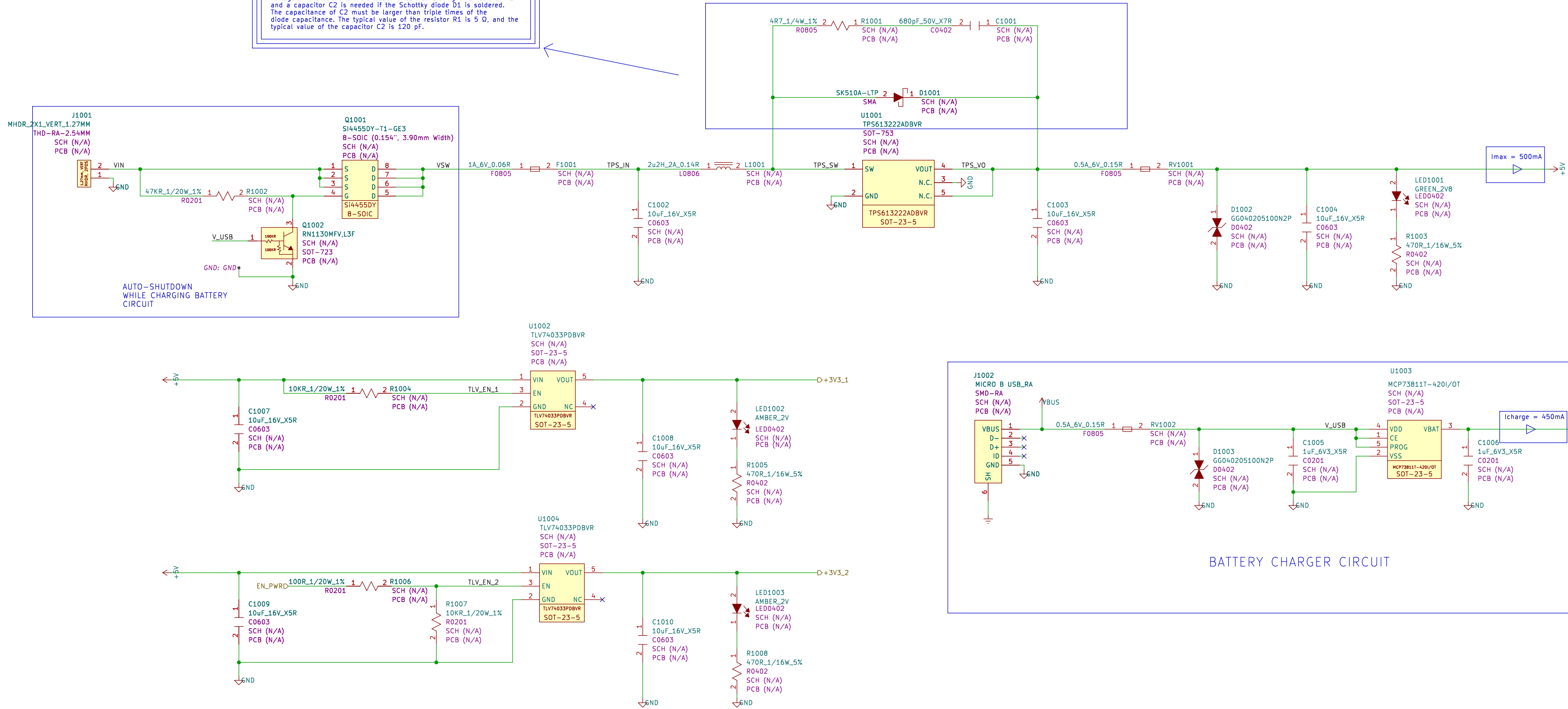
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Power Supply

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DESIGN NOTE:

The high switching frequency of TPS61322xx demands a high-speed rectifying switch for optimum efficiency. Ensure that the average and peak current rating of the diode exceeds the average output current and peak inductor current. In addition, the reverse breakdown voltage of the diode must exceed the maximum output voltage of the converter. A snubber circuit consisting of a resistor R1 and a capacitor C2 is needed if the Schottky diode D1 is soldered. The capacitance of C2 must be larger than triple times of the diode capacitance. The typical value of the resistor R1 is 5 Ω , and the typical value of the capacitor C2 is 120 pF.



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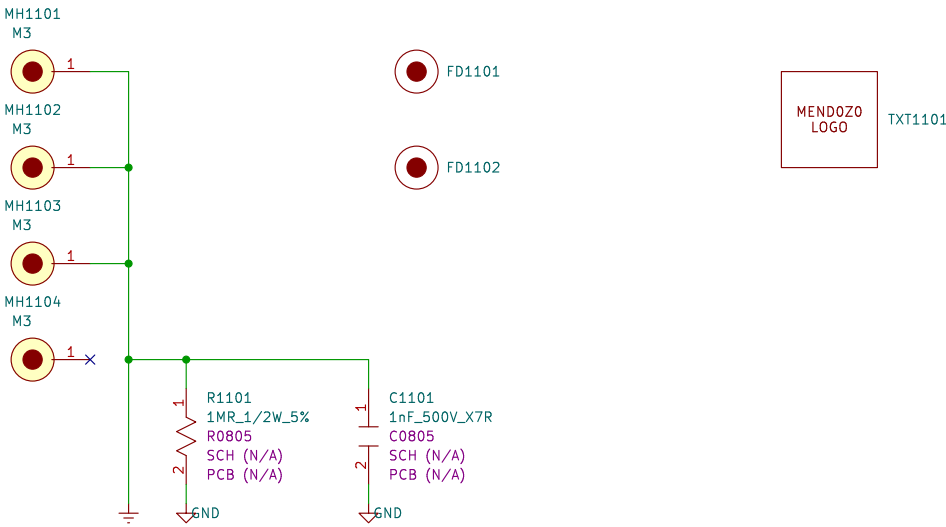
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MECHANICAL PARTS

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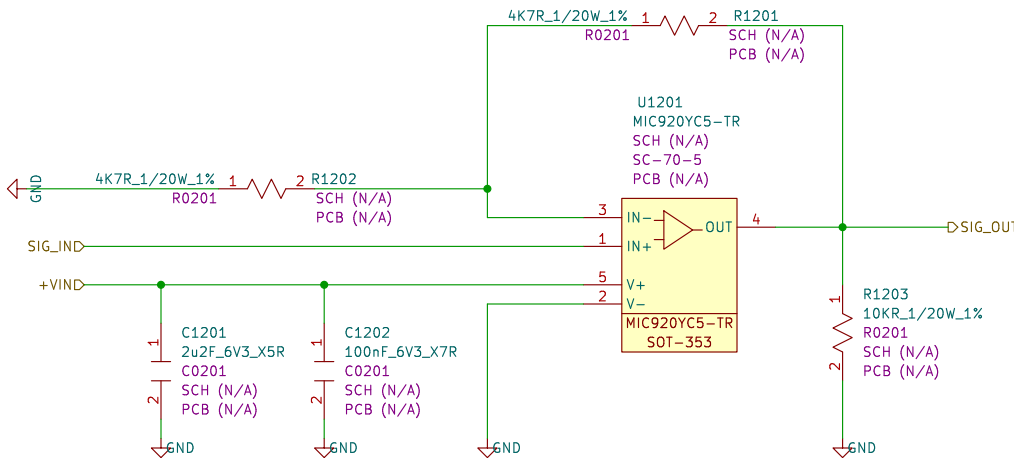
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VIDEO_SIG_BUFF

RELEASED



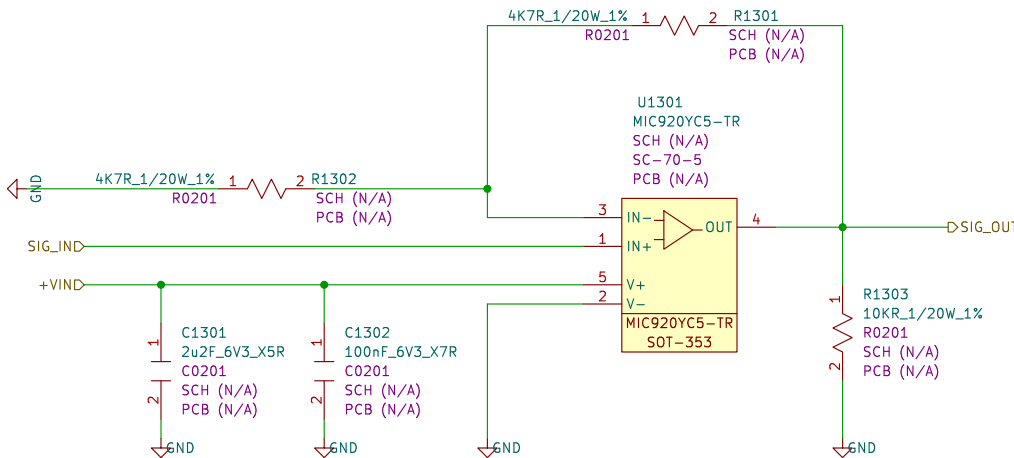
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VIDEO_SIG_BUFF1

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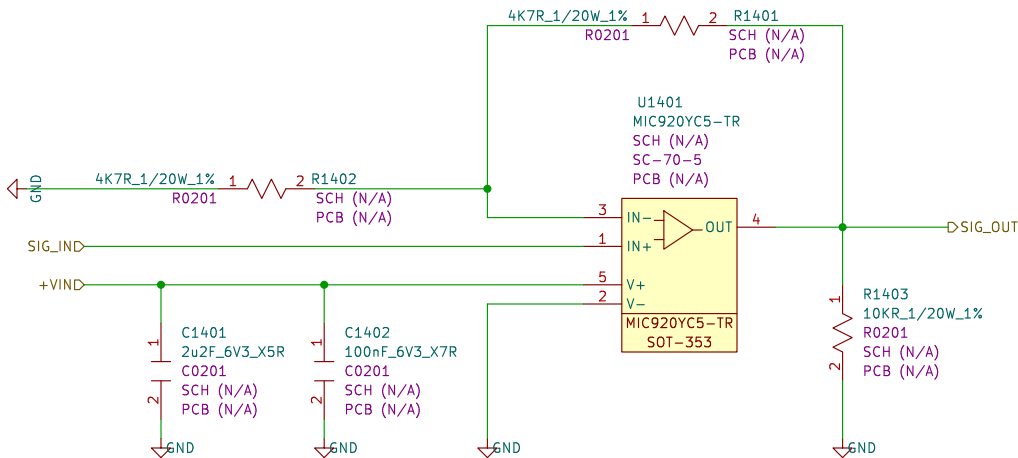
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VIDEO_SIG_BUFF2

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PWR SEQUENCE

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REVISION HISTORY

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