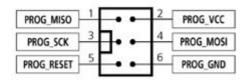
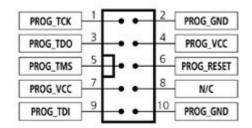
Megacard v4 - Schnittstellen

WA, 16.5.2016

a) ISP (Programmieren):

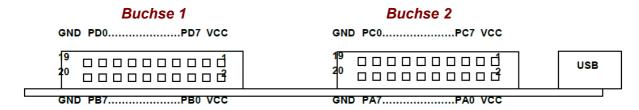
b) JTAG (Programmieren, OCD):





c) Parallele Schnittstellen:

Ansicht von vorne:



Buchse 1:

PIN	Funktion	Anschluss	PIN	Funktion	Anschluss
20	GND		19	GND	
18	PB7 (SCK)	LCD (D7), ISP	17	PD0 (RXD)	IR-Rx (1k5)
16	PB6 (MISO)	LCD (D6), ISP	15	PD1 (TXD)	IR-Tx (1k5)
14	PB5 (MOSI)	LCD (D5), ISP	13	PD2 (INT0)	USB- (1k5)
12	PB4 (SS)	Reset (X1), ISP	11	PD3 (INT1)	X5 (1k5)
10	PB3 (AIN1, OC0)	V5, → PA7(10k)	9	PD4 (OC1B)	
8	PB2 (AIN0, INT2)	USB- Pullup (1k5), LCD (D4)	7	PD5 (OC1A)	
6	PB1 (T1)	USB+ (68R)	5	PD6 (ICP1)	
4	PB0 (T0)	USB- (68R)	3	PD7 (OC2)	
2	VCC		1	VCC	

Buchse 2:

PIN	Funktion	Anschluss	PIN	Funktion	Anschluss
20	GND		19	GND	
18	PA7 (ADC7)	C10 (100n)	17	PC0 (SCL)	LED0 (X9)
16	PA6 (ADC6)	PTC (R25), RS (LCD)	15	PC1 (SDA)	LED1 (X9)
14	PA5 (ADC5)	R15 (3k3), R20 (Poti)	13	PC2 (TCK)	LED2, <i>JTAG</i>
12	PA4 (ADC4)	Piepser (X6), E (LCD)	11	PC3 (TMS)	LED3, <i>JTAG</i>
10	PA3 (ADC3)	Taster S3	9	PC4 (TDO)	LED4, <i>JTAG</i>
8	PA2 (ADC2)	Taster S2	7	PC5 (TDI)	LED5, <i>JTAG</i>
6	PA1 (ADC1)	Taster S1	5	PC6 (TOSC1)	LED6
4	PA0 (ADC0)	Taster S0	3	PC7 (TOSC2)	LED7
2	VCC		1	VCC	