

MIDTERM 2 / Final Project

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Primary Github address: github.com/mendos1/submission_da

Directory: DA6A

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.
2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/Midterm, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

- I. Atmega328p
- II. Jumper wires
- III. Breadboard
- IV. Apds device
- V. nodeMCU esp8266 device
- VI. USB cables

2. INITIAL/MODIFIED/DEVELOPED CODE OF TASK 1/A

```
/*                               Main.c File                               */

#define F_CPU 16000000UL
#include <avr/io.h>
#include <util/delay.h>
#include <stdio.h>
#include "i2c_master.h"
#include "uart.h"
#include "apds.h"

// This is used to set up a filestream to use UART_string
FILE UART_string = FDEV_SETUP_STREAM(uart_putchar, NULL , _FDEV_SETUP_WRITE);

// This array of chars is where we will store our result
char The_Result[256];

int main(void)
{
    // red, green, and blue are the rgb components that we want.
    uint16_t red = 0, green = 0, blue = 0;

    // Initialize I2C communication protocol
    i2c_init();

    // Initialize UART communication protocol
    init_UART();

    // variable used for UART string streaming
    stdout = &UART_string;

    // Initialize APDS device
    APDS_init();

    _delay_ms(2000);
    printf("AT\r\n");

    _delay_ms(5000);
    printf("AT+CWMODE=1\r\n");

    _delay_ms(5000);
    printf("AT+CWJAP=\"Pornhub.com\", \"xoxo123\" \r\n");
```

```

while (1)
{
    _delay_ms(5000);
    printf("AT+CIPMUX=0\r\n");

    _delay_ms(5000);
    printf("AT+CIPSTART=\"TCP\", \"api.thingspeak.com\", 80\r\n");

    _delay_ms(5000);
    RGB_reader(&red, &green, &blue);
    printf("AT+CIPSEND=104\r\n");
    printf("GET
https://api.thingspeak.com/update?api_key=Q099IRW0GDEGZYV&field1=0%05u&field2=%05u&field
3=%05u\r\n", red, green, blue);

    _delay_ms(3000);
}
}

```

```

/* I2C Lib */

#ifndef I2C_MASTER_H
#define I2C_MASTER_H

#define I2C_READ 0x01
#define I2C_WRITE 0x00

void i2c_init(void);
uint8_t i2c_start(uint8_t address);
uint8_t i2c_write(uint8_t data);
uint8_t i2c_read_ack(void);
uint8_t i2c_read_nack(void);
uint8_t i2c_transmit(uint8_t address, uint8_t* data, uint16_t length);
uint8_t i2c_receive(uint8_t address, uint8_t* data, uint16_t length);
uint8_t i2c_writeReg(uint8_t devaddr, uint8_t regaddr, uint8_t* data, uint16_t length);
uint8_t i2c_readReg(uint8_t devaddr, uint8_t regaddr, uint8_t* data, uint16_t length);
void i2c_stop(void);

#endif // I2C_MASTER_H

////////////////////////////////////

#ifndef F_CPU
#define F_CPU 16000000UL
#endif

#include <avr/io.h>
#include <util/twi.h>

#include "i2c_master.h"

#define F_SCL 100000UL // SCL frequency
#define Prescaler 1
#define TWBR_val (((F_CPU / F_SCL) / Prescaler) - 16) / 2

void i2c_init(void)

```

```

{
    TWBR = (uint8_t)TWBR_val;
}

uint8_t i2c_start(uint8_t address)
{
    // reset TWI control register
    TWCR = 0;
    // transmit START condition
    TWCR = (1<<TWINT) | (1<<TWSTA) | (1<<TWEN);
    // wait for end of transmission
    while( !(TWCR & (1<<TWINT)) );

    // check if the start condition was successfully transmitted
    if((TWSR & 0xF8) != TW_START){ return 1; }

    // load slave address into data register
    TWDR = address;
    // start transmission of address
    TWCR = (1<<TWINT) | (1<<TWEN);
    // wait for end of transmission
    while( !(TWCR & (1<<TWINT)) );

    // check if the device has acknowledged the READ / WRITE mode
    uint8_t twst = TW_STATUS & 0xF8;
    if ( (twst != TW_MT_SLA_ACK) && (twst != TW_MR_SLA_ACK) ) return 1;

    return 0;
}

uint8_t i2c_write(uint8_t data)
{
    // load data into data register
    TWDR = data;
    // start transmission of data
    TWCR = (1<<TWINT) | (1<<TWEN);
    // wait for end of transmission
    while( !(TWCR & (1<<TWINT)) );

    if( (TWSR & 0xF8) != TW_MT_DATA_ACK ){ return 1; }

    return 0;
}

uint8_t i2c_read_ack(void)
{
    // start TWI module and acknowledge data after reception
    TWCR = (1<<TWINT) | (1<<TWEN) | (1<<TWEA);
    // wait for end of transmission
    while( !(TWCR & (1<<TWINT)) );
    // return received data from TWDR
    return TWDR;
}

uint8_t i2c_read_nack(void)
{

```

```

        // start receiving without acknowledging reception
        TWCR = (1<<TWINT) | (1<<TWEN);
        // wait for end of transmission
        while( !(TWCR & (1<<TWINT)) );
        // return received data from TWDR
        return TWDR;
    }

uint8_t i2c_transmit(uint8_t address, uint8_t* data, uint16_t length)
{
    if (i2c_start(address | I2C_WRITE)) return 1;

    for (uint16_t i = 0; i < length; i++)
    {
        if (i2c_write(data[i])) return 1;
    }

    i2c_stop();

    return 0;
}

uint8_t i2c_receive(uint8_t address, uint8_t* data, uint16_t length)
{
    if (i2c_start(address | I2C_READ)) return 1;

    for (uint16_t i = 0; i < (length-1); i++)
    {
        data[i] = i2c_read_ack();
    }
    data[(length-1)] = i2c_read_nack();

    i2c_stop();

    return 0;
}

uint8_t i2c_writeReg(uint8_t devaddr, uint8_t regaddr, uint8_t* data, uint16_t length)
{
    if (i2c_start(devaddr | 0x00)) return 1;

    i2c_write(regaddr);

    for (uint16_t i = 0; i < length; i++)
    {
        if (i2c_write(data[i])) return 1;
    }

    i2c_stop();

    return 0;
}

uint8_t i2c_readReg(uint8_t devaddr, uint8_t regaddr, uint8_t* data, uint16_t length)
{
    if (i2c_start(devaddr)) return 1;

    i2c_write(regaddr);

```

```

        if (i2c_start(devaddr | 0x01)) return 1;

        for (uint16_t i = 0; i < (length-1); i++)
        {
            data[i] = i2c_read_ack();
        }
        data[(length-1)] = i2c_read_nack();

        i2c_stop();

        return 0;
    }

    void i2c_stop(void)
    {
        // transmit STOP condition
        TWCR = (1<<TWINT) | (1<<TWEN) | (1<<TWSTO);
    }

```

```

/*                                     UART lib                                     */

```

```

#ifndef UART_328P_H
#define UART_328P_H

```

```

#ifndef F_CPU
#define F_CPU 16000000UL
#endif

```

```

#include <stdio.h>
#include <avr/io.h>
#include <avr/interrupt.h>
#define BAUD 9600
#define BRGVAL (F_CPU/16/BAUD) - 1

```

```

void init_UART();
int uart_putchar( char c, FILE *stream);

```

```

#endif

```

```

////////////////////////////////////

```

```

#include <stdio.h>
#include <avr/io.h>
#include <avr/interrupt.h>
#include "uart.h"

```

```

void init_UART(void){
    //Set baud rate
    uint16_t baud_rate = BRGVAL;
    UBRR0H = baud_rate >> 8;
    UBRR0L = baud_rate & 0xFF;

    //Enable receiver and transmitter
    UCSRB = ( 1 <<RXEN0)|( 1 <<TXEN0);

    // Set frame format: 8data, 1stop bit

```

```

        UCSR0C = (3 << UCSZ00);
    }

    int uart_putchar(char c, FILE *stream){
        //wait until buffer empty
        while ( !( UCSR0A & ( 1 <<UDRE0)) );

        //Put data into buffer
        UDR0 = c;
        return 0;
    }

```

```

/*                      ADPS lib                      */

```

```

#ifndef APDS_H
#define APDS_H

#include <avr/io.h>
#include "i2c_master.h"
#include "apds.h"

#define APDS_WRITE    (0x39 << 1) | 0
#define APDS_READ     (0x39 << 1) | 1

/* Debug */

/* APDS-9960 I2C address */
#define APDS9960_I2C_ADDR    0x39

/* Gesture parameters */
#define GESTURE_THRESHOLD_OUT    10
#define GESTURE_SENSITIVITY_1    50
#define GESTURE_SENSITIVITY_2    20

/* Error code for returned values */
#define ERROR    0xFF

/* Acceptable device IDs */
#define APDS9960_ID_1    0xAB
#define APDS9960_ID_2    0x9C

/* Misc parameters */
#define FIFO_PAUSE_TIME    30    // Wait period (ms) between FIFO reads

/* APDS-9960 register addresses */
#define APDS9960_ENABLE    0x80
#define APDS9960_ATIME    0x81
#define APDS9960_WTIME    0x83
#define APDS9960_AILT    0x84
#define APDS9960_AILTH    0x85
#define APDS9960_AIHT    0x86
#define APDS9960_AIHTh    0x87
#define APDS9960_PILT    0x89
#define APDS9960_PIHT    0x8B
#define APDS9960_PERS    0x8C
#define APDS9960_CONFIG1    0x8D
#define APDS9960_PPULSE    0x8E
#define APDS9960_CONTROL    0x8F

```

```

#define APDS9960_CONFIG2      0x90
#define APDS9960_ID           0x92
#define APDS9960_STATUS      0x93
#define APDS9960_CDATAL      0x94
#define APDS9960_CDATAH      0x95
#define APDS9960_RDATAL      0x96
#define APDS9960_RDATAH      0x97
#define APDS9960_GDATAL      0x98
#define APDS9960_GDATAH      0x99
#define APDS9960_BDATAL      0x9A
#define APDS9960_BDATAH      0x9B
#define APDS9960_PDATA       0x9C
#define APDS9960_POFFSET_UR  0x9D
#define APDS9960_POFFSET_DL  0x9E
#define APDS9960_CONFIG3     0x9F
#define APDS9960_GPENTH      0xA0
#define APDS9960_GEXTH       0xA1
#define APDS9960_GCONF1      0xA2
#define APDS9960_GCONF2      0xA3
#define APDS9960_GOFFSET_U   0xA4
#define APDS9960_GOFFSET_D   0xA5
#define APDS9960_GOFFSET_L   0xA7
#define APDS9960_GOFFSET_R   0xA9
#define APDS9960_GPULSE      0xA6
#define APDS9960_GCONF3      0xAA
#define APDS9960_GCONF4      0xAB
#define APDS9960_GFLVL       0xAE
#define APDS9960_GSTATUS     0xAF
#define APDS9960_IFORCE      0xE4
#define APDS9960_PICLEAR     0xE5
#define APDS9960_CICLEAR     0xE6
#define APDS9960_AICLEAR     0xE7
#define APDS9960_GFIFO_U     0xFC
#define APDS9960_GFIFO_D     0xFD
#define APDS9960_GFIFO_L     0xFE
#define APDS9960_GFIFO_R     0xFF

/* Bit fields */
#define APDS9960_PON          0b00000001
#define APDS9960_AEN          0b00000010
#define APDS9960_PEN          0b00000100
#define APDS9960_WEN          0b00001000
#define APDS9960_AIEN         0b00010000
#define APDS9960_PIEN         0b00100000
#define APDS9960_GEN          0b01000000
#define APDS9960_GVALID       0b00000001

/* On/Off definitions */
#define OFF                    0
#define ON                      1

/* Acceptable parameters for setMode */
#define POWER                   0
#define AMBIENT_LIGHT           1
#define PROXIMITY               2
#define WAIT                     3
#define AMBIENT_LIGHT_INT       4
#define PROXIMITY_INT           5

```



```

#define GESTURE                6
#define ALL                    7

/* LED Drive values */
#define LED_DRIVE_100MA        0
#define LED_DRIVE_50MA         1
#define LED_DRIVE_25MA         2
#define LED_DRIVE_12_5MA       3

/* Proximity Gain (PGAIN) values */
#define PGAIN_1X                0
#define PGAIN_2X                1
#define PGAIN_4X                2
#define PGAIN_8X                3

/* ALS Gain (AGAIN) values */
#define AGAIN_1X                0
#define AGAIN_4X                1
#define AGAIN_16X               2
#define AGAIN_64X               3

/* Gesture Gain (GGAIN) values */
#define GGAIN_1X                0
#define GGAIN_2X                1
#define GGAIN_4X                2
#define GGAIN_8X                3

/* LED Boost values */
#define LED_BOOST_100           0
#define LED_BOOST_150           1
#define LED_BOOST_200           2
#define LED_BOOST_300           3

/* Gesture wait time values */
#define GWTIME_0MS              0
#define GWTIME_2_8MS            1
#define GWTIME_5_6MS            2
#define GWTIME_8_4MS            3
#define GWTIME_14_0MS           4
#define GWTIME_22_4MS           5
#define GWTIME_30_8MS           6
#define GWTIME_39_2MS           7

/* Default values */
#define DEFAULT_ETIME            219    // 103ms
#define DEFAULT_WTIME            246    // 27ms
#define DEFAULT_PROX_PPULSE      0x87    // 16us, 8 pulses
#define DEFAULT_GESTURE_PPULSE   0x89    // 16us, 10 pulses
#define DEFAULT_POFFSET_UR        0      // 0 offset
#define DEFAULT_POFFSET_DL        0      // 0 offset
#define DEFAULT_CONFIG1          0x60    // No 12x wait (WTIME) factor
#define DEFAULT_LDRIVE            LED_DRIVE_100MA
#define DEFAULT_PGAIN             PGAIN_4X
#define DEFAULT_AGAIN             AGAIN_4X
#define DEFAULT_PILT              0      // Low proximity threshold
#define DEFAULT_PIHT              50     // High proximity threshold
#define DEFAULT_AILT              0xFFFF // Force interrupt for calibration
#define DEFAULT_AIHT              0

```

```

#define DEFAULT_PERS          0x11    // 2 consecutive prox or ALS for int.
#define DEFAULT_CONFIG2      0x01    // No saturation interrupts or LED boost
#define DEFAULT_CONFIG3      0        // Enable all photodiodes, no SAI
#define DEFAULT_GPENTH       40      // Threshold for entering gesture mode
#define DEFAULT_GEXTH        30      // Threshold for exiting gesture mode
#define DEFAULT_GCONF1       0x40    // 4 gesture events for int., 1 for exit
#define DEFAULT_GGAIN         GGAIN_4X
#define DEFAULT_GLDRIIVE     LED_DRIVE_100MA
#define DEFAULT_GWTIME        GWTIME_2_8MS
#define DEFAULT_GOFFSET      0        // No offset scaling for gesture mode
#define DEFAULT_GPULSE       0xC9    // 32us, 10 pulses
#define DEFAULT_GCONF3       0        // All photodiodes active during gesture
#define DEFAULT_GIEN         0        // Disable gesture interrupts

```

```

void APDS_init();
void RGB_reader();

```

```

#endif

```

```

////////////////////////////////////

```

```

#include <avr/io.h>
#include "i2c_master.h"
#include "apds.h"

```

```

void APDS_init(){
    uint8_t setup;
    i2c_readReg(APDS_WRITE, APDS9960_ID, &setup, 1);
    if(setup != APDS9960_ID_1) while(1);
    setup = 1 << 1 | 1<<0 | 1<<3 | 1<<4;
    i2c_writeReg(APDS_WRITE, APDS9960_ENABLE, &setup, 1);
    setup = DEFAULT_ETIME;
    i2c_writeReg(APDS_WRITE, APDS9960_ETIME, &setup, 1);
    setup = DEFAULT_WTIME;
    i2c_writeReg(APDS_WRITE, APDS9960_WTIME, &setup, 1);
    setup = DEFAULT_PROX_PPULSE;
    i2c_writeReg(APDS_WRITE, APDS9960_PPULSE, &setup, 1);
    setup = DEFAULT_POFFSET_UR;
    i2c_writeReg(APDS_WRITE, APDS9960_POFFSET_UR, &setup, 1);
    setup = DEFAULT_POFFSET_DL;
    i2c_writeReg(APDS_WRITE, APDS9960_POFFSET_DL, &setup, 1);
    setup = DEFAULT_CONFIG1;
    i2c_writeReg(APDS_WRITE, APDS9960_CONFIG1, &setup, 1);
    setup = DEFAULT_PERS;
    i2c_writeReg(APDS_WRITE, APDS9960_PERS, &setup, 1);
    setup = DEFAULT_CONFIG2;
    i2c_writeReg(APDS_WRITE, APDS9960_CONFIG2, &setup, 1);
    setup = DEFAULT_CONFIG3;
    i2c_writeReg(APDS_WRITE, APDS9960_CONFIG3, &setup, 1);
}

```

```

void RGB_reader(uint16_t *red, uint16_t *green, uint16_t *blue){
    uint8_t redl, redh;
    uint8_t greenl, greenh;
    uint8_t blue1, blueh;
    i2c_readReg(APDS_WRITE, APDS9960_RDATAL, &redl, 1);
    i2c_readReg(APDS_WRITE, APDS9960_RDATAH, &redh, 1);

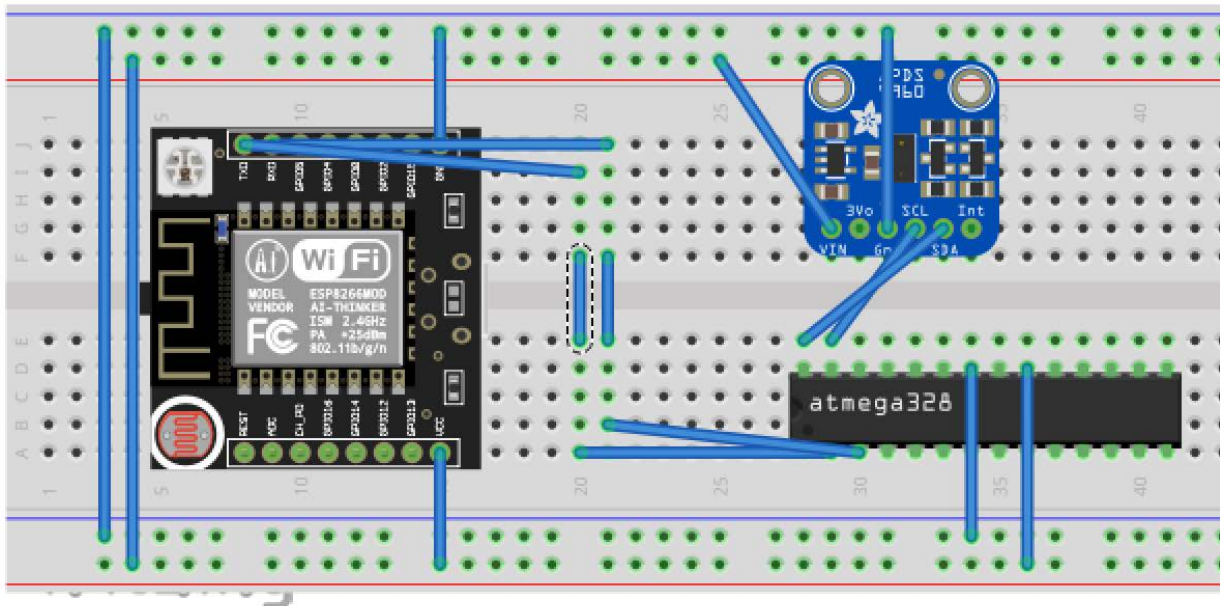
```

```

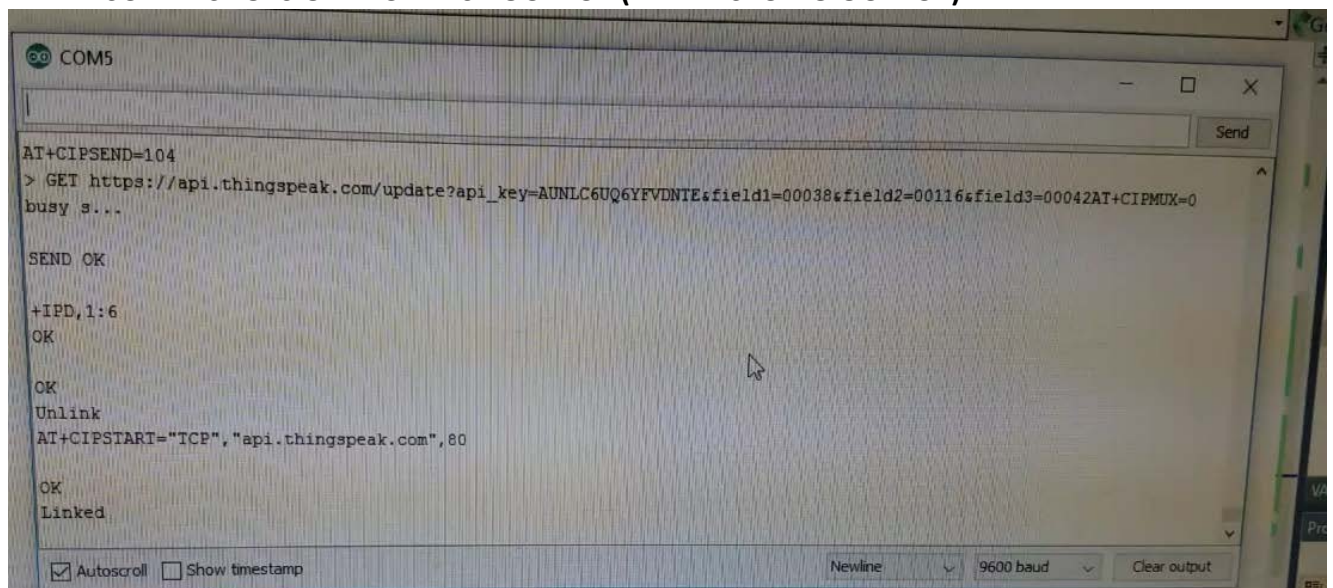
i2c_readReg(APDS_WRITE, APDS9960_GDATA_L, &greenl, 1);
i2c_readReg(APDS_WRITE, APDS9960_GDATA_H, &greenh, 1);
i2c_readReg(APDS_WRITE, APDS9960_BDATA_L, &bluel, 1);
i2c_readReg(APDS_WRITE, APDS9960_BDATA_H, &blueh, 1);
*red = redh << 8 | redl;
*green = greenh << 8 | greenl;
*blue = blueh << 8 | blueh;
}

```

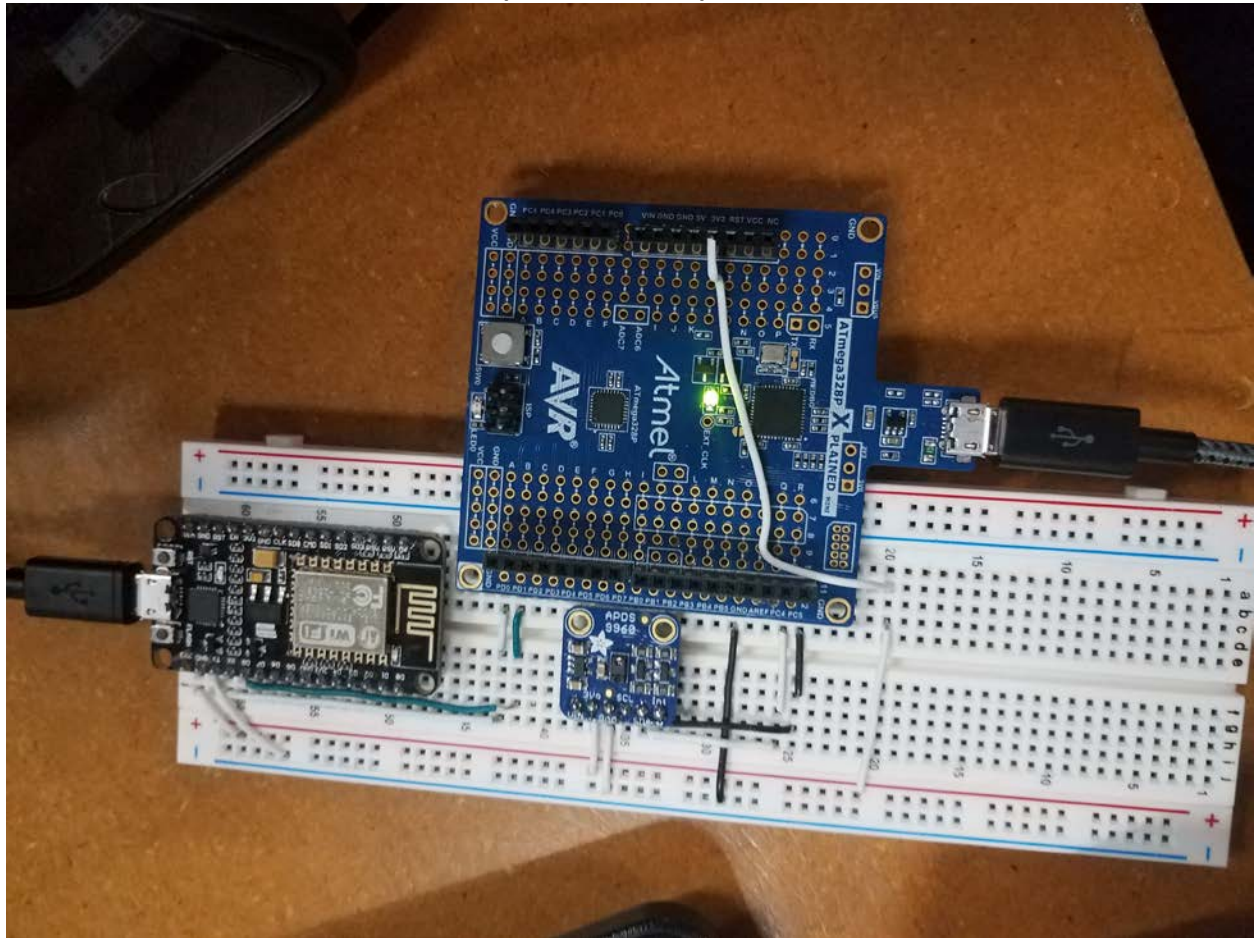
3. SCHEMATICS



4. SCREENSHOTS OF EACH TASK OUTPUT (ATEL STUDIO OUTPUT)



5. SCREENSHOT OF EACH DEMO (BOARD SETUP)



6. VIDEO LINKS OF EACH DEMO

<https://www.youtube.com/watch?v=B7fWGgiAWxw>

7. GITHUB LINK OF THIS DA

https://github.com/mendos1/submission_da

Student Academic Misconduct Policy

<http://studentconduct.unlv.edu/misconduct/policy.html>

"This assignment submission is my own, original work".

NAME OF THE STUDENT