

Func	100001	100011						000000
OP	000000	000000	001101	100011	101011	001111	000100	000000
指令	Addu	Subu	Ori	Lw	Sw	Lui	Beq	nop
Branch	0	0	0	0	0	0	1	0
MemtoReg[1:0]	00	00	00	01	xx	10	xx	xx
MemWrite	0	0	0	0	1	0	0	0
MemRead	0	0	0	1	0	0	0	0
ALUOp[1:0]	add(00)	sub(01)	or(10)	add(00)	add(00)	xx	xx	xx
ALUSrc	0	0	1	1	1	x	0	x
RegWrite	1	1	1	1	0	1	0	0
WriteRegDist	1	1	0	0	x	1	x	x