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CS220
Practical 2

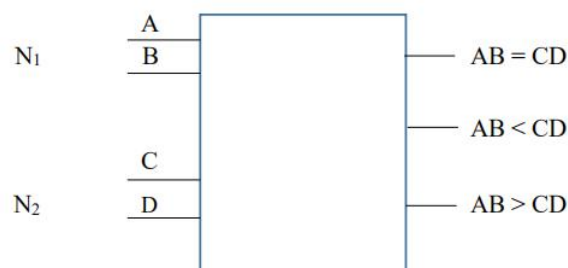
Two Bit Comparator Circuit

Design a circuit that takes as input two 2-bit numbers N₁ and N₂ to be compared and generates

three outputs:- one output for N₁=N₂, one for N₁< N₂ and one for N₁>N₂.

The outputs are logic 1 if the corresponding condition is true and logic 0 otherwise.

A block diagram of the circuit is given below:-

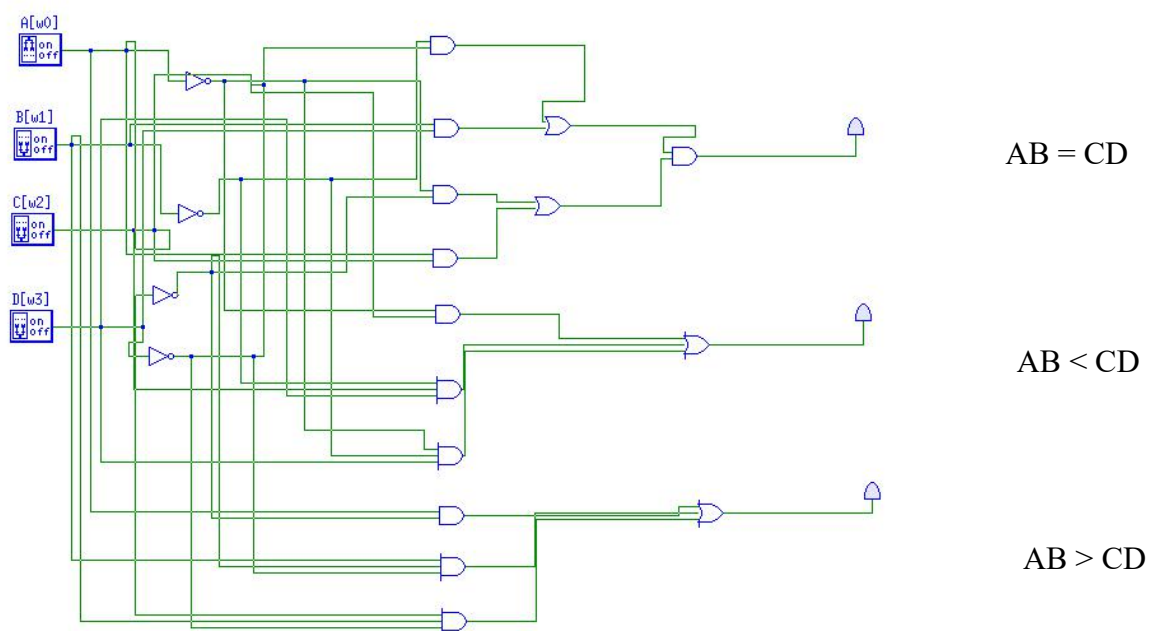


$$F_{eq}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}B\overline{C}D + ABCD + A\overline{B}C\overline{D}$$

$$= (\overline{B}\overline{D} + BD)(\overline{A}C + AC)$$

$$F_{lt}(A, B, C, D) = \overline{A}C + \overline{A}B\overline{D} + \overline{B}CD$$

$$F_{gt}(A, B, C, D) = \overline{A}C + \overline{B}C\overline{D} + AB\overline{D}$$



Switching Equations

Derive the minimal sum of products expressions for each function from the respective K-Maps above.

$$F_{eq}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}\overline{B}CD + ABCD + A\overline{B}C\overline{D}$$

$$F_{lt}(A, B, C, D) = \overline{A}C + \overline{A}\overline{B}D + \overline{B}CD$$

$$F_{gt}(A, B, C, D) = A\overline{C} + \overline{B}\overline{C}\overline{D} + AB\overline{D}$$

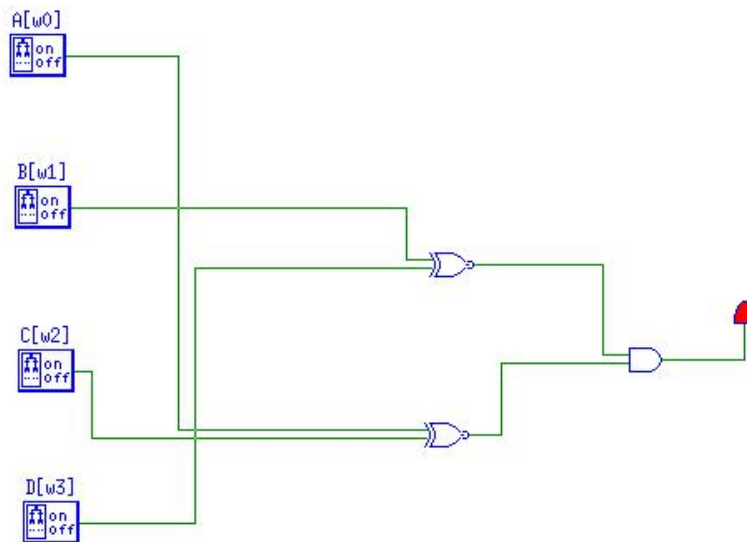
In the case of the Feq function, simplify the sum of products expression so that the function can be expressed in two levels using two 2-input XNOR gates and one 2-input AND gate instead. Show your workings.

(Note that $A'B + AB' = A \text{ EXOR } B$ and $A'B' + AB = A \text{ XNOR } B$)

$$F_{eq}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}\overline{B}CD + ABCD + A\overline{B}C\overline{D}$$

$$= (\overline{B}\overline{D} + BD)(\overline{A}C + AC)$$

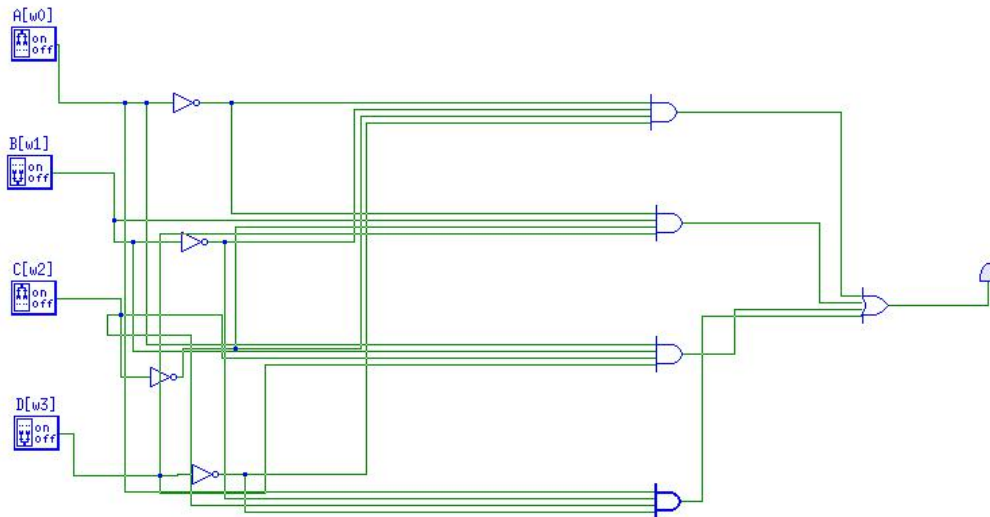
$$= (B \text{ XNOR } D)(A \text{ XNOR } C)$$



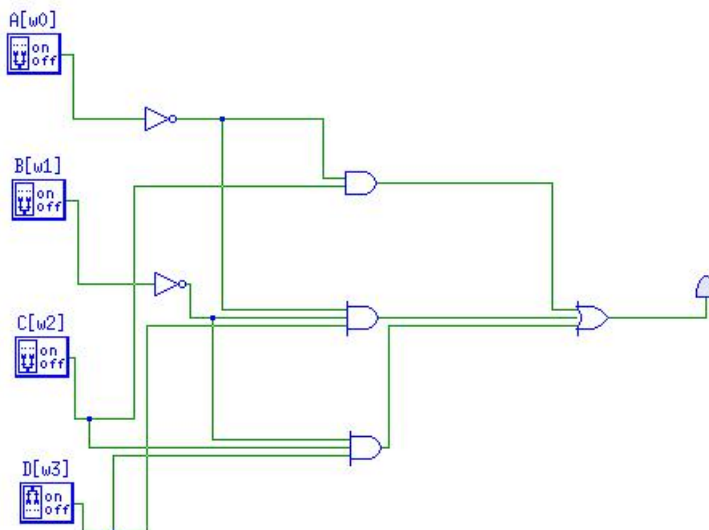
Logic Schematics

Create the comparator circuit from the switching equations you derived from the k-maps.

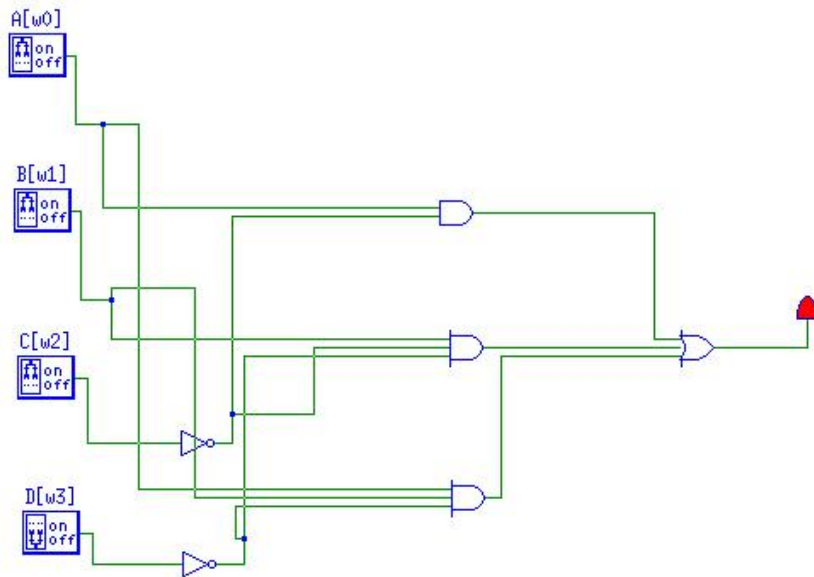
① $F_{eq}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}\overline{B}CD + A\overline{B}C\overline{D} + A\overline{B}CD$



② $F_{lt}(A, B, C, D) = \overline{A}C + \overline{A}\overline{B}D + \overline{B}CD$



③ $F_{gt}(A, B, C, D) = \overline{A}C + \overline{B}C\overline{D} + AB\overline{D}$



In the case of the equality function ($N1=N2$), implement both the sum-of-products form and the form which uses XNOR gates.

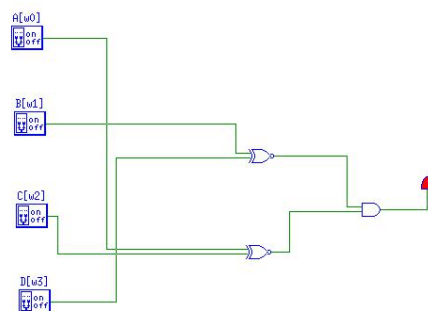
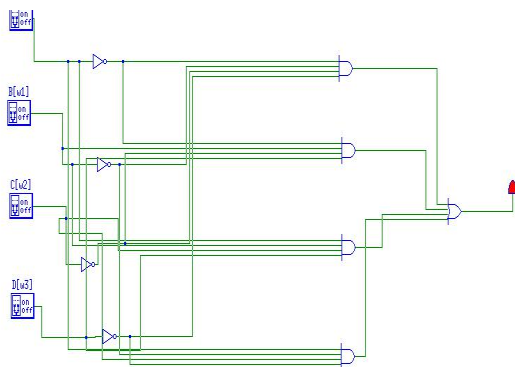
$$F_{eq}(A, B, C, D) = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}B\overline{C}D + ABCD + A\overline{B}C\overline{D}$$

$$= (\overline{B}\overline{D} + BD)(\overline{A}C + AC)$$

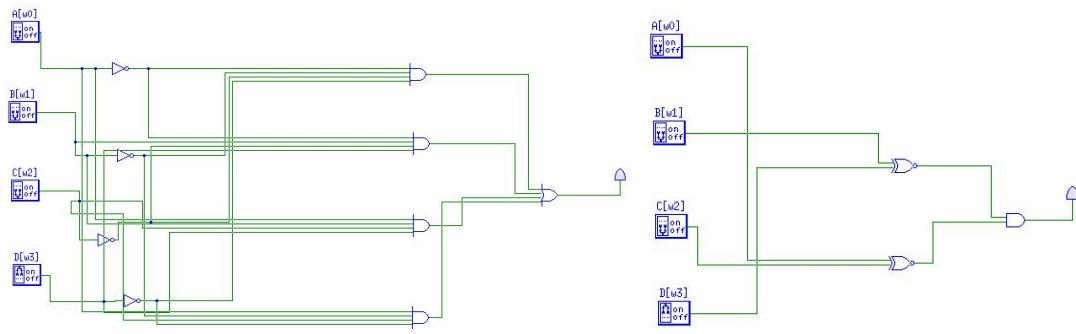
$$F_{eq}(A, B, C, D) = (B \text{ XNOR } D) (A \text{ XNOR } C)$$

Verify that both implementations of the equality function produce the same outputs for all combinations of inputs. (Hint: Use the same input switches for both implementations to simplify testing.)

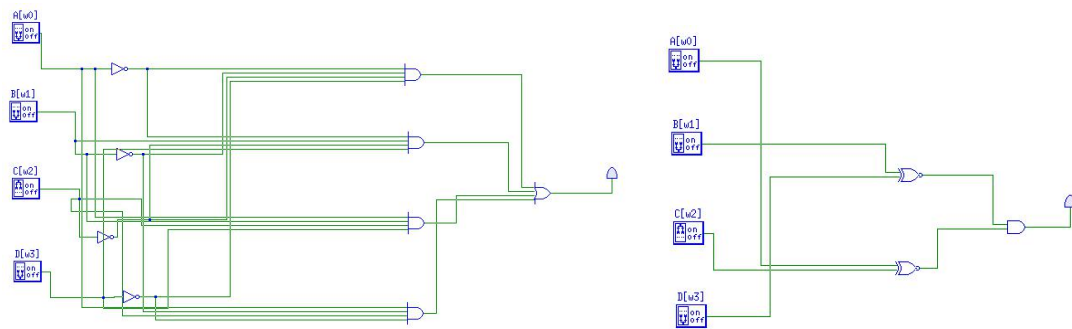
$$A = 0 \ B = 0 \ C = 0 \ D = 0$$



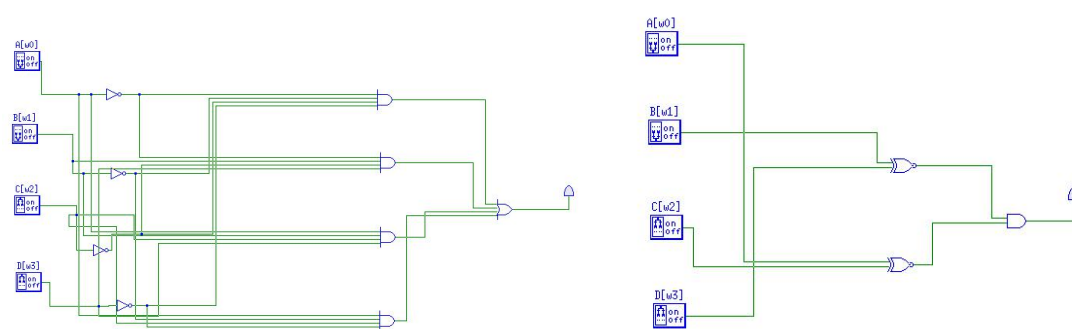
$$A = 0 \ B = 0 \ C = 0 \ D = 1$$



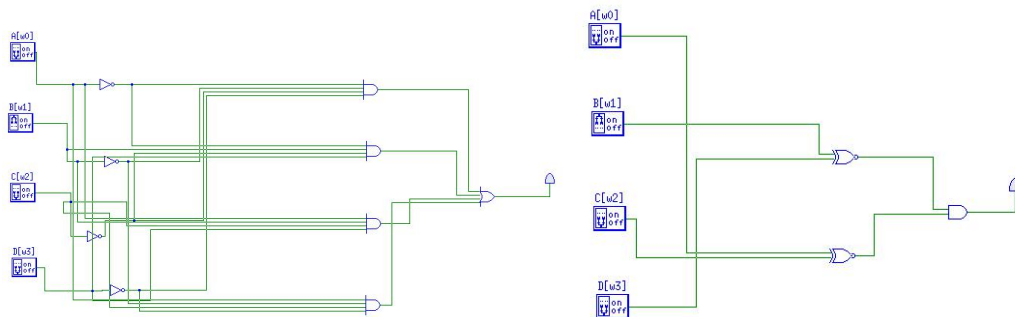
$$A = 0 \ B = 0 \ C = 1 \ D = 0$$



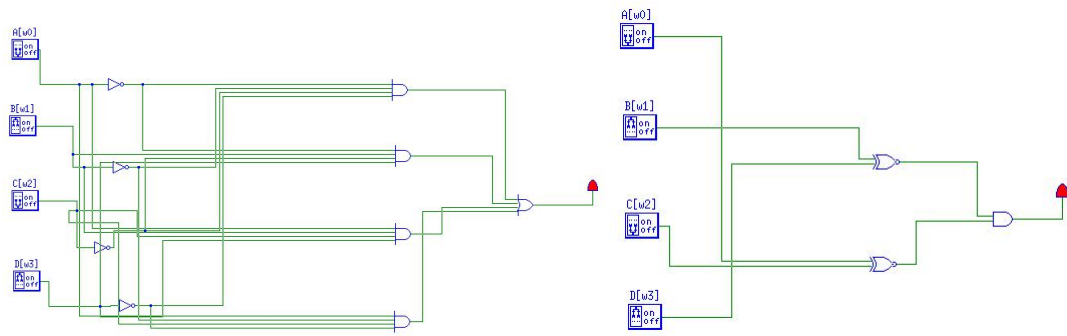
$$A = 0 \ B = 0 \ C = 1 \ D = 1$$



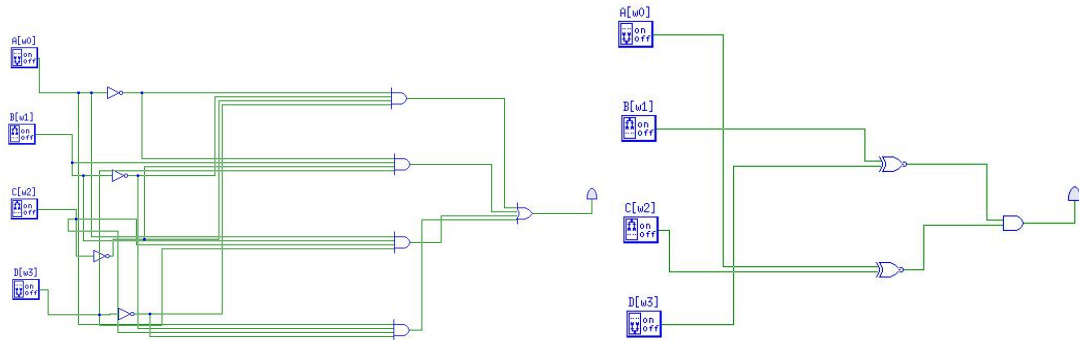
$$A = 0 \ B = 1 \ C = 0 \ D = 0$$



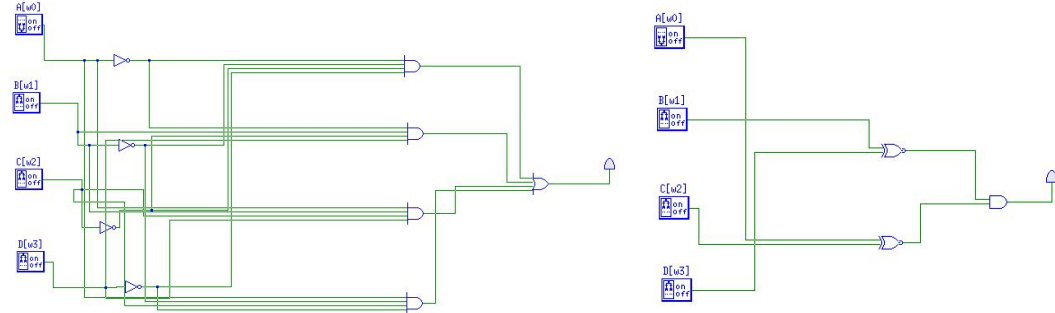
$$A = 0 \ B = 1 \ C = 0 \ D = 1$$



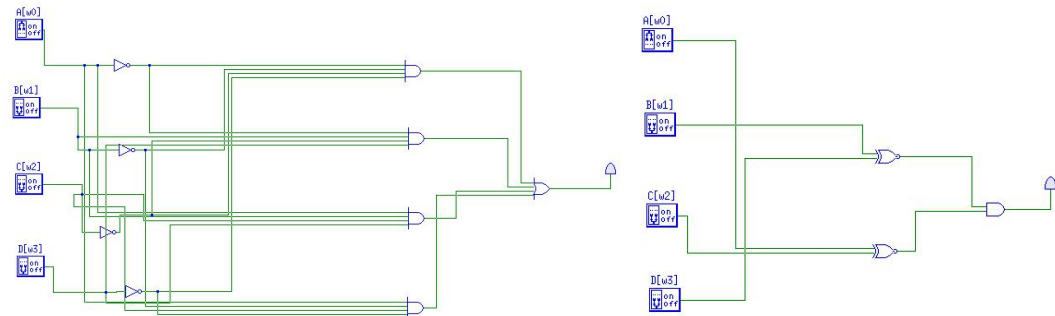
$$A = 0 \ B = 1 \ C = 1 \ D = 0$$



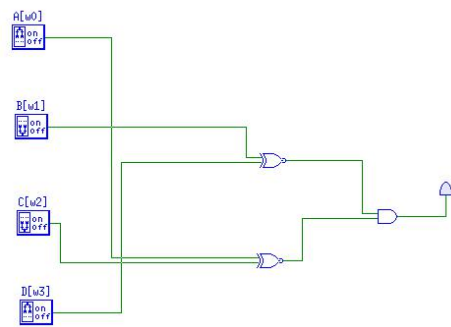
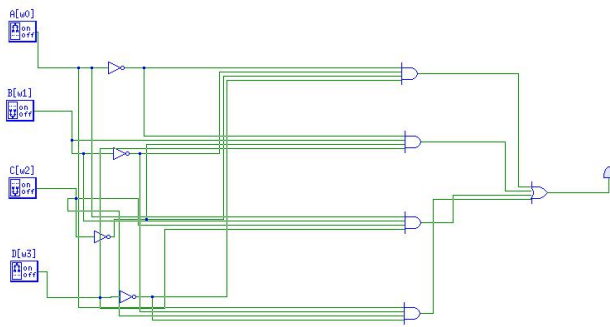
$$A = 0 \ B = 1 \ C = 1 \ D = 1$$



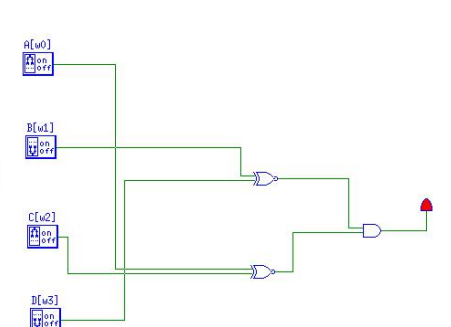
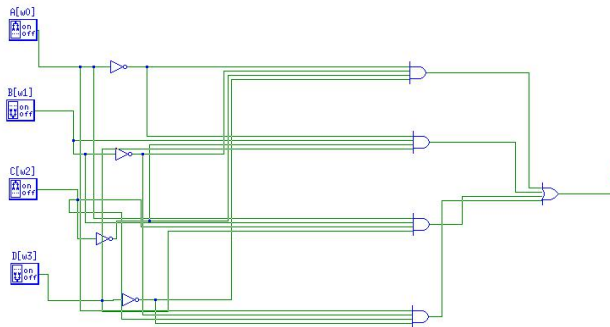
$$A = 1 \ B = 0 \ C = 0 \ D = 0$$



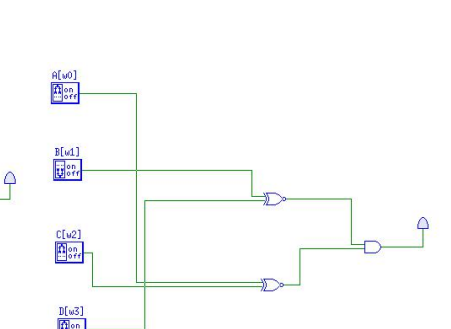
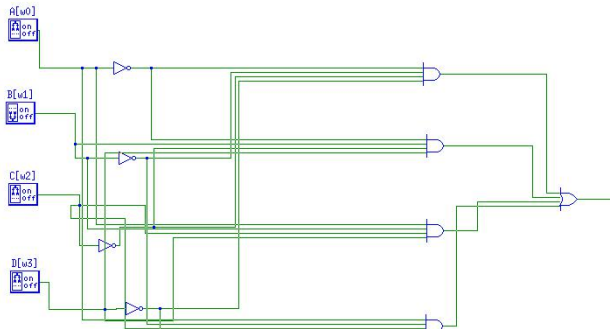
$$A = 1 \ B = 0 \ C = 0 \ D = 1$$



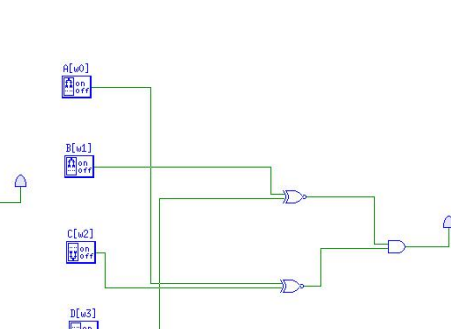
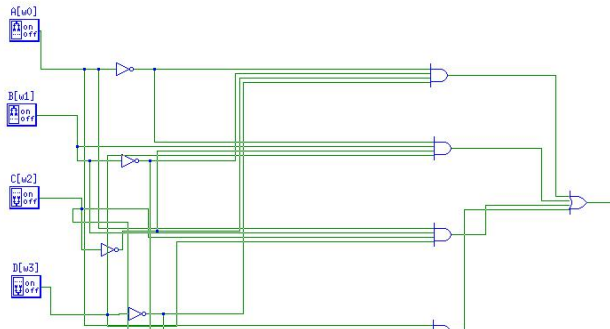
$$A = 1 \ B = 0 \ C = 1 \ D = 0$$



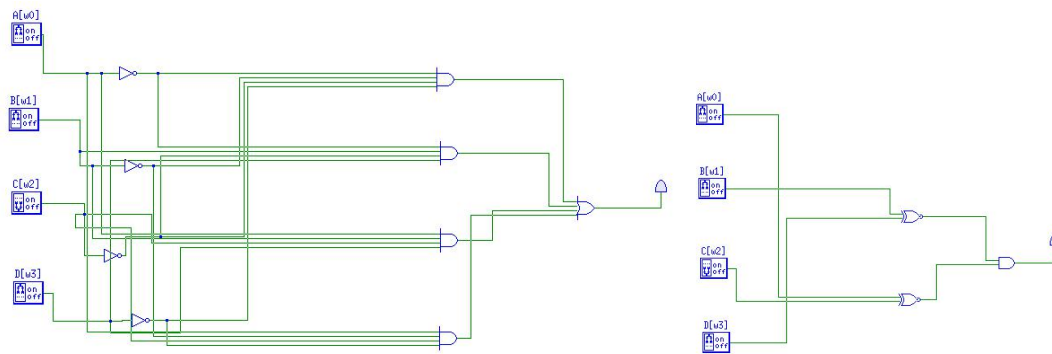
$$A = 1 \ B = 0 \ C = 1 \ D = 1$$



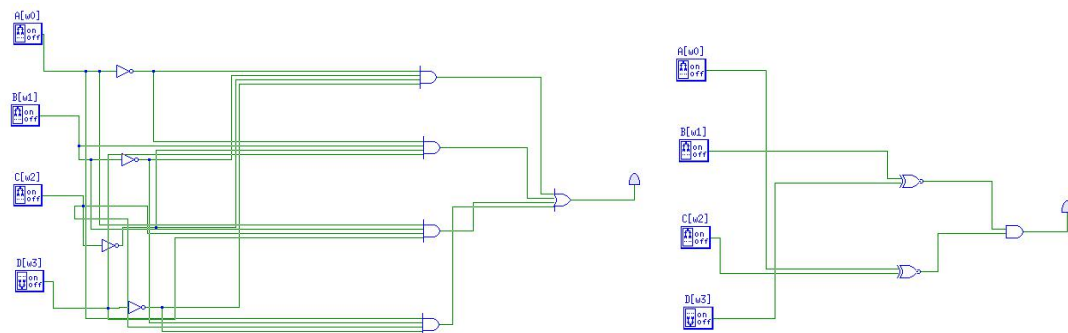
$$A = 1 \ B = 1 \ C = 0 \ D = 0$$



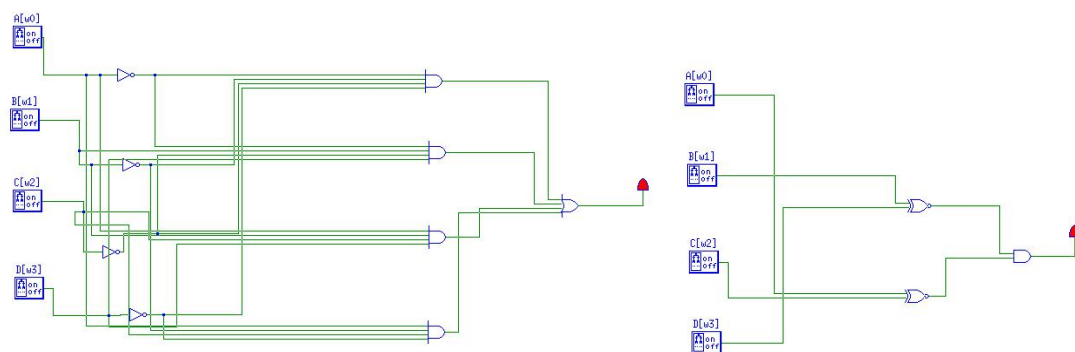
$A = 1 \ B = 1 \ C = 0 \ D = 1$



$A = 1 \ B = 1 \ C = 1 \ D = 0$



$A = 1 \ B = 1 \ C = 1 \ D = 1$



So that both implementations of the equality function produce the same outputs for all combinations of inputs.