Entity: Combined56

- **File**: combined56.vhd

Diagram
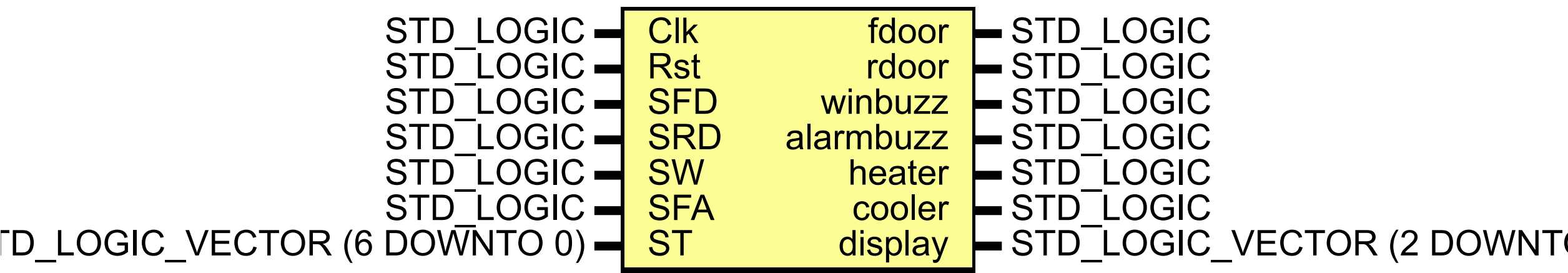
Ports

Port name	Direction	Type	Description
Clk	in	STD_LOGIC	
Rst	in	STD_LOGIC	
SFD	in	STD_LOGIC	
SRD	in	STD_LOGIC	
SW	in	STD_LOGIC	
SFA	in	STD_LOGIC	
ST	in	STD_LOGIC_VECTOR (6 DOWNTO 0)	
fdoor	out	STD_LOGIC	
rdoor	out	STD_LOGIC	
winbuzz	out	STD_LOGIC	
alarmbuzz	out	STD_LOGIC	
heater	out	STD_LOGIC	
cooler	out	STD_LOGIC	
display	out	STD_LOGIC_VECTOR (2 DOWNTO 0)	

Signals

Name	Type	Description
current_state	MOORE_FSM	
next_state	MOORE_FSM	

Types

Name	Type	Description
MOORE_FSM	(Zero, One, Two, Three, Four, Five, six)	

Processes

- unnamed: (Clk)

- unnamed: (current_state, SFD, SRD, SFA, SW, ST)

- unnamed: (current_state)

State machines