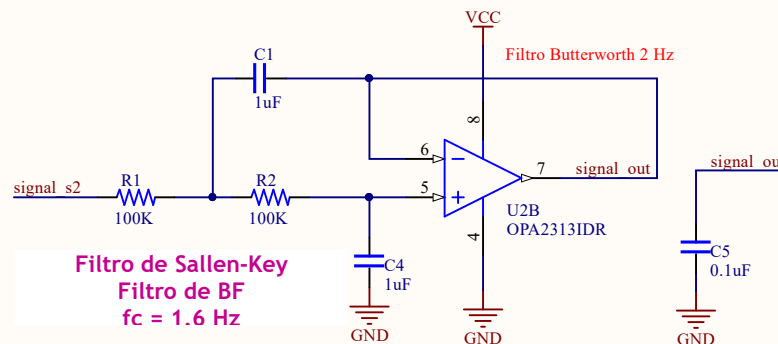
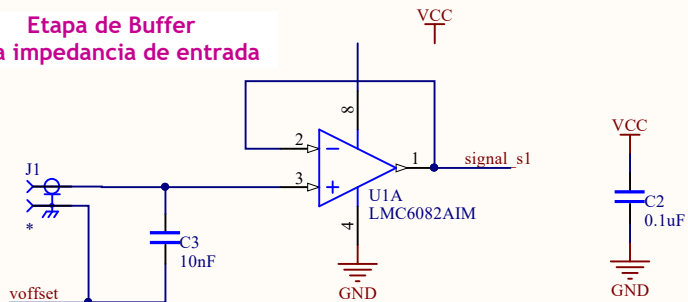
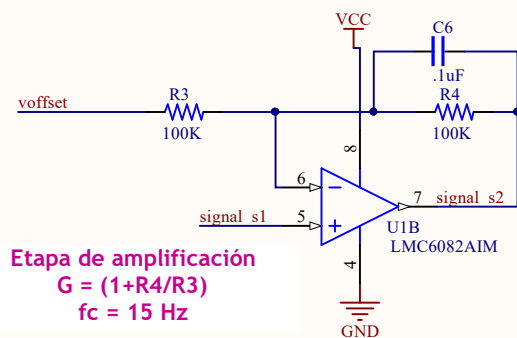


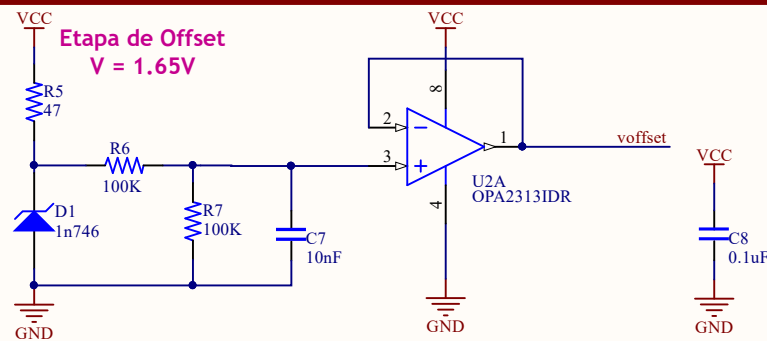
Etapa de Buffer Alta impedancia de entrada



Filtro de Sallen-Key Filtro de BF $f_c = 1.6 \text{ Hz}$

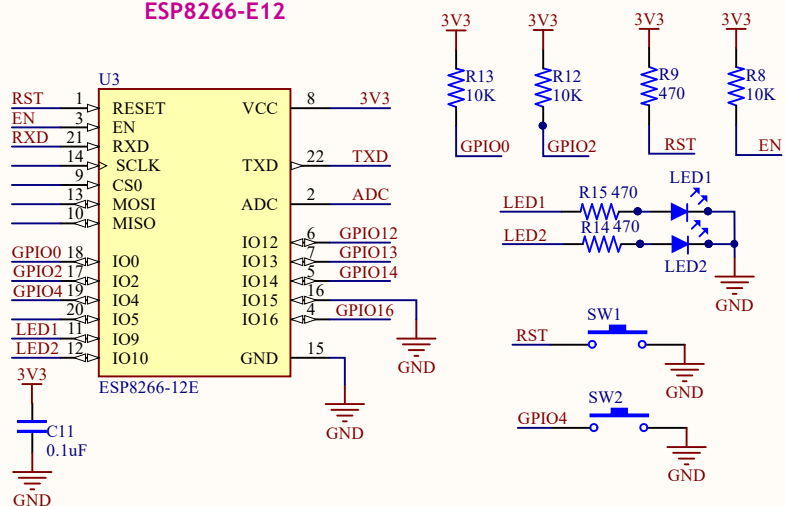


Etapa de amplificación $G = (1 + R4/R3)$ $f_c = 15 \text{ Hz}$

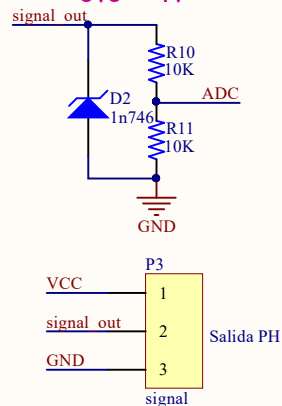


Etapa de Offset $V = 1.65 \text{ V}$

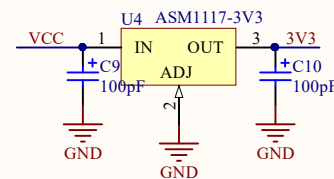
ESP8266-E12



Divisor resistivo ADC $3\text{V3} \rightarrow 1\text{V}$



Regulador de tensión $5\text{V} \rightarrow 3\text{V3}$



Programador

