

4KB ram = 4 chip 1KB

Address lines =  $2^{10} = 1KB$  total 10 address line ( $A_0 - A_9$ )

Data line = 8 ( $D_0 - D_7$ )

Control Signal = Memory Read  
Memory Write

RAM 1 → 8000H

EPROM-01

0000 0000 0000 0000  
0001 1111 1111 1111

EPROM-02

0000 0000 0000 0000  
0001 1111 1111 1111

RAM-01

1000 0000 0000 0000  
1000 0011 1111 1111

RAM-02

1000 0100 0000 0000  
1000 0111 1111 1111

RAM-03

1000 1000 0000 0000  
1000 1011 1111 1111

RAM-04

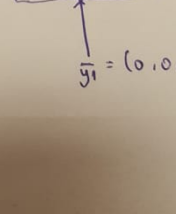
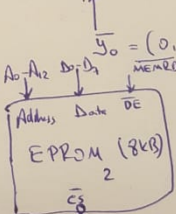
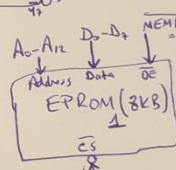
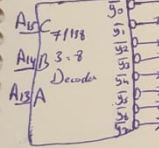
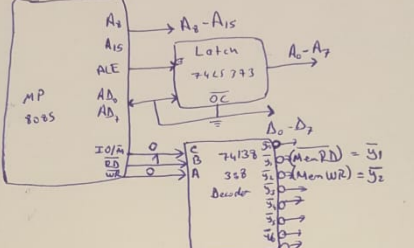
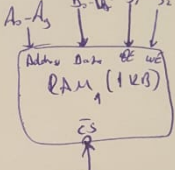
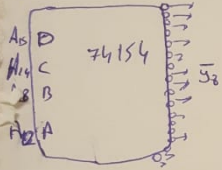
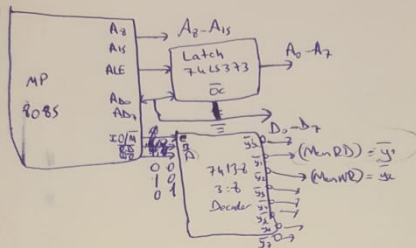
1000 1100 0000 0000  
1000 1111 1111 1111

EPROM → 16KB → 2 chips (8KB)

Address lines = 8KB →  $2^3 = 8$  address line ( $A_0 - A_2$ )

Data lines = 8 ( $D_0 - D_7$ )

Control signal = Memory Read



1000 1111 1111 1111  
1000 1111 1111 1111  
1000 1111 1111 1111  
1000 1111 1111 1111