4KB ram = 4 chip 1KB

Address lines = 2^{10} = 1KB total : 10 address line $(A_0 - A_9)$

Data line = 8 $(D_0 - D_7)$

Control Signal = Memory Read
Memory Write

RAM 1 → 8000H

EPROM-01

EPROM-02

RAM-01

RAM-02

RAM-03

RAM-04

1KB

8K

64KB / 64 = 1KB

EPROM → 16 KB → 2 time (8KB)

Address lines = 8KB → 2^{13} = Address line $(A_0 - A_{12})$

Data lines = 8 $(D_0 - D_7)$

Control signal = Memory Read

MP 8085

Latch 74LS373

74138 3:8 Decoder

74154

RAM 1 (1KB)

RAM 2

RAM 3

RAM 4

EPROM (8KB) 1

EPROM (8KB) 2

$\overline{y_0}$ = (0,0,0)

$\overline{y_1}$ = (0,0,1)