

```

|----- MODULE appex4_1_3 -----|
|
| EXTENDS Integers, TLC
|
|-----|
|
| - wfNext
|
| --algorithm test {
| variables x = 1, y = 12;
| {
| l1: assert x = 1  $\wedge$  y = 12;
| x := 2 * y;
| l2: assert x = 1  $\wedge$  y = 24;
|
| l3: print  $\langle x, y \rangle$ ;
| }
| }
|
| BEGIN TRANSLATION
|
| VARIABLES x, y, pc
|
| vars  $\triangleq \langle x, y, pc \rangle$ 
|
| Init  $\triangleq$  Global variables
|            $\wedge x = 1$ 
|            $\wedge y = 12$ 
|            $\wedge pc = \text{"l1"}$ 
|
| l1  $\triangleq$   $\wedge pc = \text{"l1"}$ 
|            $\wedge \text{Assert}(x = 1 \wedge y = 12, \text{"Failure of assertion at line 11, column 4."})$ 
|            $\wedge x' = 2 * y$ 
|            $\wedge pc' = \text{"l2"}$ 
|            $\wedge y' = y$ 
|
| l2  $\triangleq$   $\wedge pc = \text{"l2"}$ 
|            $\wedge \text{Assert}(x = 1 \wedge y = 24, \text{"Failure of assertion at line 13, column 4."})$ 
|            $\wedge pc' = \text{"l3"}$ 
|            $\wedge \text{UNCHANGED } \langle x, y \rangle$ 
|
| l3  $\triangleq$   $\wedge pc = \text{"l3"}$ 
|            $\wedge \text{PrintT}(\langle x, y \rangle)$ 
|            $\wedge pc' = \text{"Done"}$ 
|            $\wedge \text{UNCHANGED } \langle x, y \rangle$ 
|
| Allow infinite stuttering to prevent deadlock on termination.
| Terminating  $\triangleq pc = \text{"Done"} \wedge \text{UNCHANGED } vars$ 
|
| Next  $\triangleq l1 \vee l2 \vee l3$ 
|            $\vee \text{Terminating}$ 

```

$$Spec \triangleq Init \wedge \Box[Next]_{vars}$$

$$Termination \triangleq \Diamond(pc = \text{"Done"})$$

END TRANSLATION

$$MAX \triangleq 32768 \quad \text{16 bits}$$

$$D \triangleq 0 \dots 32768$$

$$x \leq 32760$$

$$DD(X) \triangleq (X \in D)$$

$$Safety_absence \triangleq DD(x) \wedge DD(y)$$

$$Inv \triangleq$$

$$\wedge pc = \text{"l1"} \Rightarrow x = 1 \wedge y = 12$$

$$\wedge pc = \text{"l2"} \Rightarrow x = 1 \wedge y = 24$$