



Modelling Software-based Systems Tutorial

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General Summary

1 Invariant versus theorem in Event-B

2 Designing a C program for $\lambda x.x \times x$

Current Summary

- 1 Invariant versus theorem in Event-B
- **2** Designing a C program for $\lambda x.x \times x$

```
VARIABLES x
INVARIANTS
 @inv1 \ x \in \mathbb{Z}
 @the property \ x < 0
 EVENT INITIALISATION
    then
   @act1 \ x := -1
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 EVENT event1
    where
   @qrd1 x > 0
    then
   @act1 \ x := x + 1
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 - $x \le 0, x \ge 0, x = 0, x' = x + 1 \vdash 1 < 0!$

```
VARIABLES x
INVARIANTS
 @inv1 x \in \mathbb{Z}
 @inv2 \ x = -1
 theorem @safety1 x < 0
 EVENT INITIALISATION
    then
   @act1 \ x := -1
  end
 EVENT event1
    where
   @grd1 \ x \ge 0
    then
   @act1 \ x := x + 1
  end
 EVENT event2
    then
   @act1 \ x := x
end
```

- $x \le 0$ is always true.
- $x = -1 \wedge BA(event1)(x, x') \Rightarrow x' = -1$ is correct
- x < 0 is a theorem
- x = -1 is an inductive invariant.

Current Summary

- 1 Invariant versus theorem in Event-B
- **2** Designing a C program for $\lambda x.x \times x$

Designing a C program for $\lambda x.x \times x$

Computing $\lambda x.x \times x$ with only addition

The problem is to derive a C program which is computing the function $\lambda x.x \times x$ using only addition.

```
#ifndef _A_H
#define _A_H
#include <limits.h>
/*@ axiomatic auxmath {
  @ axiom rule1: \ for all int n; n > 0 \Longrightarrow n*n \Longrightarrow (n-1)*(n-1)+2*n-1
  @ } */
/*0 requires 0 \le x;
      requires x \le INT_-MAX;
      requires x*x \le INT\_MAX:
  assigns \ nothing:
      ensures \ result == x*x;
*/
int power2(int x);
#endif
```

Context for computing $\lambda x.x \times x$

```
CONTEXT power20
 CONSTANTS n0 v w s
 AXIOMS
  @axm1\ n0 \in \mathbb{N} // precondition
  @axm2 \ w \in \mathbb{N} \to \mathbb{Z}
  @axm3\ w(0) = 0
  @axm4 \ \forall \ n. \ n \in \mathbb{N} \Rightarrow w(n+1) = w(n) + 2
  @axm5 v \in \mathbb{N} \to \mathbb{Z}
  @axm6\ v(0) = 0
  @axm7 \ \forall n. n \in \mathbb{N} \Rightarrow v(n+1) = v(n) + w(n) + 1
  @axm8 \ s \in \mathbb{N} \to \mathbb{N} \ \land \ (\forall i. i \in \mathbb{N} \Rightarrow s(i) = i+1)
  @axm9 \ \forall A. A \subseteq \mathbb{N} \ \land \ 0 \in A \ \land \ s[A] \subseteq A \ \Rightarrow \ \mathbb{N} \subseteq A
  theorem @axm10 \forall n.n \in \mathbb{N} \Rightarrow w(n) = 2 * n
  theorem @axm11 \forall n.n \in \mathbb{N} \Rightarrow v(n) = n * n
  @axm12\ n0 > 3
end
```

```
\begin{array}{lll} \text{MACHINE} & power21 & \text{SEES} & power20 \\ \\ & \text{VARIABLES} & r \ ok \ n \\ & \text{INVARIANTS} \\ & @inv1 \ r \ \in \ \mathbb{Z} \\ & @inv2 \ n \ \in \ \mathbb{Z} \\ & @inv2 \ n \ \in \ \mathbb{Z} \\ & @inv3 \ ok \ \in \ BOOL \\ & @inv4 \ ok = TRUE \ \Rightarrow \ r = n0 \times n0 \\ & @inv5 \ n = n0 \end{array}
```

- Defining variables and invariant
- r is the variable for the result.
- ok is the boolean variable used for expressing the process termination.
- *n* is the variable containing the input of the process.

$\begin{array}{l} \text{EVENT} \quad INITIALISATION \\ \text{then} \\ @act1 \ r \ : \in \ \mathbb{Z} \\ @act2 \ ok \ := \ FALSE \\ @act3 \ n \ := \ n0 \\ \text{end} \\ \text{EVENT} \quad final \\ \text{where} \\ @grd1 \ ok \ = \ FALSE \\ \text{then} \\ @act1 \ r \ := \ v(n) \\ @act2 \ ok \ := \ TRUE \\ \text{end} \end{array}$

- INITIALISATION is setting variables especially n=n0
- final is observed and gets the value v(n) which is sound since $v(n) = n \times n$.
- ok controls the obsedrvation of the event final at most one time.

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- iteration is anticipating an hidden iteration.

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Machine power22 for stating the computing process

```
\begin{array}{llll} \text{MACHINE} & power22 & \text{REFINES} & power21 & \text{SEES} & power20 \\ \text{VARIABLES} & r vv \ k \ ww \ ok \ n & \\ \text{INVARIANTS} & \\ @inv1 \ vv \ \in \ \mathbb{N} \ \rightarrow \ \mathbb{Z} & \\ @inv2 \ ww \ \in \ \mathbb{N} \ \rightarrow \ \mathbb{Z} & \\ @inv2 \ ww \ \in \ \mathbb{N} \ \rightarrow \ \mathbb{Z} & \\ @inv2 \ ww \ \in \ \mathbb{N} \ \rightarrow \ \mathbb{Z} & \\ @inv2 \ ww \ \in \ \mathbb{N} \ \rightarrow \ \mathbb{Z} & \\ @inv2 \ k \ \in \ \mathbb{N} & \\ @inv4 \ \forall i. \ i \ \in \ dom(vv) \ \Rightarrow \ vv(i) = v(i) & \\ @inv5 \ \forall i. \ i \ \in \ dom(ww) \ \Rightarrow \ ww(i) = w(i) & \\ @inv5 \ \forall i. \ i \ \in \ dom(vv) \ \Rightarrow \ vv(i) = i*i & \\ @inv8 \ k \ \leq \ n & \\ theorem \ @safe2 \ \forall i. \ i \ \in \ dom(ww) \ \Rightarrow \ ww(i) = 2*i & \\ @inv11 \ k \ < \ n \ \Rightarrow \ ok = FALSE & \\ \end{array}
```

- Two new variables vv and ww are introduced for storing the two sequences v and w by iterating over k
- Condition of termination is that $n \in dom(vv)$
- vv(i)=v(i) and ww(i)=w(i) are expressing the relationship between computed values and mathematically defined values of the two sequences.

Machine power22 for stating the computing process

$\begin{array}{ll} \text{EVENT} & INITIALISATION \\ \text{then} & @act1 \ r \ : \in \ \mathbb{Z} \\ @act2 \ vv \ : = \ \{\ 0 \mapsto 0\} \\ @act3 \ ww \ : = \ \{\ 0 \mapsto 0\} \\ @act4 \ k \ : = \ 0 \\ @act5 \ ok \ : = \ FALSE \\ @act6 \ n \ : = \ n0 \\ \text{end} \end{array}$

- INITIALISATION is setting variables especially ww and vv
- Sequences v and w are used for intialisation.

Machine power22 for stating the computing process

```
EVENT final REFINES final
   where
  @grd1 \ n \in dom(vv)
  @qrd2 \ ok = FALSE
   then
  @act1 \ r := vv(n)
  @act2\ ok := TRUE
end
convergent EVENT step - computing
 REFINES iteration
   where
  @grd1 \ n \notin dom(vv)
  @ard2\ ok = FALSE
   then
  @act1\ vv(k+1) := vv(k) + ww(k) + 1
  @act2\ k := k+1
  @act3\ ww(k+1) := ww(k) + 2
end
VARIANT n-k
```

- the event final is controlled by the condition n ∈ dom(vv) meaning that we have finally reached the computing goal.
- SIM proof obligations are generated.
- the event step-computing is refining iteration and when it observed, the variant n-k is decreasing.
- it refines iteration

Machine power23 for getting an algorithmic process

```
MACHINE power23
REFINES power22
SEES power20
```

VARIABLES r vv k cv ww cw ok n

INVARIANTS

```
\begin{array}{l} @inv1 \ cv \ \in \ \mathbb{Z} \\ @inv2 \ cv = vv(k) \\ @inv3 \ cw \ \in \ \mathbb{Z} \\ @inv4 \ cw = ww(k) \\ theorem \ @inv5 \ k \ \in \ 0..n \\ theorem \ @inv6 \ cw = 2 * k \\ theorem \ @inv7 \ cv = k * k \\ theorem \ @inv8 \ 4 * cv = cw * cw \end{array}
```

- Two new variables are introduced for storing really useful data namey the last computed values of the two sequences.
- Obviously, cv = vv(k) and cw = ww(k)
- Previous properties of abstrcat variables are safety properties which are no more to be reproved, thanks to refignement.
- We can get extra properties that are relating the variables as $4 \times cv = cw \times cw$.

Machine power23 for getting an algorithmic process

 Initialisation of new variables according to the invariant.

Machine power23 for getting an algorithmic process

```
EVENT final REFINES final
    where
   @ard1 \ k = n
    then
   @act1 \ r := cv
   @act2\ ok := TRUE
 end
 convergent EVENT step - prealgo
  REFINES step-computing
    where
   @qrd1 k < n
    then
   @act1 \ vv(k+1) := vv(k) + ww(k) + 1
   @act2\ k := k+1
   @act3\ cv := cv + cw + 1
   @act4\ ww(k+1) := ww(k) + 2
   @act5\ cw\ :=\ cw+2
   end
    VARIANT n-k
```

- The two events SIMulate the abstrcat events.
- However, the guards are strengthened and are made closer to an implmentation : k < n implies $n \notin dom(vv)$ and k = n implies that $n \in dom(vv)$.

Machine power24 for getting an algorithmic machine

 $\begin{array}{cccc} {\sf MACHINE} & power24 & {\sf REFINES} & power23 \\ {\sf SEES} & power20 \end{array}$

VARIABLES rkcvcwokn

INVARIANTS

theorem $@th1 \ cw = 2 * k$ theorem $@th2 \ cv = k * k$ theorem $@inv1th3 \ 4 * cv = cw * cw$

- The two variables vv and ww are now hidden and they disappear from the machine.
- They are playing the role of model variables as ghost variables.
- Invariants and safety properties are preserved through refinement.

Machine power24 for getting an algorithmic machine

```
\begin{array}{l} \text{EVENT} \quad INITIALISATION \\ \text{then} \\ @act1 \ r : \in \ \mathbb{Z} \\ @act5 \ k \ := \ 0 \\ @act8 \ cv \ := \ 0 \\ @act10 \ cw \ := \ 0 \\ @act11 \ ok \ := \ FALSE \\ @act12 \ n \ := \ n0 \\ \text{end} \end{array}
```

 INITILISATION is the same event without vv and ww.

Machine power24 for getting an algorithmic machine

```
EVENT final REFINES final
   where
  @qrd1 \ k = n
   then
  @act1 \ r := cv
  @act2 ok := TRUE
end
convergent EVENT step
 REFINES step-prealgo
   where
  @ard1 \ k < n
   then
  @act4\ k := k+1
  @act5\ cv := cv + cw + 1
  @act7\ cw\ :=\ cw+2
end
```

 Assignments of vv and ww are removed.

Translating the machine power24 to an algorithm

```
\begin{array}{llll} begin & & int \ r,k & := \ 0, \ cv & := \ 0, \ cw & := \ 0, \ ok & := \ FALSE, n \ := \ n0; \\ while \ k & < \ n \ \{ & (\ k,cv,cv) \ := \ (k+1,cv+cw+1,cw \ := \ cw+2); \\ \ \}; & & & & \\ r & := \ cv; & & & \\ ok & := \ TRUE & & \\ end & & & & \\ \end{array}
```

Translating the machine power24 to an algorithm

```
#include <limits.h>
#include "ppower2.h"
int power2(int x)
{ int r, k, cv, cw, or, ok, ocv, ocw;
  r=0; k=0; cv=0; cw=0; or=0; ok=k; ocv=cv; ocw=cw;
       /*@ loop invariant cv = k*k;
          @ loop invariant k \le x;
          @ loop invariant cw = 2*k;
          @ loop invariant 4*cv = cw*cw;
          @ loop assigns k, cv, cw, or, ok, ocv, ocw;
           @ loop variant x-k:
  while (k<x)
           ok=k:ocv=cv:ocw=cw:
           k=ok+1:
           cv = ocv + ocw + 1:
           cw=ocw+2:
  r=cv:
  return(r);
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```