


**Design of cmos bandgap voltage reference circuit**

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# Design of cmos bandgap voltage reference circuit

Design of bandgap core and startup circuits for all cmos bandgap voltage reference.

See PDFVolume 114, August 2021, 105157 Get rights and content Hongwei Shen received the degree of electronics from Huazhong University of Science and Technology in 2003, and Ph.D. degree of microelectronics from the Institute of Microelectronics, Chinese Academy of Science in 2008. He is mainly engaged in the design of circuits and radio frequency identification systems. He currently works at the Beijing Smarchip Microelectronics Technology Company Limited. He has published several technical documents and over 20+ patents in the analog integrated circuits area.(e-mail:(email protected)) Yubo Wang received the Master of Electronics from Harbin Institute of Technology in 1997, is mainly engaged in the design of sensors and circuits and security systems. He currently works at the Beijing Smarchip Microelectronics Technology Company Limited. He has published 39 technical documents and over 27 patents in the context of analog integrated circuits. Xiaoke Tang, received B.S. from Northern Jiao tong University, China, in 2000. He has engaged in internet smart network of chips things and research and development of the home CPU. Its research interests include home CPU, vector communication, low power design, security algorithm. He has published 20 technical documents and over 20 patents in the context of analog integrated circuits. Dejian Li received the master's degree from Tsinghua University in 2002. It focuses on the design and verification of ASIC on a large scale, especially in the industrial control sectors. He currently works at the Beijing Smarchip Microelectronics Technology Company Limited. He has published 10+ technical documents and 10+ patents in related areas. Xi Feng received the degree of science and electronic technology from the University of Tsinghua in 2004, and the master of microelectronics from the University of Tsinghua in 2006. He is mainly engaged in low power design SoC. He currently works at Beijing Smarchip Microelectronics Technology Company Limited. He has published several technical documents and over 4 patents in the digital integrated circuits area. An integrated circuit of high frequency CMOS pulse width modulation. Sahin, F. KoçerEngineering2016 IEEE International conference on electron devices and solid-state circuits (EDSSC)2016In this work, an integrated circuit of modulation of high-frequency pulse width (PWM) is presented, designed and implemented in a commercial process of 0.35 μm CMOS. Based on the natural sampling method,... 1-10 15 REFERENCESSORT BYRelevanceMost Influenced PapersRecencyDesign of a reference for high-performance band-gap A high-performance CMOS band reference (BGR) is designed in this document, where a three-stage operating amplifier is adoptedGet a high PSRR and only the first order temperature compensation technology is used to obtain a low temperature coefficient. Expansion a best bandgap reference with a high refusal of power a improvement improvement The reference with the refusal to power supply (PSR) is presented, which feeds the power noise directly in the bandband circuit feedback cycle that could help to suppress power noise. ExpandView 1 Excert, references background background

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