



Proof of Necessary Work: Succinct State Verification with Fairness Guarantees

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Abstract. Blockchain-based payment systems utilize an append-only log of transactions whose correctness can be verified by any observer. Classically, verification costs grow linearly in either the number of transactions or blocks in the blockchain (often both). Incrementally Verifiable Computation (IVC) can be used to enable constant-time verification, but generating the necessary proofs is expensive. We introduce the notion of Proof of Necessary Work (PoNW), in which proof generation is an integral part of the proof-of-work used in Nakamoto consensus, producing proofs using energy that would otherwise be wasted. We implement and benchmark a prototype of our system, enabling stateless clients to verify the entire blockchain history in about 40 milliseconds.

Keywords: proof-of-work · zero-knowledge proofs · consensus algorithms

1 Introduction

Balancing throughput with decentralization is a major challenge in modern cryptocurrencies. Current systems such as Bitcoin require participants to process the entire system history to verify that the current state (the most recent block in the chain) is correct. Despite strict limits on blockchain growth which cap total system throughput, verification costs are prohibitive for many clients. Joining the system requires downloading and verifying over 450 GB of blockchain history (as of 2022) which takes days on a typical laptop. In practice, most clients don't perform independent verification and rely on a trusted third party instead.

Succinct blockchains aim to support efficient verification of the system's entire history by any participant without trusting any third parties. Participants only need to obtain some fixed public parameters from a trusted source (e.g. the genesis block and the system's rules). Participants can then join the system at any time and receive a succinct validity proof for the most recent block in the system using minimal bandwidth and time. These proofs demonstrate both that there exists a sequence of valid transactions from the genesis state S_0 to the state committed in the current block, and that the block's *branch* (the sequence of predecessor blocks) is of quality q according to the consensus protocol. In this work we focus on aggregate proof-of-work (PoW) difficulty as the measure of

branch quality, as used in Bitcoin consensus. Currently, systems such as Bitcoin require $O(t + h)$ work to completely verify a branch containing t transactions and h blocks. Succinct proofs enable optimal asymptotic performance of $O(1)$ verification costs for a client joining the system at any point in its history.

A key challenge for succinct blockchains is incentivizing the expensive costs of computing a validity proof for each block. Meanwhile, Bitcoin employs proof-of-work (PoW), which provides system security by verifying energy consumption. This energy, while necessary for the consensus protocol, is not used for anything else and hence is often described as ‘wasted.’ We propose a new approach to useful PoW in which the work aids in the verification of the system itself. We denote this as *proof of necessary work* (PoNW) and show how it can be used within a succinct blockchain architecture as a suitable PoW puzzle.

A synergistic benefit is directly incentivizing hardware acceleration of zero-knowledge proofs. This is relevant for many distributed payment systems in which proof generation time is a critical bottleneck limiting transaction throughput and/or latency [6, 7, 15, 20]. Indeed, recent industry developments [2] based on our work have yielded interest in dedicating resources toward an industry-wide effort to maximize the performance of zero-knowledge proof systems. We believe this to be beneficial not only for distributed payments, but also for any application where high-throughput, low-latency and low-energy zero-knowledge proof generation is required.

Building a consensus algorithm which produces validity proofs for each block as a useful byproduct requires carefully designing the PoW process to replicate the security properties of Bitcoin’s non-useful puzzle. Our main technical contribution is a method to deeply embed a nonce into the proof computation process, making it suitable as a *progress-free* PoW puzzle. We formalize this intuition by introducing the notion of ϵ -amortization resistance and propose a protocol which achieves this. Our results are based on the average-case hardness of multiexponentiation in the Generic Group Model (GGM) [32]. We build a prototype allowing a stateless client to rapidly verify a block (and thus its complete history) in milliseconds with 500 bytes of data downloaded. This also assists miners in quickly validating new blocks broadcast on the network, which may reduce the risk of block collisions and enable faster block frequency.

2 Proof of Necessary Work

To allow proof generation to serve as a PoW puzzle, we require (a) a proof π_i whose generation algorithm \mathcal{P} is moderately difficult to compute and (b) a PoW puzzle $P_V^{\mathcal{H},d}$ that requires the miner to fully recompute \mathcal{P} to test a potential solution. The second property is necessary for the puzzle to be progress-free for fairness to miners of differing size. Indeed, if generating unique proofs π_i based on randomly sampled nonces n_i is sufficiently ‘hard’, then using $P_V^{\mathcal{H},d}$ instead of a generic puzzle (such as computing the double SHA256 digest in Bitcoin) would allow us to not only perform PoW with the same theoretical guarantees, but also compute a valid proof π_i in the process.

We do not formally analyze any consensus properties, since our goal is not to design a new consensus protocol but to retain that used by Bitcoin (and similar systems) and inherit its properties. However, we would like the work done to be useful by producing proofs of each block’s validity. We introduce the notion of performing PoW by proving the validity system state, denoted by *Proof of Necessary Work* (PoNW).

Definition 1. (*Proof of Necessary Work*). *Given a pseudorandom function \mathcal{H} and a proof $\pi_i \in \mathcal{Z}$ in some RSM with transition tuple $(\text{NewState}, \text{VerifyState})$, we define the verification puzzle $P_V^{\mathcal{H}, d} : \mathcal{S} \times \mathcal{S} \times \mathcal{Z} \rightarrow \{0, 1\}$ with difficulty d as the solution to the following function:*

$$P_V^{\mathcal{H}}(\mathcal{S}_i, \mathcal{S}_{i+1}, \pi_{i+1}) = \mathbf{1} \left[\begin{array}{l} \text{VerifyState}(\mathcal{S}_i, \mathcal{S}_{i+1}, \pi_{i+1}) = 1 \\ \mathcal{H}(\pi_{i+1}) < d \end{array} \right],$$

where $\mathbf{1}[\cdot]$ is the indicator function.

By having access to a proof generating algorithm $\mathcal{P}(\mathbf{t}, \mathcal{S}_i, \mathcal{S}_{i+1}, n_i) \rightarrow \pi_{i+1}$ that generates unique (yet valid) π_{i+1} for each n_i , we can generate π_{i+1} for $\mathcal{S}_{i+1} = \text{NewState}(\mathbf{t}, \mathcal{S}_i, \pi_i)$ using a uniformly randomly sampled n_i until the puzzle condition is satisfied:

$$P_V^{\mathcal{H}}(\mathcal{S}_i, \mathcal{S}_{i+1}, \mathcal{P}(\mathbf{t}, \mathcal{S}_i, \mathcal{S}_{i+1}, n_i)) = 1.$$

Then π_{i+1} suffices for public verification that PoW has been performed. This is because our prover will always fail with constant probability (when $\mathcal{H}(\pi_{i+1}) \geq d$), so iteratively sampling new proofs (by sampling new n_i) until a valid one is found can be shown, under the assumption that \mathcal{P} is the most efficient way to find such an n_i , to be a memoryless exponential process and hence *fair*. Note that, by construction, we also guarantee that π_{i+1} is a valid witness for the RSM. The number of transactions verified is always *fixed* (with empty transactions still ‘added’) as otherwise miners would be incentivized to mine puzzles with the smallest blocks.

Like Nakamoto consensus, our puzzle needs the property that solutions are equally hard to test even after testing an arbitrary number of previous solutions. In other words, a miner should not be able to *amortize* costs while testing multiple potential solutions. This property is defined more formally below based on the μ -Incompressibility of [26], although we work in the bounded-size precomputation model. We model PoNW as a function $f^{\mathcal{O}}$ with limited access to some oracle \mathcal{O} that performs a hard computation in an encoding of some group \mathbb{G} .

Definition 2. (ϵ -Amortization Resistance). *For inputs of length λ and outputs $q \in \text{poly}(\lambda)$, function $f^{\mathcal{O}} = \{f^{\mathcal{O}}(n)\}_{n \in \mathcal{N}}$ is ϵ -amortization resistant on average with respect to a sampler S if for all adversaries $\mathcal{A} = (\mathcal{A}_1^{\mathcal{O}}, \mathcal{A}_2^{\mathcal{O}})$ with \mathcal{A} performing less than $(1 - \epsilon)qN$ queries to the oracle \mathcal{O} on average, where N number of queries required for one evaluation of $f^{\mathcal{O}}(n)$ on average, the following is negligible in λ :*

$$\Pr \left[\forall i \in [q], \pi_i = f^{\mathcal{O}}(n_i) \left| \begin{array}{l} \{n_i\}_{i=1}^q \leftarrow n, (n, \text{aux}) \leftarrow \mathsf{S}(1^\lambda) \\ \text{precomp} \leftarrow \mathcal{A}_1^{\mathcal{O}}(1^\lambda, \text{aux}) \\ \{\pi_i\}_{i=1}^q \leftarrow \mathcal{A}_2^{\mathcal{O}}(1^\lambda, n, \text{precomp}) \end{array} \right. \right]$$

This definition captures the fact that computing multiple proofs does not come with marginal gains: indeed, provers cannot use larger computational resources to batch process proofs and achieve disproportionate performance improvements. By preventing large miners from achieving algorithmic returns-to-scale, this property is crucial in ensuring fairness. With the above objectives in mind, we now look at how to adapt our implementation to realize such a system.

Before we look at designing an amortization resistant PoNW system, we summarize the computationally expensive components of proof generation in the Quadratic Arithmetic Program (QAP) Non-Interactive Proof (NIPs) of [27] compiled with [21]. For an ℓ -size statement with m internal variables and n constraints, the prover \mathcal{P} needs to (1) update inputs and witnesses, and (2) perform $9m + n$ exponentiations in \mathbb{G} using elements from the proving key as bases. Since updating variable assignments is orders-of-magnitude faster, amortization resistance requires \mathcal{P} to recompute (almost) all exponentiations for each new nonce.

Amortization of Multiexponentiation. Multiexponentiation is inherently amortizable [16, 19] given enough memory, although space requirements scale exponentially with the number of computed elements. This is because we can precompute the exponents of specific basis elements and perform look-ups that can be used by multiple evaluations at once. We make precise the relationship between size and amortization gain to demonstrate that non-negligible amortization gains require an infeasibly large amount of space. Since we are interested in average-case guarantees, all input elements to the multiexponentiation algorithm (i.e. the enumerated exponents, or puzzle instances) are sampled uniformly randomly from some S .

We consider amortization in Shoup’s Generic Group Model (GGM) [32],¹ in which the adversary can only compute products based on existing group elements (with non-negligible probability), or directly query the exponentiation of some index. The adversary has access to a multiplication oracle $\mathcal{O} : \mathbb{G} \times \mathbb{G} \rightarrow \mathbb{G}$, which returns the multiplication of the input elements over some random encoding $\sigma : \mathbb{Z}_p \rightarrow \mathbb{G}$. This oracle computes $\mathcal{O}(\sigma(i), \sigma(j)) = \sigma(i + j)$. The adversary may also use a polynomially-sized precomputation string. Since they don’t have access to the exponents of the bases that are being multiplied together (so as to perform a direct look-up), computing some $\sigma(k)$ requires the generation of an addition chain ending with $\sigma(k)$.

However, this is the only assumption underlying the lower-bound results which prove the optimality of (the generalized) Pippenger’s algorithm [19], as they obtain lower-bounds on the length of the minimal addition chain needed to

¹ Maurer proposed a slightly different GGM definition [25], for a comparison see [35].

compute some element. In short, our main formal contribution relies on adapting the packing lower-bound ideas of [14, 28] to formalize the relationship between amortization of multiexponentiation of random indices and the amount of space available to the adversary. We do this by making explicit the average-case lower bounds for multiexponentiation, which were only stated (but not proven) in [14, 28] to be a constant term away from the worst-case lower bounds.

Note that the notion of average-case hardness requires an underlying probability distribution over which the input indices are sampled. Obviously, the distribution of the sampled puzzle instances can affect the average-case bounds if, for example, the sampler provides structured output with high probability. Therefore, all results have to be taken with respect to the underlying distribution of the inputs, which is in turn specified by the choice of sampling algorithm S . Where this S is taken to be uniform (as in this work), the notion of average-case hardness defaults to the traditional average-case lower bound results.

In order to make formal statements about the amortization resistance of computing multiple NIPs, we need to show that there exists some sampling algorithm S_{NIP} outputting instance-witness pairs (ϕ, w) so that, on average over its public coins, these output puzzle instances require a minimum number of oracle calls each for computation of their corresponding proof π . Firstly, we construct the equivalent multiexponentiation problem that the above will reduce to. In the following, we restrict ourselves to the NIP of [27], in which the valid output proof consists of κ group elements of the form $\sum_{k=1}^{\kappa} w_k G_i^k$ for $i \in [9]$, $w_k \in [N]$ and an additional element $\sum_{m=1}^{\mu} g(w_1, \dots, w_{\kappa})_m H_m$, where g an m -dimensional n -variable polynomial encoding the instance's witness and $G_i, H_m \in \mathbb{G}$.

Since the hardness of the above computation depends on the structure of w and g , it becomes apparent that we need to restrict the types of *predicates* that we are looking at. In subsequent sections, we make precise the following construction: a circuit with an efficient sampler S such that (1) accepting witness elements $w_1, \dots, w_{\kappa} \in [N]$ are randomly distributed, (2) for each valid instance ϕ there exists only one valid w , and (3) for each valid w , there exists a unique valid g . Note that (1) and (2) are properties of the predicate, while (3) requires a stronger result on the NIP's knowledge guarantees. We will show that predicates satisfying (1) and (2) are enough to reduce the computation of a NIP from [27] (which satisfies (3)) to a multiexponentiation problem (Definition 3) whose amortization we can bound.

Definition 3. *The (κ, μ) -length MultiExp function $f : [N]^{\kappa} \rightarrow \mathbb{G}^{\nu}$ of dimension ν for $\{G_i^{(1)}, \dots, G_i^{(\nu-1)}\}_{i=1}^{\kappa}$, $\{G_i^{(\nu)}\}_{i=1}^{\mu}$, and function $g : [N]^{\kappa} \rightarrow K \subseteq [N]^{\mu}$ is*

$$f(x_1, \dots, x_{\kappa}) := \left(\sum_{i=1}^{\kappa} x_i G_i^{(1)}, \dots, \sum_{i=1}^{\kappa} x_i G_i^{(\nu-1)}, \sum_{i=1}^{\mu} g(x)_i G_i^{(\nu)} \right),$$

where the x_i are given by sampler S , based on its random coins.

In order to provide a reduction that exactly captures the average-case hardness of the above problem, the structure of g becomes important. This requires

a more technical treatment, so here we work in the case where g is a weakly collision-resistant map from the witness elements $x = (x_1, \dots, x_\kappa)$ to the values $(g(x)_1, \dots, g(x)_\mu) \in K \subseteq [N]^\mu$. This defines a computationally unique correspondence between witness elements and representations of μ -degree polynomials with coefficients in $[N]$. We specifically require the mapping $g : [N]^\kappa \rightarrow K \subseteq [N]^\mu$ to be collision-resistant in each of its output coordinates, or that the following probability is negligible for all PPT adversaries \mathcal{A} :

$$\Pr[\exists i \text{ s.t. } g(\mathcal{A}(z))_i = z_i; z \leftarrow g(x), x \leftarrow_R [N]^\kappa] \approx 0,$$

where z_i denotes the i -th coordinate of z . This is enough to provide multiexponentiation amortization bounds, which are given below for the case when $\kappa = \mu$. Note that the general case for $\mu > \kappa$ can also be calculated in the exact same way, but has been omitted for simplicity.

Theorem 1. *The (κ, κ) -length MultiExp function (c.f. Definition 3) of dimension ν over index size $\lambda := \log(N)$, group \mathbb{G} with $|\mathbb{G}| = 2^\lambda$, and storage size q is ϵ -amortization resistant with respect to the uniform sampler for all collision-resistant g , and for large enough κ, λ, ν, q satisfies:*

$$\epsilon \leq \frac{\log(q) + o(1)}{\log(q) + \log(\kappa) + \log(\nu) + \log(\lambda)}.$$

We prove Theorem 1 in Appendix A. This amortization gain is unavoidable for NIPs that reduce to multiexponentiation; such as by compilation with [21].

2.1 Amortization Resistance and Efficiency

We modify the DPS predicate Π to ensure that most of the proof variables change unpredictably with modifications of the nonce or state. This gives amortization resistance in exchange for increasing the number of variables and constraints in the predicate. The performance overhead originates from the need to commit to state and ‘mask’ the computation, which can be expensive for large predicates.

The naive approach would be to isolate each of the different circuits in the system and show that they can be modified to change unpredictably based on some seed. The design challenge here is how to make this happen while conserving the proof’s correctness guarantees. For this, we ideally want to leverage a property specific to our predicate in order to ‘mask’ the computations and treat the proving system as a black box. We leverage the Pedersen hash function to transform our predicate Π to an amortization resistant version in Sect. 4.

Given some nonce n , the prover might only change a part of the input in order to (re)check difficulty. This is an issue if the same nonce can be used with many inputs (in our case, transactions), as an adversarial prover would compute a proof and then only switch out a single transaction (or bit!), rechecking difficulty with no expensive recomputation. Define $\rho := \text{PRF}_n(\text{state})$ that commits to state where PRF a pseudorandom function family. We need to commit to all block

transactions, ensuring that changing one transaction changes ρ . This can be expensive if we exploit no information about the underlying predicate, since PRF would have to commit to every single original variable.

Fortunately, for our predicate the input to PRF is small: we use $\rho = \text{PRF}_n(rt)$ where rt the root of the new state and n the given nonce. Since this input will anyways be computed as part of the protocol, we don't actually suffer any overhead apart from having to verify the above computation. Note that this is actually *constant* in predicate size. In the GGM, we can replace the PRF by a collision resistant hash function CRT instead, since the randomness of the group encoding is sufficient for the witness elements to look random to an adversary.

We can force unique changes to the Merkle path updating the account if we require n to be part of the leaf: since a change in the block (or nonce) would lead to a new n , all update paths need to be recomputed if any transaction is changed. However, we also need to enforce change to the *old* Merkle path checking account existence. This technique is thus not ideal, since these paths do not depend on the current nonce (or state) at all, meaning that around half our variables will remain the same, giving $\epsilon \approx 1/2$.

To get around this, we opt for a different approach. We ‘mask’ the input variables to \mathcal{H} by interaction with ρ (which also commits to n) and transform the constraints of the hash function subcircuit $C_{\mathcal{H}}$ into a new circuit that retains the original Proof of Knowledge (PoK) guarantees by verifying the same underlying computation. By the unpredictability of ρ and randomness of n , we hope to achieve upper bounds for amortization resistance based on the security of the CRT. In this case, the sampler would need to provide valid witnesses for $C_{\mathcal{H}}$ of the form $w = (w_1, \dots, w_m)$ whose encodings are indistinguishable from random, given n sampled uniformly randomly and access to a multiplication oracle \mathcal{O} for a randomized encoding of some \mathbb{G} .

3 Implications for Nakamoto Consensus

PoNW introduces two novel effects on the consensus protocol due to the fact that checking a nonce (on the order of seconds to minutes) can now take a significant fraction of the average block frequency (ten minutes in the case of Bitcoin), whereas it was negligible for traditional PoW puzzles. We can evaluate these effects assuming a single puzzle solution takes time τ to check (with the mean block arrival time normalized to 1). When τ becomes a significant fraction of the average block generation time ($\tau \sim 1$), miners face a loss of efficiency as they will often be forced to discard a partially-checked puzzle solution when a block is broadcast while checking previous solutions. We prove the scale of this efficiency loss in a short theorem:

Theorem 2. *A miner in a PoW protocol with puzzle checking time τ will discard a fraction $1 - \frac{\tau}{e^{\tau} - 1}$ of their work due to newly broadcast solutions.*

Note that as $\tau \rightarrow 0$ (fast puzzle checking time relative to block interval), the fraction of wasted work drops to 0. This is why this effect has never been

considered in prior work. In the reverse direction, as $\tau \rightarrow \infty$ the fraction of wasted work approaches 1. For $\tau = 1$ (solutions take as long to check as the mean block interval), the fraction of wasted work is $\frac{e-2}{e-1} \approx 0.42$, suggesting that we should aim to keep the time (even for slow miners) to get a solution significantly shorter than the mean block time.

Slow puzzle checking time also introduces a concern that miners might refuse to stop working on a partially-checked solution (and hence discard partial work) even if a valid solution is found and broadcast. These *stubborn* miners might cause collisions in the blockchain (two blocks being found at the same height in the chain). We can analyse a worst-case scenario in which all miners are synchronized with identical proving time, in effect making all miners stubborn and maximizing the probability of simultaneous solutions. If miners aren't synchronized, they may opt to finish their current effort after a block is found, but even if all miners do so this reduces to the above case where all miners finish checking a solution simultaneously. We call each synchronized period in which all miners check a solution a *round*.

Theorem 3. *The expected number of solutions in a synchronized mining round is defined by a Poisson distribution with $\lambda = \tau$. The proportion of rounds with multiple solutions (of rounds with any solution) is upper bounded by $\tau/2$.*

By Theorem 3, our prototype unoptimized 100 s proving time (and 10 min block time) leads to less than $\frac{1}{12}$ worst case collisions.

4 Design and Instantiation

We prototype our system using `libsrank` [31], a C++ library implementing the IVC system in [4] using the construction from [27]. This is done using Succinct Non-Interactive Arguments of Knowledge (SNARKs) [3], non-interactive proofs of knowledge with the additional property of *succinctness*: producing constant-sized proofs that can be instantly verified. We can equivalently consider Π_S as an arithmetic circuit C_Π , evaluating to 1 on some input B_i if and only if B_i is a valid commitment to the output of `UpdateState` given some transaction set \mathbf{t} and \mathcal{S}_{i-1} . In our implementation, C_Π is a QAP. Since this construction depends on SNARKs over pairs of elliptic curves that form *IVC-friendly cycles*, we use the same pair of non-supersingular curves of prime order as [4] with 80 bits of security and field size $\log p \approx 298$.

A tree depth of 32 for our implementation allows for 4.2 billion accounts. We compare this to 32 million unique used wallets on the Bitcoin blockchain after 10 years of operation. This requires $32 \cdot 4 = 128$ hash checks for each transaction. We use the circuits in `libsrank` to verify such proofs of inclusion and modification. We modify the Pedersen hash [13] to compute $\prod_{i=1}^D G_i^{1-2x_i}$ where $\{x_i\}_{i=1}^D$ is the bit representation of the input x and $\{G_i\}_{i=1}^D$ is a set of primitive roots for an elliptic curve group $E(\mathbb{F}_p)$. We use Schnorr signatures [30] over the same elliptic curve (EC), based on the hardness of DLP.

In addition to some input x of length n bits, our evaluation requires a pseudorandom seed $\rho \in \{0, 1\}^n$. Consider the following modification, which can be thought of as masking the underlying evaluation by using two sets of input variables: $\mathcal{H}_G(\rho)^2 \cdot \mathcal{H}_H(\rho)$ and x_i for $i \in [n]$, where $\mathcal{H}_G(\cdot)$ the evaluation of the Pedersen function $\mathcal{H}_G(x) = \prod_{i=1}^n G_i^{1-2x_i}$.

The variable $h_0 = \mathcal{H}_G(\rho)^2 \cdot \mathcal{H}_H(\rho)$ forms the ‘starting point’ of the evaluation. In the beginning, the prover will have access to generator constants $\{H_i, H_i^{-1}, G_i^{-2}H_i^{-1}, G_i^2H_i\}$ for the specific instance of the problem. It would then perform a 2-bit lookup based on x_i and ρ_i , multiplying the intermediate variable c_i by one of the above. By carefully choosing these q_i , we can design the circuit in such a way that unpredictability based on the seed is retained by all intermediate variables except the output y , which we ensure equals $\mathcal{H}_G(x)$.

Algorithm 1. MaskedPedersen

Require: $x, \rho \in \{0, 1\}^n, G, H \in \mathbb{G}^n$

Ensure: $y \in \mathbb{G}$

```

1: procedure CACHEGENERATORS( $\rho, G, H$ )
2:   Parse  $\{\rho_i\}_{i=1}^n \leftarrow \rho$ 
3:   Compute  $h \leftarrow \mathcal{H}(\rho, G)$ ,  $h_2 \leftarrow \mathcal{H}(\rho, H)$ ,  $h_0 = h^2 \cdot h_2$ 
4:   return  $h_0, h$ 
5: end procedure
6: procedure MASKEDHASH( $x, \rho, h_0, h$ )
7:   Parse  $\{x_i\}_{i=1}^n \leftarrow x$ ,  $\{\rho_i\}_{i=1}^n \leftarrow \rho$ 
8:   Define  $q = \{q_i\}_{i=1}^n$ ,  $c = \{c_i\}_{i=0}^n$  and set  $c_0 = h_0$ 
9:   for  $i \leq n$  do
10:    if  $\rho_i = 0, x_i = 0$  then  $q_i = H_i^{-1}$ 
11:    else if  $\rho_i = 0, x_i = 1$  then  $q_i = G_i^{-2} \cdot H_i^{-1}$ 
12:    else if  $\rho_i = 1, x_i = 0$  then  $q_i = G_i^2 \cdot H_i$ 
13:    else if  $\rho_i = 1, x_i = 1$  then  $q_i = H_i$ 
14:    end if
15:     $c_i = c_{i-1} \cdot q_i$ 
16:   end for
17:    $y = c_n \cdot h^{-1}$ 
18:   return  $y$ 
19: end procedure

```

Correctness follows from the following observation: at step 0, the variable $c_0 = \mathcal{H}_H(\rho) \cdot \mathcal{H}_G(\rho)^2 = \mathcal{H}_G(\rho) \cdot \prod_{i=1}^n G_i^{1-2\rho_i} \cdot H_i^{1-2\rho_i}$ is initialized as the hash of the seed. For all intermediate steps $j < n$, we have that $c_j = \mathcal{H}_G(\rho) \cdot \left(\prod_{i=1}^j G_i^{1-2x_i} \right) \cdot \left(\prod_{i=j+1}^n G_i^{1-2\rho_i} H_i^{1-2\rho_i} \right)$. Finally, after the n -th bit has been processed the final intermediate variable c_n is equal to the Pedersen hash of the original input x multiplied by (the unpredictable) $\mathcal{H}_G(\rho)$. By multiplying with $\mathcal{H}_G(\rho)^{-1}$, we get $\mathcal{H}_G(x)$. This follows easily from the fact that at every step we are performing the following operation: $c_i = c_{i-1} \cdot (H_i \cdot \mathbf{1}[\rho_i, x_i = 1] + H_i^{-1} \cdot \mathbf{1}[\rho_i, x_i = 0] + G_i^{-2}H_i^{-1} \cdot \mathbf{1}[\rho_i = 0, x_i = 1] + G_i^2H_i \cdot \mathbf{1}[\rho_i = 1, x_i = 0])$.

It can be quickly checked that this computation ensures the previous recursive property when initialized with $c_0 = \mathcal{H}_H(\rho) \cdot \mathcal{H}_G(\rho)^2$. By induction, this implies that after the n -th bit, only $\mathcal{H}_G(\rho)$ and the exponentiations due to the bits of x remain in the output variable i.e. $c_n = \mathcal{H}_G(\rho) \cdot \prod_{i=1}^n G_i^{1-2x_i}$.

Where we know that the variable a_i has small support (when, for example, it is boolean $a_i \in \{0, 1\}$), the prover can always precompute once and use the same answers without performing exponentiations. This is not a problem since all miners would know what the precomputed answers are from the very beginning and can incorporate them with a small memory cost. The problem with creating variables that become more and more ‘deterministic’ is that at some point their support becomes so small that an adversary will be able to precompute some oracle queries. However, since the end value of the sequence of variables $\{c_i\}_{i=1}^n$ is $h \cdot \mathcal{H}_G(x)$ which is also unpredictable due to h , it is not feasible to predict any index $i \in [n]$ without violating the security of the operation $\mathcal{H}_G(\rho) = h$ even if $\mathcal{H}_G(x)$ is previously known. Note that h can be ‘offset’ by a random element I as $h_i = h + I_i$ for each path $i \in [N]$. This provides independence between authentication paths using the same nonce.

We must restrict the proof systems used because certain constructions are inherently insecure: Groth16 [17] can easily be re-randomized, for example, with only a few additional group multiplications. We thus need a notion akin to non-malleability, ensuring that we cannot construct proofs given access to previous valid proofs. To achieve this, we show that Pinocchio [27] satisfies *unique witness extractability*. This property requires the proof system to output proofs with unique encodings for each distinct statement-witness pair, and hence rules out malleability.

Definition 4. Let $\text{NIP} := (\text{Setup}, \text{Prove}, \text{Verify}, \text{Simulate})$ denote a NIP for relation \mathcal{R} . Define the PPT algorithm \mathcal{A} with extractor $\chi_{\mathcal{A}}$, $\mathbf{Adv}_{\mathcal{BG}, R, \mathcal{A}, \chi_{\mathcal{A}}}^{\text{uve}}(\lambda) = \Pr[\mathcal{G}_{\mathcal{BG}, R, \mathcal{A}, \chi_{\mathcal{A}}}^{\text{uve}}(\lambda)]$, and $\mathcal{G}_{\mathcal{BG}, R, \mathcal{A}, \chi_{\mathcal{A}}}^{\text{uve}}(\lambda)$ as:

$$\begin{array}{l} \text{Main } \mathcal{G}_{\mathcal{BG}, R, \mathcal{A}, \chi_{\mathcal{A}}}^{\text{uve}}(\lambda) \\ \hline (p, \mathbb{G}_1, \mathbb{G}_2, \mathbb{G}_T, e, g) \leftarrow \mathcal{BG}(1^\lambda) \\ (\mathbf{crs}, \tau) \leftarrow \text{Setup}(\mathcal{R}) \\ (\phi, \pi_1, \pi_2) \leftarrow \mathcal{A}^{\mathcal{O}}(\mathbf{crs}) \\ (w_1, w_2) \leftarrow \chi_{\mathcal{A}}(\mathbf{tr}_{\mathcal{A}}) \\ b_1 \leftarrow (w_1 = w_2) \cup (\mathcal{R}(\phi, w_1) \neq 1) \cup (\mathcal{R}(\phi, w_2) \neq 1) \\ b_2 \leftarrow \text{Verify}(\mathbf{crs}, \phi, \pi_1) \cap \text{Verify}(\mathbf{crs}, \phi, \pi_2) \cap ((\phi, \pi_1) \notin Q) \cap ((\phi, \pi_1) \notin Q) \cap (\pi_1 \neq \pi_2) \\ \text{Return } b_1 \cap b_2 \\ \mathcal{O}(\phi) \\ \hline \pi \leftarrow \text{Simulate}(\mathbf{crs}, \tau, \phi) \\ Q = (\phi, \pi) \cup Q \\ \text{Return } \pi \end{array}$$

NIP is unique witness extractable if $\forall \mathcal{A} \exists \chi_{\mathcal{A}}$ s.t. $\mathbf{Adv}_{\mathcal{BG}, R, \mathcal{A}, \chi_{\mathcal{A}}}^{\text{uve}}(\lambda) \in \text{negl}(\lambda)$.

Theorem 4. Assume the q -PDH, $2q$ -SDH and d -PKE assumptions hold for $q \geq \max(2d - 1, d + 2)$. [27] satisfies unique witness extractability.

The ability to resample witnesses for a statement-witness pair is advantageous to an adversary, since an ‘easy’ witness could be found by repeated sampling. We follow the definition of 2-hard instances in [12] and define *single witness hard* languages, for which it is hard to find a new witness given an existing one.

Definition 5. Let R_L be a relation, and $\mathcal{L} = \{\phi | \exists w \text{ s.t. } R_L(\phi, w) = 1\}$ an NP language. \mathcal{L} is a hard single-witness language if:

1. **Efficient Sampling:** There exists a PPT sampler $S(1^\lambda)$ outputting a statement-witness pair $\langle S^x, S^w \rangle$ with $S^x \in \{0, 1\}^\lambda$ and $(S^x, S^w) \in R_L$.
2. **Witness Intractability:** For every PPT \mathcal{A} there exists a negligible function $\mu(\cdot)$ such that:

$$\Pr[(S^x(1^\lambda), \mathcal{A}(S(1^\lambda), 1^\lambda)) \in R_{\mathcal{L}}, \mathcal{A}(S(1^\lambda), 1^\lambda) \neq S^w(1^\lambda)] \leq \mu(\lambda).$$

A relation whose statements are outputs of a CRT hash function \mathcal{H} defines a hard single-witness language. We show this for $\mathcal{L}(\mathcal{H}_P) = \{\phi : \exists w \text{ s.t. } \mathcal{H}_{P,|w|}^G(w) = \phi\}$ where $\mathcal{H}_{P,n}^G$ a weakly collision-resistant hash function.

We show that computing a [27] proof for the evaluation of **MaskedHash** (and our DPS predicate) will take on average a similar number of queries as a suitably parametrized MultiExp instance. We restrict to the case of outputs from a sampler S which samples a ρ randomly and generates valid witnesses. Since we are working in the GGM, the witness variables of the **MaskedHash** instance have an encoding that is indistinguishable from random. Therefore, the amortization bounds of Theorem 1 apply.

Theorem 5. There exists a sampler S and QAP R evaluating N parallel instances of k -bit inputs of **MaskedHash** for which the [27] prover and the $(4N(k+1), 8N(k+1)+2k)$ -length MultiExp problem of dimension 10 are equivalent up to constant terms with respect to multiplicative hardness.

The vast majority of the constraints and variables in the predicate of the designed system are hash evaluations, so Theorem 5 can be used to show that there exists a proof system verifying state transitions for the DPS with bounded amortization-resistance guarantees. This is because the DPS predicate spends the vast majority of its time computing a proof whose hardness can be bounded by Theorem 5, since it is a sequence of iterated Pedersen hashes over a unique simulation extractable NIP.

Corollary 1. There exists a DPS with block size T , state tree depth d , and index size λ that admits a Proof of Necessary Work that is ϵ -amortization resistant w.r.t. a multiplication oracle and for which:

$$\epsilon \lesssim \frac{\log(q)}{\log(q) + \log(dT\lambda) + \log(\lambda)},$$

where q is memory size measured in proof elements.

Table 1. Prototype Times and Key Sizes for Predicates verifying different numbers of transactions: Average running times for setup \mathcal{G} , prover \mathcal{P} and verifier \mathcal{V} over 10 iterations are shown alongside proving/verification key and proof sizes.

Txs	Constraints	Generator	Prover	Verifier	Size		
#	#	Avg (s)	Avg (s)	Avg (ms)	pk (GB)	vk (kB)	π (B)
3	3658281	53.99	24.57	16.0	0.74	0.76	373
10	10071527	161.24	88.14		1.96		
20	19233307	268.93	185.10		3.74		
30	28395087	354.83	198.61		5.61		
40	37556867	485.52	286.50		7.15		
50	46718647	570.09	358.95		9.01		

We construct the DPS based on the above specifications and investigate its running time and memory consumption. Results are displayed in Table 1. Our benchmark machine was an Amazon Web Services (AWS) c5.24xlarge instance, with 96 vCPUs and 192GiB of RAM. The security properties of the DPS are based on the guarantee of Π -compliance provided by IVC. It is apparent that setup and proving times dominate both the running time and memory consumption in the protocol. Setup takes place once by a trusted third-party and hence is less critical for day-to-day system performance.

The prover is run by the miners, or full nodes. These generate PoW solutions repeatedly and would compute proof instances for many input nonces. Thus, larger storage requirements (~ 5.42 GB key sizes) could be easily met by these nodes, as could the need for more parallelism and better computing power to bring down the proving rate. We normalize the block time to achieve $\tau = 1/3$ in the sense of Theorem 2 for a proof including 30 transactions. This gives us that a miner will discard in expectation 15.59% of their work for an efficiency of $\sim 84\%$ if all miners operated based on the above benchmarks. Theorem 3 then gives an upper bound on the block orphan rate (or likelihood of block collisions) of 16.65%. Since we are keeping block times constant at 10 minutes, we note that any improvements in SNARK proof generation times will correspondingly decrease the amount of wasted work and orphan rate. Moreover, this does not depend on the way that the proofs are generated: distributed techniques among many participants (such as [34]) would also benefit efficiency through the corresponding decrease of average proof time.

5 Related Work

Several proposals have aimed to reduce verification costs for light clients; Chatzigiannis et al. provide a survey [9]. Most relevant to our work are Vault [24] and MimbleWimble [29] which speed up verifying transaction history and NIPoPoW [22] and FlyClient [8] which speed up verifying consensus. None of these proposals achieve constant-time verification, though they require significantly less work

from provers. Succinct blockchains, which provide optimal $O(1)$ bandwidth and computation costs to verify both history and consensus, were proposed in 2020, simultaneously by this work and the Mina project [5] (formerly Coda). Mina takes a similar high-level approach, encoding state transitions in a recursive proof system for asymptotically optimal verification time. The two proposals vary in a number of technical details, but the main conceptual differences lie in our choice of consensus protocol. Mina implements proof-of-stake consensus, specifically a variant of Ouroboros [23] designed for succinct proofs, but does not incentivize efficient proof generation. By contrast, we implement a PoW variant specifically designed to incentivize proving efficiency.

Subsequent work has provided novel and efficient constructions for succinct blockchains, though not focused directly on prover incentivization. Chen et al. [10] propose a general framework for succinct blockchains over arbitrary transition functions, alongside benchmarks using the Marlin [11] proof system. Hegde et al. [18] tackle a related but critical problem: that of minimizing the total memory requirements of *full nodes*. Vesely et al. [33] propose Plumo, which leverages offline signature aggregation to design a cost and latency optimized light client for the Celo [33] blockchain. Abusalah et al. [1] propose SNACKS, a formal framework that adds knowledge extraction guarantees to Proofs of Sequential Work. We note that these contributions are orthogonal to our main focus of incentivizing efficient proving, and all could be incorporated in a practical PoNW implementation.

A Security Proofs

Proof. (Proof of Theorem 2). Assume that a blocks are found in a Poisson process with a mean of $\lambda = 1$ and an individual miner can check one puzzle solution in time τ . Consider the expected number of blocks this individual miner is able to check before the network broadcasts a solution. A block will be found by the network in less than time τ with probability $\int_0^\tau e^{-x} dx = 1 - e^{-\tau}$. In this case, the miner will not even finish checking a single block. If the network does not broadcast a block within time τ , the miner will check at least one block. The Poisson process then repeats, since it is memoryless. So the expected number of blocks checked is $E_{blocks} = (1 - e^{-\tau}) \cdot 0 + e^{-\tau} \cdot (1 + E_{blocks})$ or:

$$e^{-\tau} \cdot E_{blocks} = 1 + E_{blocks} \cdot E_{blocks} = \frac{1}{e^{-\tau} - 1}.$$

If no partially-checked solutions were wasted, the miner would always expect to check $\frac{1}{\tau}$ solutions. Thus, the fraction of wasted work is:

$$1 - \frac{\frac{1}{e^{-\tau}-1}}{\frac{1}{\tau}} = 1 - \frac{\tau}{e^{-\tau}-1}.$$

Proof. (Proof of Theorem 3). Since solutions are Poisson random variables:

$$\Pr[\text{collision}] = [1 - \text{Po}(1, \tau)/(1 - \text{Po}(0, \tau))] \leq \tau/2.$$

We borrow notation from [28] and parametrize with q input indices, p outputs and maximum index size 2^λ . Where not specified, $H = pq\lambda$. Let $L(\mathbf{y})$ be the minimum number of multiplications to compute $\mathbf{y} = (y_1, \dots, y_p)$ with $y_i \in [2^\lambda]^q$ and $[2^\lambda] = \{1, \dots, 2^\lambda - 1\}$ from the inputs and unit vectors and $L(p, q, 2^\lambda)$ be the maximum over all of them.

Lemma 1. *For any value of $c \leq L(p, q, N)$, there are at most:*

$$\left(\frac{H^2}{c} \right)^c 2^{q+1} e^c (q+1) 2^{O(1)},$$

addition chains of length at most c .

Lemma 2. *Define $H := \kappa q \nu \lambda$, $\phi(q, \kappa, \nu, \lambda) :=$*

$$q \kappa \nu \log(q \kappa \nu) + \kappa \log(H) + q + \log(q+1) + 1,$$

and fix $\mu := \delta H$, corresponding to:

$$c_\delta := \frac{(1-\delta)H - \phi(q, \kappa, \nu, \lambda)}{\log(H) - \log(e) + \log(\mu) + \log(1/\delta)}.$$

For the (κ, κ) -length MultiExp function of dimension ν for CRT g :

$$\Pr_{\mathbf{x} \in_R [2^\lambda]^{\kappa \times q}, \mathbf{G} \in_R \mathbb{G}^{\kappa \times \nu}} [L(f(x_1), \dots, f(x_q)) \leq c_\delta] \leq \left(\frac{1}{2} \right)^{\mu}.$$

Proof. Write $G_k^{(j)} = r_{jk} G$. As the $x_i \in [2^\lambda]^\kappa$ and $r_{jk} \in [2^\lambda]$ are sampled randomly, the values $x_{ik} G_k^{(j)} = x_{ik} r_{jk} G$ for $i \in [q], j \in [\nu-1], k \in [\kappa]$ will be distinct w.h.p. The $\kappa \cdot q$ values $g(x_i)_k \cdot r_{\nu k} G$ will also be distinct w.h.p. as g is collision resistant in each of its κ output coordinates.

Let M be the $q \times (\kappa \nu)$ sized matrix with these values as entries. As each entry is an element in $[2^\lambda]$, the number of matrices M with $q \kappa \nu$ distinct elements is:

$$\binom{2^\lambda}{q \kappa \nu} \geq \frac{2^{\lambda q \kappa \nu}}{(q \kappa \nu)^{q \kappa \nu}},$$

and to each M there corresponds a unique matrix $F = (f(x_1), \dots, f(x_q))$ with dimension $q \times \nu$, where the κ products over random bases for each x_i have been computed. Note that $L(F) = L(M) + \kappa - 1$.

We can thus upper bound the minimal addition chain size $L(F)$ using $L(M)$ and the number of matrices M :

$$\Pr_{\mathbf{x} \in_R [2^\lambda]^{\kappa \times q}, \mathbf{G} \in_R \mathbb{G}^{\kappa \times \nu}} [L(F) \leq c] \leq \frac{|\{\mathbf{z} : L(\mathbf{z}) \leq c\}|}{2^{H - q \kappa \nu \log(q \kappa \nu)}}.$$

The numerator is upper bounded by Lemma 1 and the fact that a single chain corresponds to at most H^κ matrices, giving:

$$\Pr_{\mathbf{x} \in_R [2^\lambda]^{\kappa \times q}, \mathbf{G} \in_R \mathbb{G}^{\kappa \times \nu}} [L(F) \leq c] \leq \left(\frac{1}{2} \right)^{H - \psi(c)},$$

where $\psi(c) := c(2 \log H + \log e) + \phi(q, \kappa, \nu, \lambda) - c \log(c)$.

Suffices to show that for $c \leq c_\delta$, $\psi(c) \leq (1 - \delta)H$. Since $\psi(c)$ is increasing for $c \leq L(\kappa, \nu q, 2^\lambda)$, required to show that $\rho \geq c_\delta$ for $\psi(\rho) = (1 - \delta)H$:

$$\rho(2 \log H + \log e) + \phi(q, \kappa, \nu, \lambda) \geq (1 - \delta) \cdot H,$$

$$\begin{aligned} \log \rho &\geq \log((1 - \delta) \cdot H - \phi(q, \kappa, \nu, \lambda)) - \log(2 \log H - \log(e)), \\ \therefore \rho &\geq \frac{(1 - \delta)H - \phi(q, \kappa, \nu, \lambda)}{\log H - \log(e) + \log(\mu) + \log(1/\delta)}, \end{aligned}$$

since $\mu = \delta H$.

Corollary 2. Fix $\delta > 0$ and let $\psi(\rho_\delta) - (1 - \delta) \cdot H = 0$.

$$\mathbb{E}[L(f(x_1), \dots, f(x_q))] \geq \rho_\delta \cdot (1 - 2^{-\delta H}).$$

Proof. By Markov's inequality:

$$\Pr[L(f(x_1), \dots, f(x_q)) \geq \rho_\delta] \cdot \rho_\delta \leq \mathbb{E}[L(\mathbf{x})],$$

$$(1 - \Pr[L(f(x_1), \dots, f(x_q)) < \rho_\delta]) \cdot \rho_\delta \leq \mathbb{E}[L(f(x_1), \dots, f(x_q))].$$

Proof. (Proof of Theorem 1). Required to compute q iterations of the MultiExp function. Each iteration includes ν multiproducts over random bases, with the indices also sampled from $[2^\lambda]$.

Using c oracle queries to do this corresponds to knowledge of an addition chain of length c containing all of $F = (f(x_1), \dots, f(x_q))$ with $x_i \in [2^\lambda]^\kappa$. Therefore, the probability that we compute F for $\mathbf{x} \in_R [2^\lambda]^{\kappa \times q}$ with less than c queries is upper bounded by the probability that $L(F) \leq c$.

Fix $\delta > 0$. Lemma 2 states that $\exists c_\delta$ s.t. this probability is negligible in $\mu := \delta \kappa \nu q \lambda$ for $c \leq c_\delta$. One function computation of dimension ν with κ inputs has an upper bound on the expected number of multiplications of:

$$\min(\kappa, \nu) \cdot \lambda + \frac{\kappa \nu \lambda}{\log(\kappa \nu \lambda)} \cdot (1 + o(1)).$$

Corollary 2 implies that:

$$\begin{aligned} \epsilon &\leq 1 - q^{-1} \cdot \left(\min(\kappa, \nu) \cdot \lambda + \frac{\kappa \nu \lambda}{\log(\kappa \nu \lambda)} \cdot (1 + o(1)) \right)^{-1} \cdot (1 - 2^{-\delta \kappa \nu q \lambda}) \cdot c_\delta \\ &\leq \frac{\log(q) + \delta \log(\kappa \nu \lambda) + o(1)}{\log(\kappa \nu q \lambda)} \leq \frac{\log(q) + o(1)}{\log(\kappa \nu q \lambda)}, \end{aligned}$$

where we have taken $\delta \leq 1 / \log(\kappa \nu \lambda)$.

Proof. (Proof of Theorem 4). We know that the NIP has a PKE extractor from its security proof and so \mathcal{A} can extract two witnesses almost surely using extractor $\chi_{\mathcal{A}}^{PKE}$. If the polynomials are distinct, so are their witnesses. This follows directly from the fact that, since $\pi_1 \neq \pi_2$, either (1) one of $u_i(X), v_i(X), w_i(X)$ differs in one of the proofs, or (2) the extracted witnesses differ. Since the predicate is the same, it follows that the witnesses must differ.

Lemma 3. Let $\mathcal{H}_P = \{\mathcal{H}_{P,\lambda}^G\}_{\lambda \in \mathbb{N}_+}$ be a family of efficiently computable functions for which each $\mathcal{H}_{P,\lambda}^G : \{0,1\}^\lambda \rightarrow \mathbb{G}$ is weakly collision-resistant. $\mathcal{L}(\mathcal{H}_P)$ is hard single-witness.

Proof. (**Proof of Lemma 3**). Define S in the natural way: fix $\lambda \in \mathbb{N}_+$ and define S to randomly sample an element $x \in \{0,1\}^\lambda$, outputting $(\mathcal{H}_{P,\lambda}(x), x)$. The sampler is efficient by the efficiency of $\mathcal{H}_{P,\lambda}(x)$, and $(\mathcal{H}_{P,\lambda}(x), x) \in R_{\mathcal{L}(\mathcal{H}_{P,\lambda})}$ by definition. Witness intractability (WI) follows from the collision resistance of $\mathcal{H}_{P,\lambda}$ on constant-size inputs. If some \mathcal{A} exists that violates WI, then running S on 1^λ and then \mathcal{A} on $S(1^\lambda)$ and 1^λ , we non-negligibly find a collision in $\mathcal{H}_{P,\lambda}^G$.

Proof. (**Proof of Theorem 5**). The MaskedHash QAP has $4N(k+1)$ intermediate witness variables (and $8N(k+1) + 2k$ constraints) which admits witnesses from a sampler S where the seed ρ is uniformly random and so all witness variables (with full support) also look random by the randomness of the group encoding. This is as the intermediate values are distinct powers of a group element that is random due to ρ and the independence of the I_j index elements. By unique witness extractability and single witness hardness of CRT functions, all valid witnesses have a unique encoding and hence a unique witness polynomial h .

We start with ℓ instances of N k -bit hash evaluations from S , and require ℓ valid proofs. We reduce to the $4N(k+1)$ -length MultiExp problem for ℓ instances and g equal to the function evaluating the representation of h given the witness elements. We provide ℓ of the $4N(k+1)$ intermediate witness variables and the corresponding 9 sets of bases to the MultiExp function. The representation of h will be unique w.r.t. the witness (since the instance is single witness hard) and thus look random due to the inputs. Note that $\mu = 8N(k+1) + 2k$. We finally perform a linear in ℓ number of multiplications to add any witness variables that were not included (i.e. not randomly distributed). Since the MultiExp index distributions are also random, a proof verifies iff the MultiExp solution is valid.

Conversely, given ℓ $(4N(k+1), 8N(k+1) + 2k)$ -length MultiExp instances of dimension 10 with inputs and bases sampled from the QAP's sampler and proving key respectively, we reduce to computing ℓ proofs for N k -bit hash evaluations. This is because the unassigned witness variables can be discerned from the auxiliary input to g , which comes from the QAP sampler. By the uniqueness of the proof's encoding, the set of ℓ valid proofs will have to equal the MultiExp instances after a linear in ℓ number of operations to 'undo' products by any of the additional variables.

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