Date:10.04.2019

Student Names: Ahmet Emir KOCAAĞA, Mete Han KURT
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Group ID: 1
Session ID: FF34

CMPE 240 Experiment 5 Preliminary Work

1. State Register Inputs: Clk, D1, D0, Rst

2. State Register Outputs: N1, N0

3. Combinational Block Inputs: N1, N0, X

4. Combinational Block Outputs: Y1 , Y0, D1, D0

5. Obtain the truth table.

CURRENT STATE(N1,N0)	INPUTS (X)	NEXT STATE (D1,D0)	OUTPUTS (Y1, Y0)
00	0	10	10
00	1	00	11
01	0	10	10
01	1	00	11
10	0	10	10
10	1	11	10
11	0	10	11
11	1	11	01

6. Is this a Moore or Mealy Machine? (No explnation, only short answer)

   Mealy

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7. Draw the finite state machine.