

download_reference

estimate_ref_size

simulate_ChIP_reads

simulate_input_reads

merge_simul_fastqc

all

```
graph TD; A[download_reference] --> C[simulate_ChIP_reads]; A --> D[simulate_input_reads]; B[estimate_ref_size] --> C; B --> D; C --> E[merge_simul_fastqc]; D --> E; E --> F[all];
```