

download_reference

estimate_ref_size

simulate_input_reads

simulate_ChIP_reads

all

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graph TD; A[download_reference] --> C[simulate_input_reads]; A --> D[simulate_ChIP_reads]; B[estimate_ref_size] --> C; B --> D; C --> E[all]; D --> E;
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The diagram is a flowchart with five nodes, each in a rounded rectangle. The nodes are arranged in three rows. The top row has 'download_reference' (yellow border) and 'estimate_ref_size' (green border). The middle row has 'simulate_input_reads' (red border) and 'simulate_ChIP_reads' (blue border). The bottom row has 'all' (green border). Arrows point from 'download_reference' to 'simulate_input_reads' and 'simulate_ChIP_reads'. Arrows point from 'estimate_ref_size' to 'simulate_input_reads' and 'simulate_ChIP_reads'. Arrows point from 'simulate_input_reads' and 'simulate_ChIP_reads' to 'all'.