Question-02

```
// Declare host variables
unsigned int numRows, numCols;
// Original CSR
unsigned int *rowPtrsCSR, *colldxCSR;
float *dataCSR;
// ELL
unsigned int numElemELL;
unsigned int *colldxELL;
float *dataELL;
// COO
unsigned int nnzCOO;
unsigned int *rowldxCOO, *colldxCOO;
float *dataCOO;
// Vector
float *vecSrc, *vecDst;
// Initialize original host CSR matrix and other variables using the data from Problem 1 of this
assignment
numRows = 4;
numCols = 4;
rowPtrsCSR = new unsigned int[] {0,2,3,5,7};
colldxCSR = new unsigned int[] {0,2,2,1,2,0,3};
dataCSR = new float[] {1,7,8,4,3,2,1};
numElemELL = 1; // pick one element from each row for ELL, rest to COO
nnzCOO = 0; // setting counter to zero
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for(unsigned int rowldx = 0; rowldx < numRows; ++rowldx)
{
       unsigned int nnz = rowPtrsCSR[rowldx + 1] - rowPtrsCSR[rowldx];
       for(unsigned int idx = 0; idx < nnz; ++idx)
       {
              if(idx < numElemELL)</pre>
              {
                      unsigned int ellIdx = idx*numRows + rowIdx;
                      colldxELL[ellIdx] = colldxCSR[rowPtrsCSR[rowIdx] + idx];
                      dataELL[ellIdx] = dataCSR[rowPtrsCSR[rowIdx] + idx];
              }
              else
              {
                      rowldxCOO[nnzCOO] = rowldx;
                      colldxCOO[nnzCOO] = colldxCSR[rowPtrsCSR[rowIdx] + idx];
                      dataCOO[nnzCOO] = dataCSR[rowPtrsCSR[rowIdx] + idx];
                      ++nnzCOO;
              }
       }
}
// Declare device memory variables
unsigned int *colldxELL_d;
float *dataELL_d;
float *vecSrc_d, *vecDst_d;
```

```
// Allocate device memory
cudaMalloc((void**) &colldxELL_d, numRows*numElemELL*sizeof(unsigned int));
cudaMalloc((void**) &dataELL_d, numRows*numElemELL*sizeof(float));
cudaMalloc((void**) &vecSrc_d, numCols*sizeof(float));
cudaMalloc((void**) &vecDst_d, numRows*sizeof(float));
// Copy device memory
cudaMemcpy(colldxELL_d, colldxELL, numRows*numElemELL*sizeof(unsigned int),
cudaMemcpyHostToDevice);
cudaMemcpy(dataELL_d, dataELL, numRows*numElemELL*sizeof(float),
cudaMemcpyHostToDevice);
cudaMemcpy(vecSrc_d, vecSrc, numRows*sizeof(float), cudaMemcpyHostToDevice);
cudaMemcpy(vecDst_d, vecDst, numRows*sizeof(float), cudaMemcpyHostToDevice);
// Launch kernel
SpMV_ELL <<< (numRows - 1)/BLOCK_SIZE + 1 , BLOCK_SIZE >>> (numRows, dataELL_d,
colldxELL_d, vecSrc_d, vecDst_d);
// Copy device memory back
cudaMemcpy(vecDst, vecDst_d, numRows*sizeof(float), cudaMemcpyDevicetoHost);
// Complete COO contributions
for(int i = 0; i < nnzCOO; i++)
 vecDst[rowldxCOO[i]] += dataCOO[i]*vecSrc[colldxCOO[i]];
```