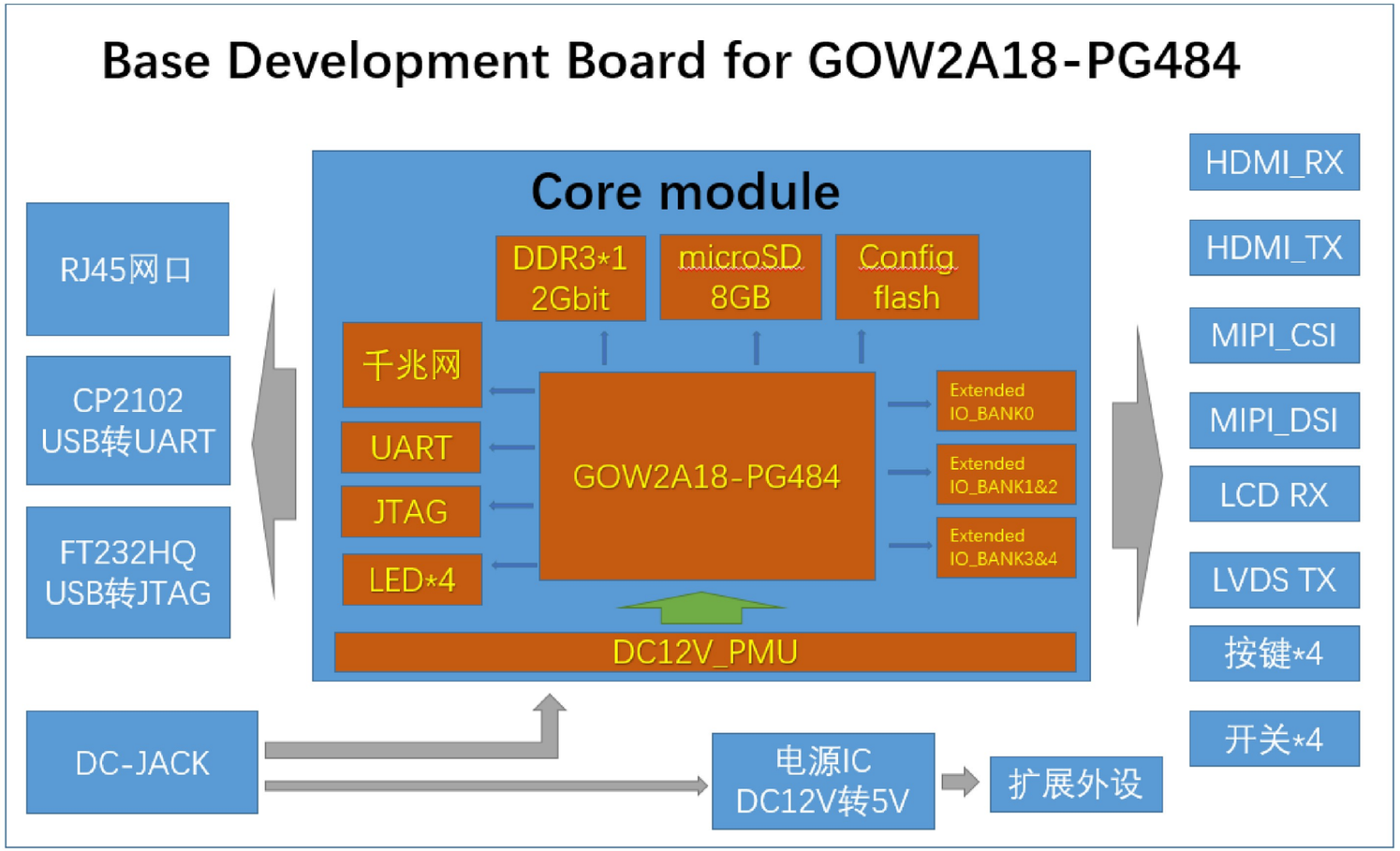
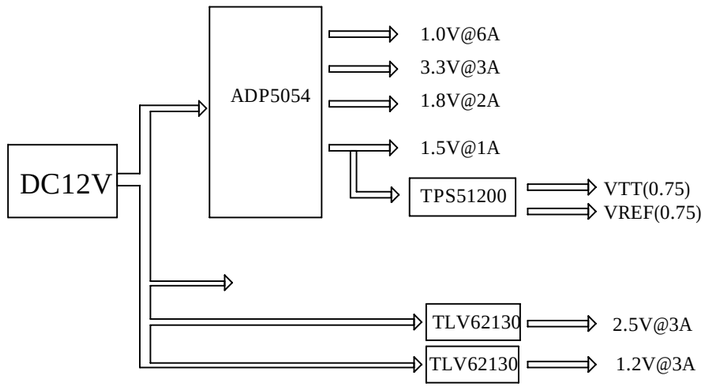
°æ±¾	ÈÕÆÚ	ÐÞ¸ÄÄÚÈÝ
REV1.0	20200807	RELESE
REV1.0	20200813	° Ï ² ¢ · Ö ° å

Block Diagram



POWERTREE



Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
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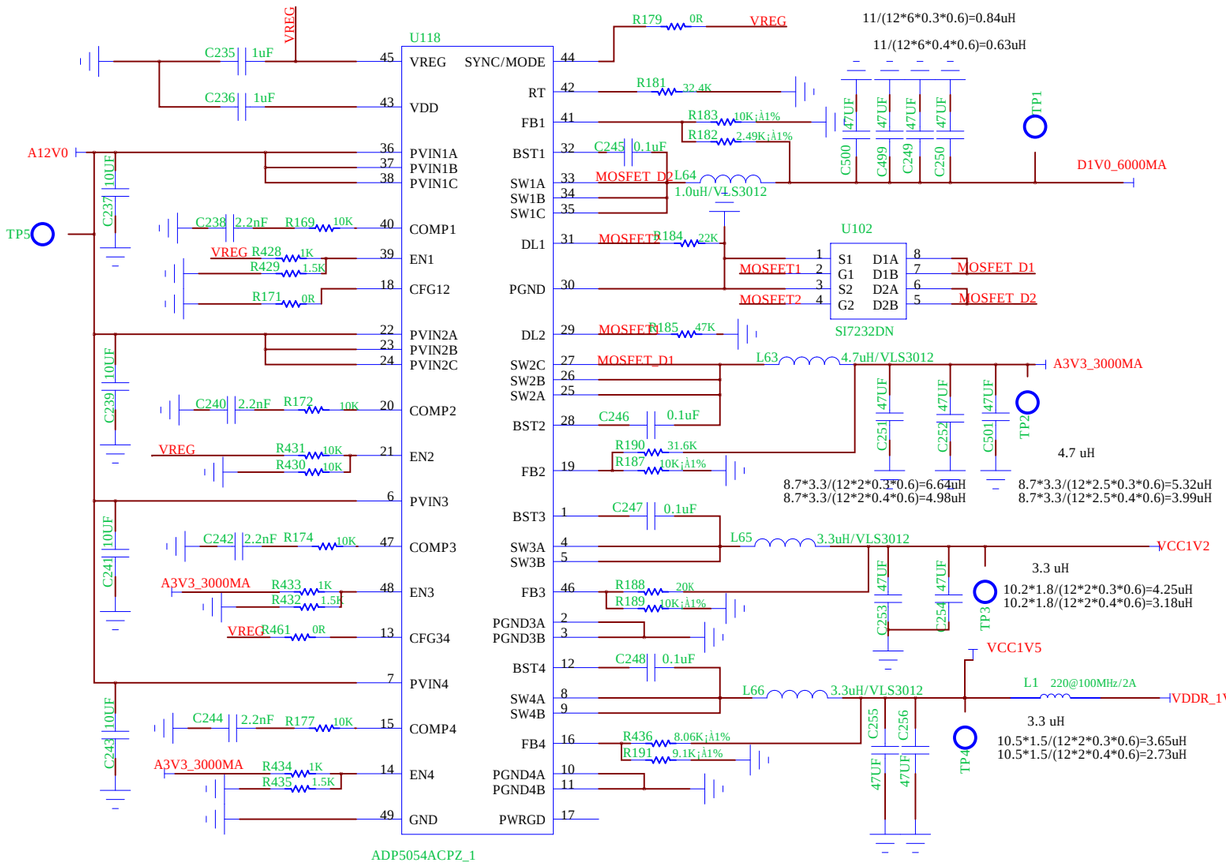
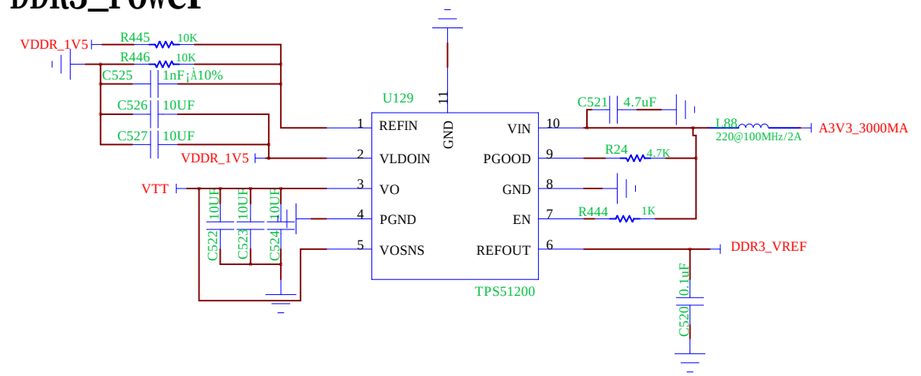
POWER Manage

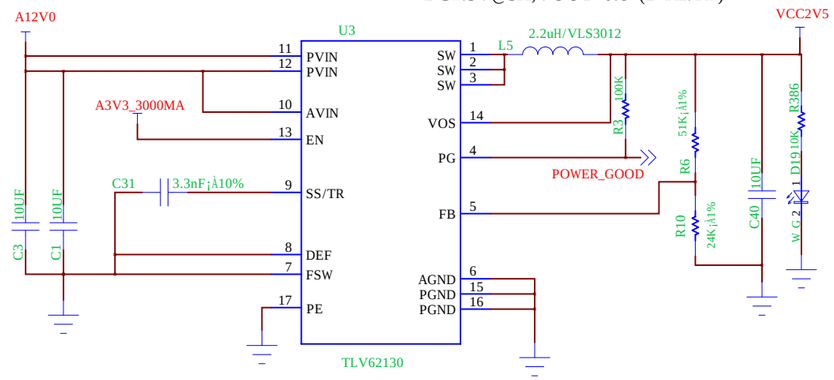


VOUT= Vref¡Á(1 + (Rtop/Rbot))

DDR3_Power



DC2.5V@3A;VOUT=0.8*(1+R1/R2)



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Drawn By:	Date: 20190312
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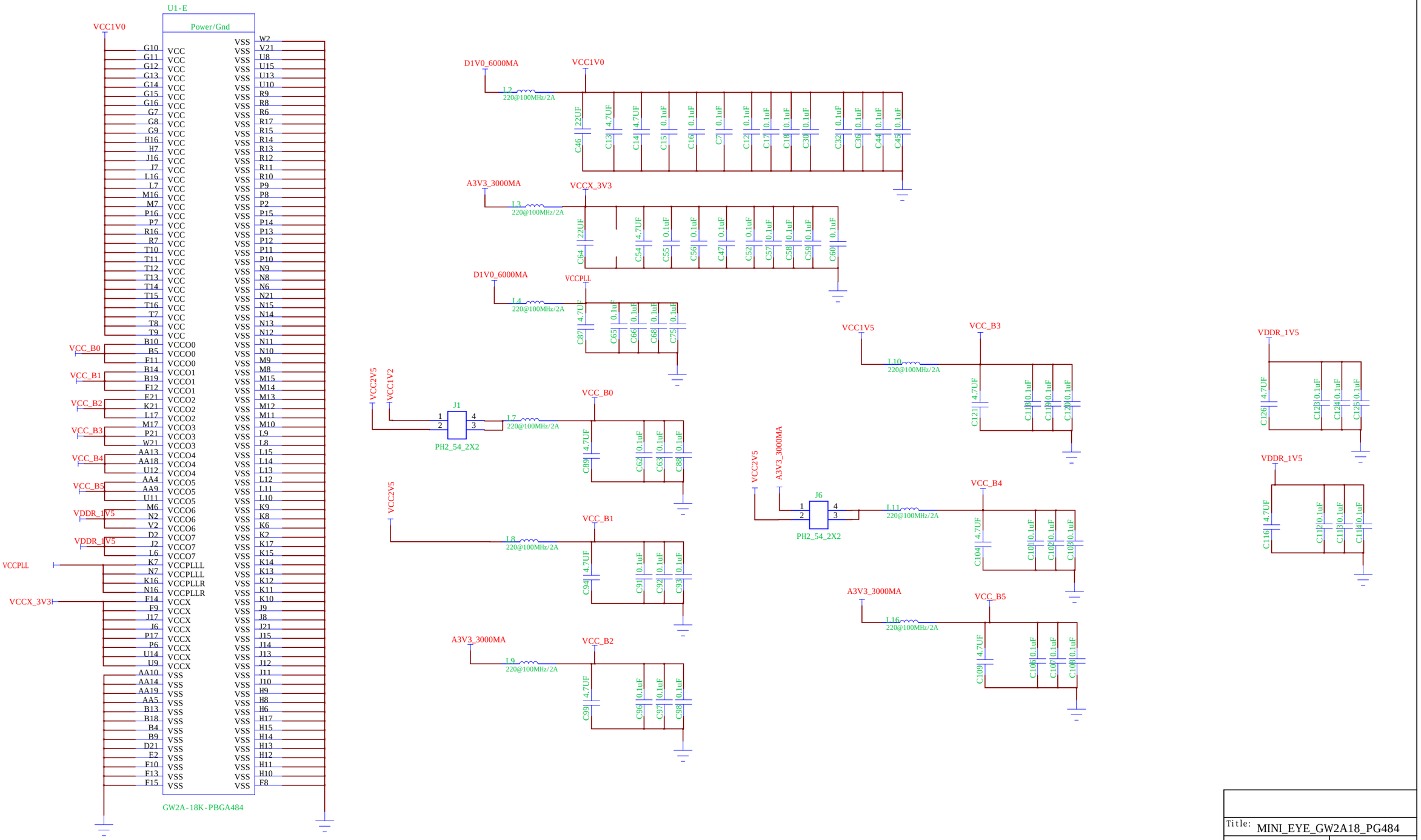
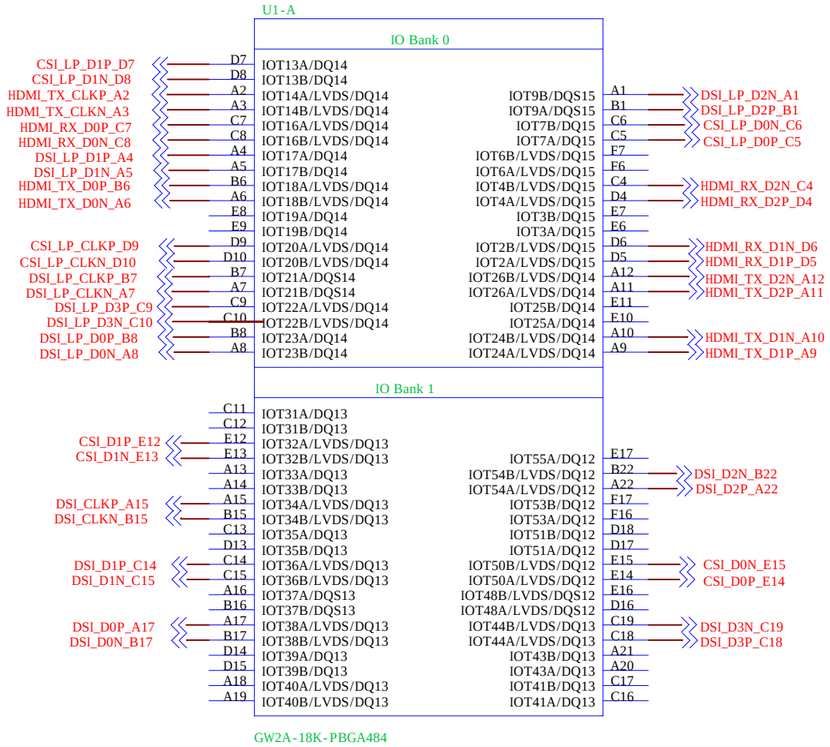
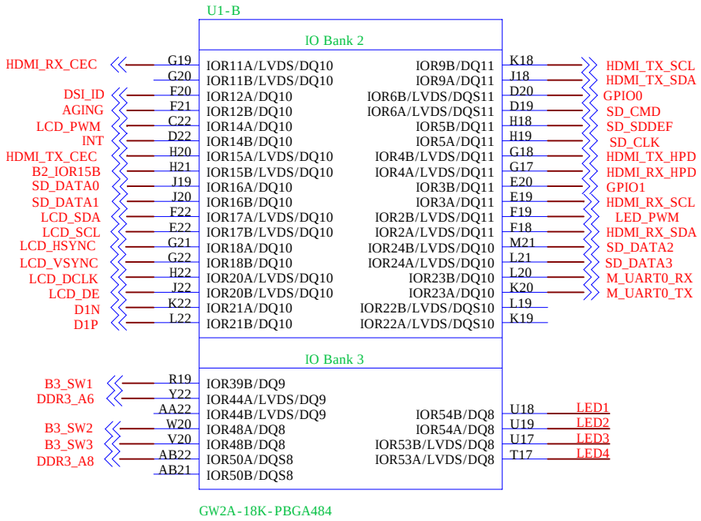
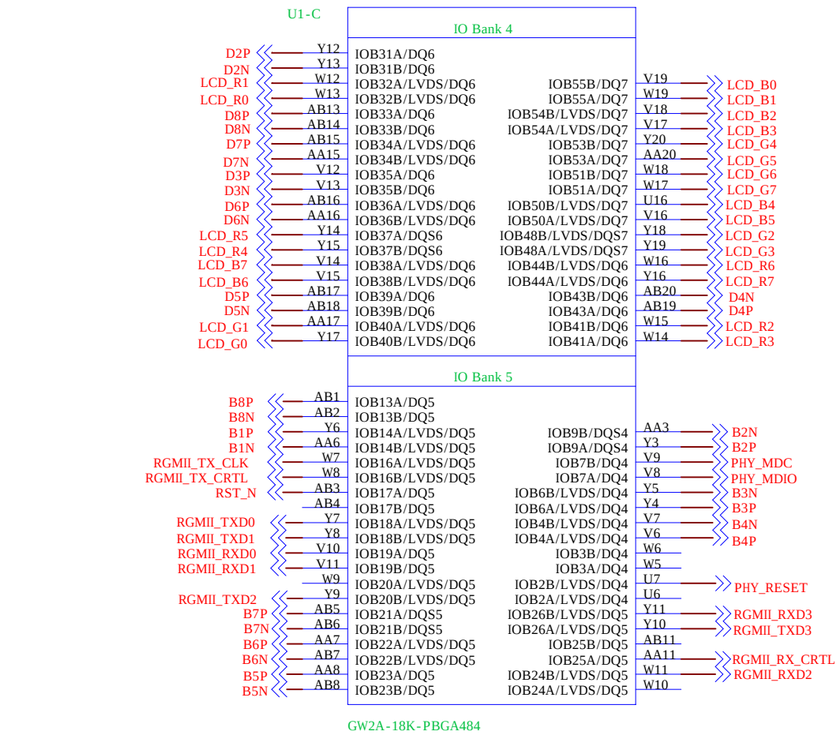
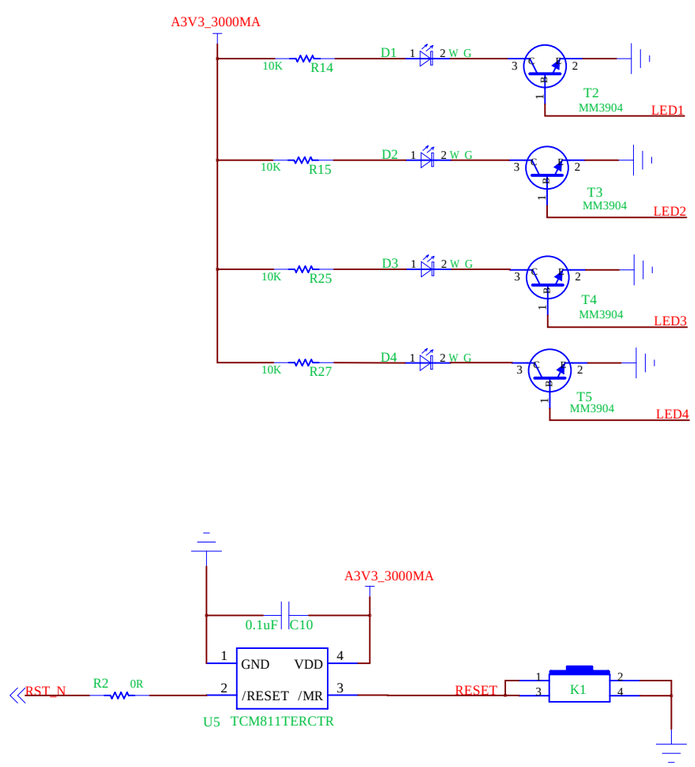

U1-E
Power/Gnd
GW2A-18K-PBGA484
VCC1V0
VCC_B0
VCC_B1
VCC_B2
VCC_B3
VCC_B4
VCC_B5
VDDR_1V5
VCCPLL
VCCX_3V3
D1V0_6000MA
A3V3_3000MA
VCC2V5
VCC1V2
VCCX_3V3
VCCPLL
VCC1V5
VDDR_1V5
J1
PH2_54_2X2
J6
L2
L3
L4
L7
L8
L9
L10
L11
L16
220@100MHz/2A
Title: MINI_EYE_GW2A18_PG484
Drawing No:
Version: V1.0
Drawn By:
Date: 20190312
Checked By:
Date:
Approved By:
Date:
Size A3
Sheet: 2/5









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Drawn By:	Date: 20190312
Checked By:	Date:
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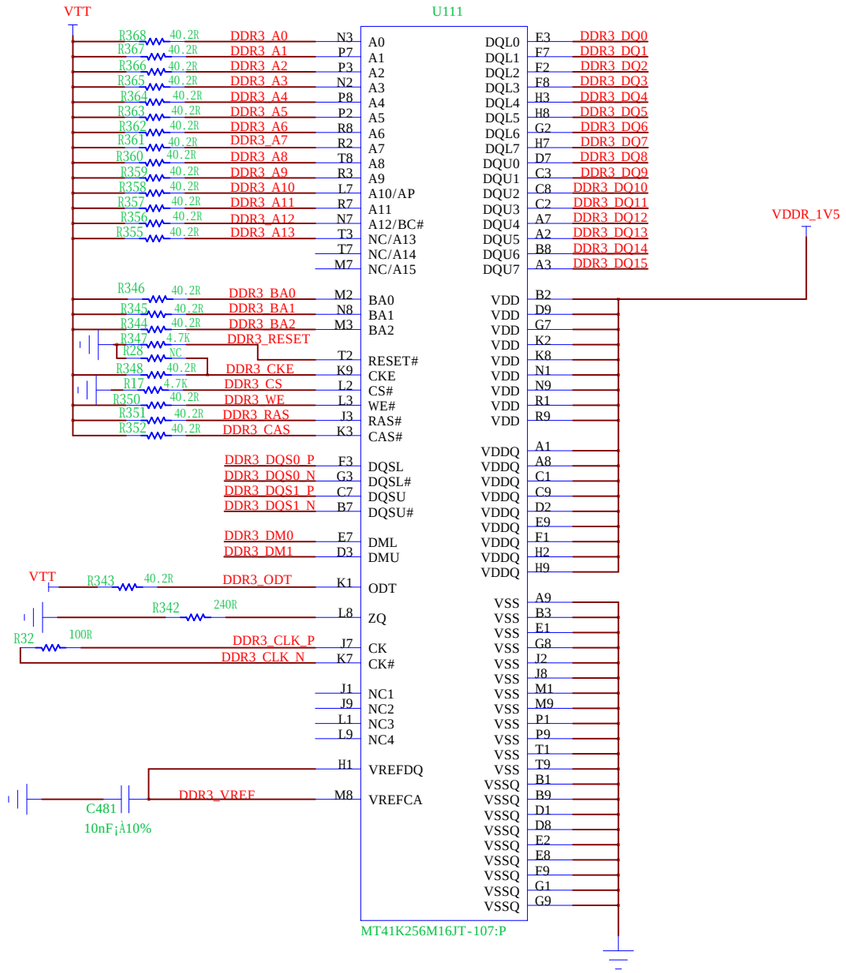

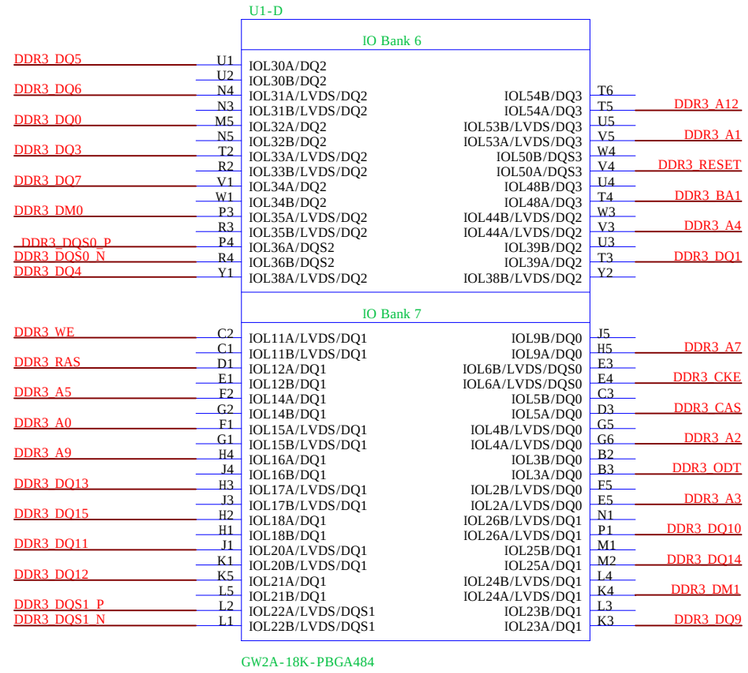

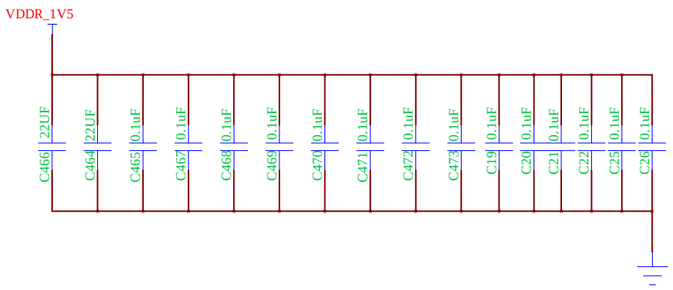

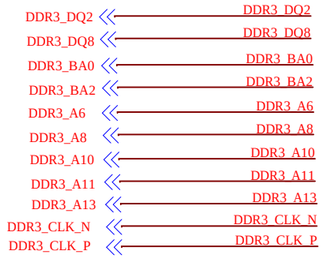

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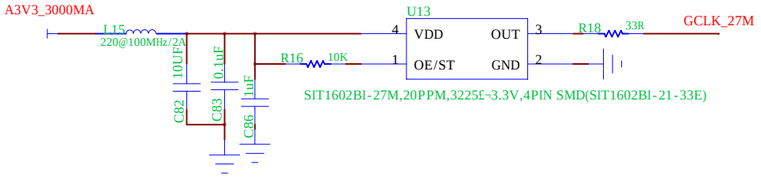
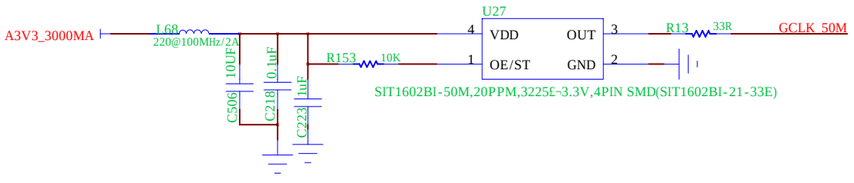
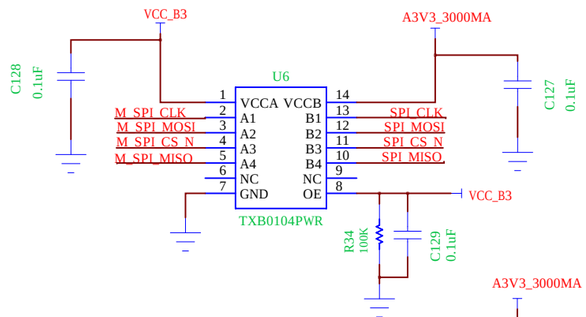
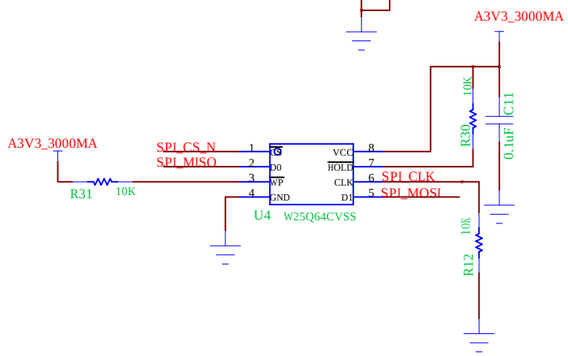
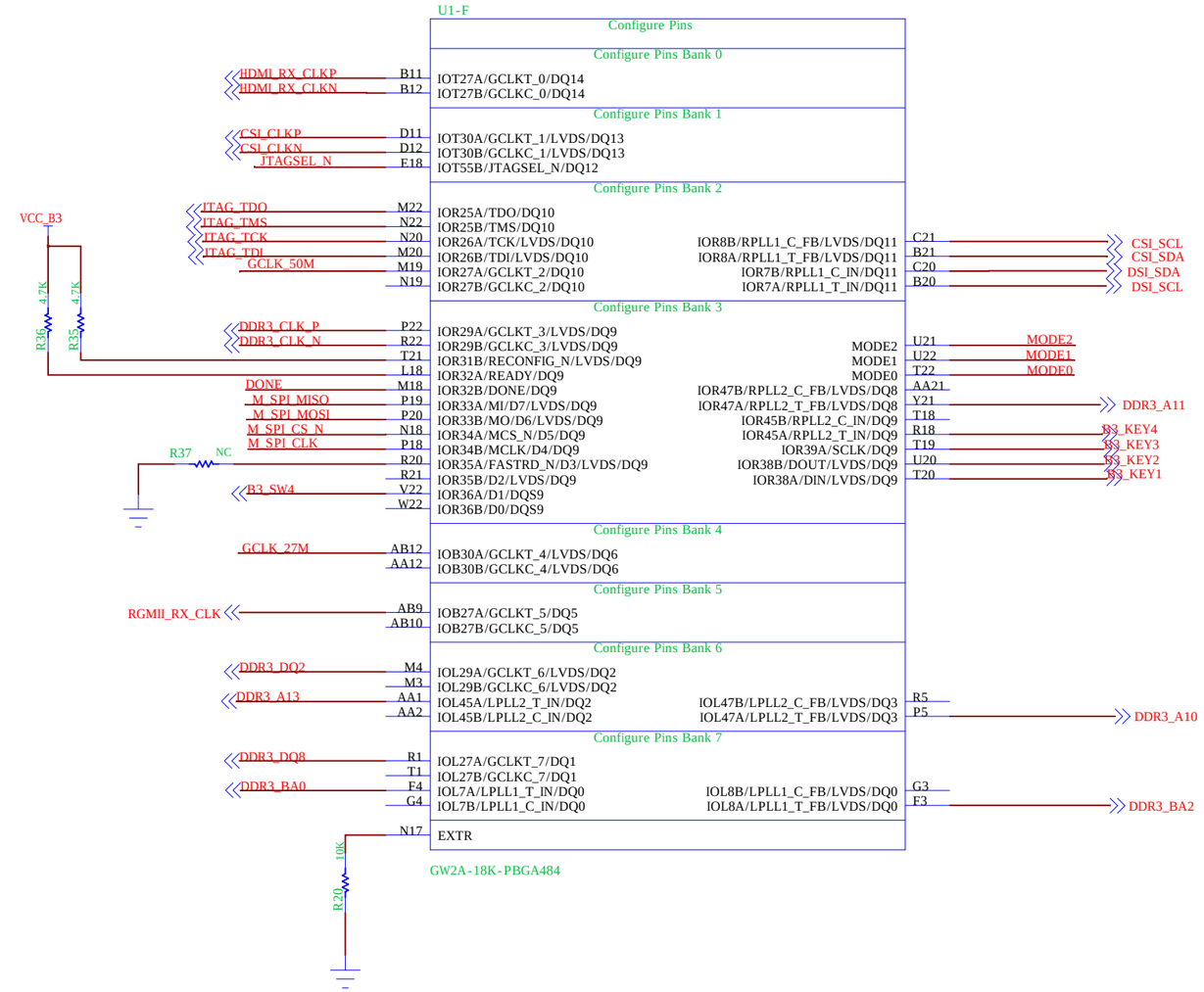
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5

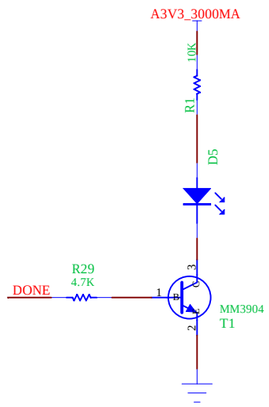


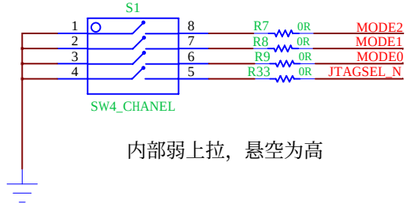












内部弱上拉，悬空为高

Title: MINI_EYE_GW2A18_PG484	
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Drawn By:	Date: 20190312
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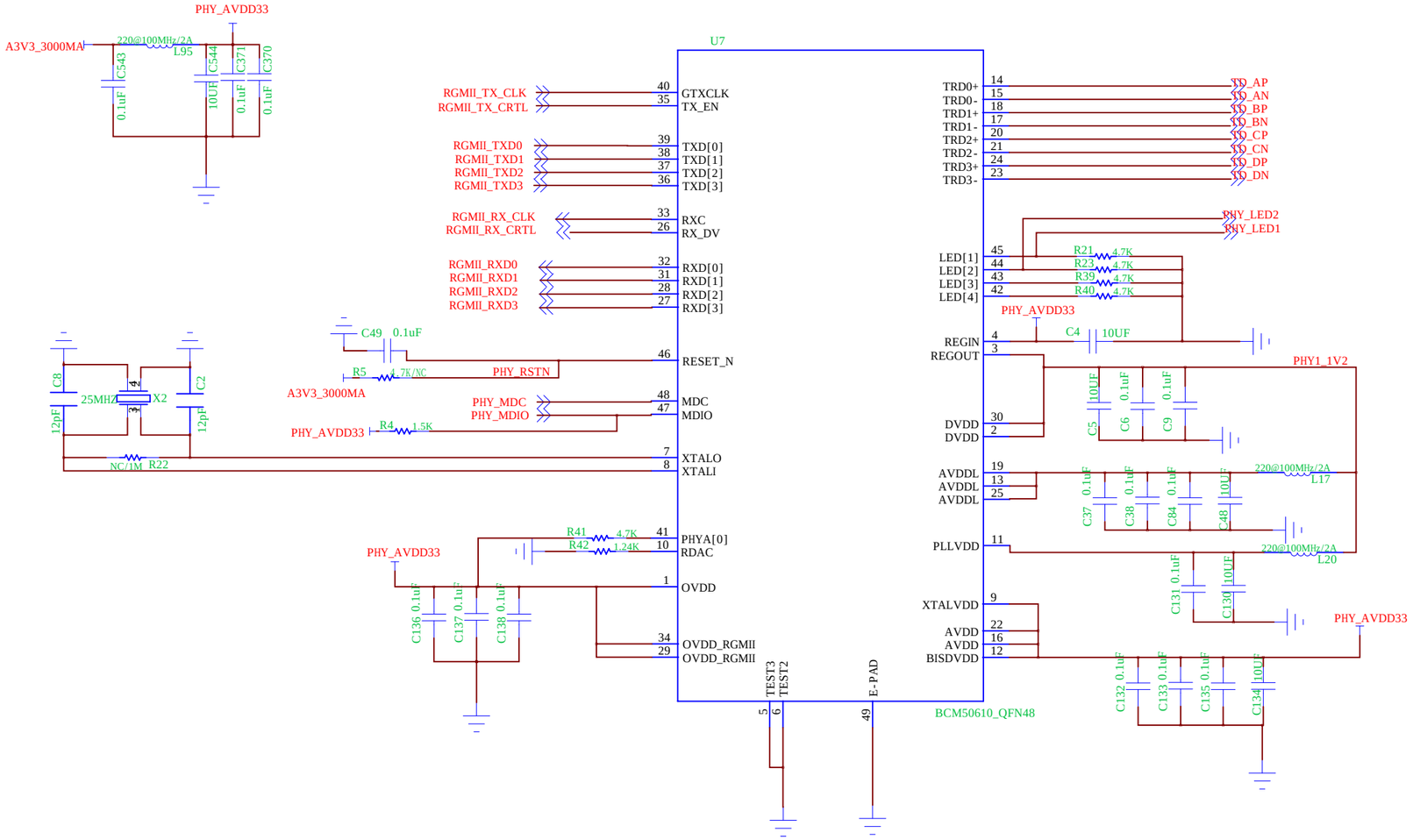
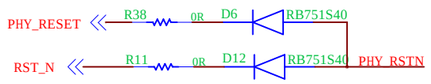
ETHENET@1000M





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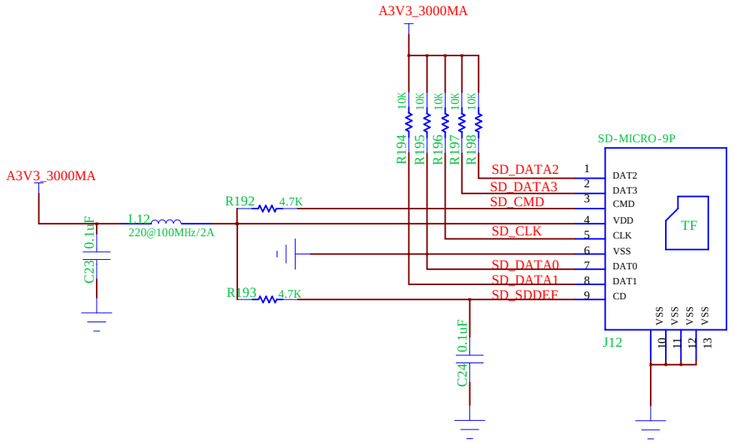
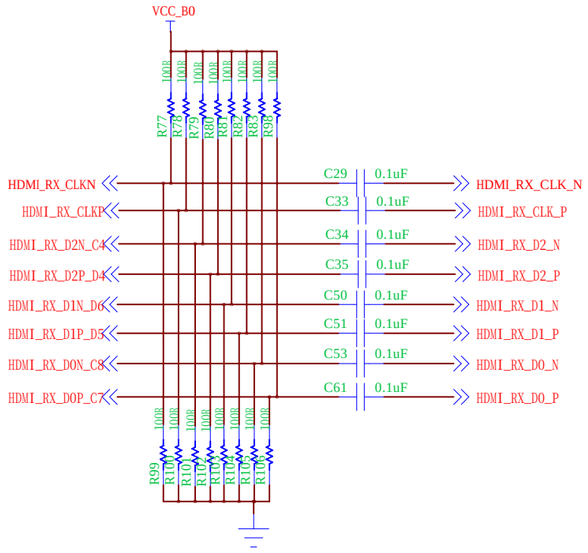
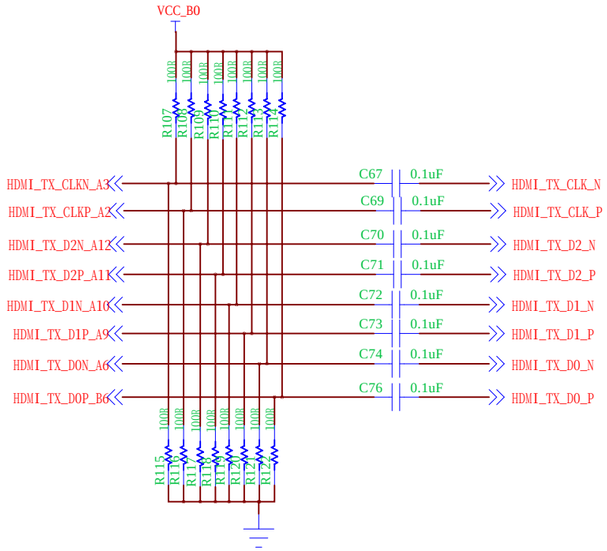
micro_SD







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Drawn By:	Date: 20190312
Checked By:	Date:
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HDMI

D7
HDMI_RX_D2_P
HDMI_RX_D2_N
HDMI_RX_D1_P
HDMI_RX_D1_N
IN1 OUT1
IN2 OUT2
GND
IN3 OUT3
IN4 OUT4
RCLAMP0524P

D8
HDMI_RX_D0_P
HDMI_RX_D0_N
HDMI_RX_CLK_P
HDMI_RX_CLK_N
RCLAMP0524P

D9
HDMI_TX_D2_P
HDMI_TX_D2_N
HDMI_TX_D1_P
HDMI_TX_D1_N
RCLAMP0524P

D11
HDMI_TX_D0_P
HDMI_TX_D0_N
HDMI_TX_CLK_P
HDMI_TX_CLK_N
RCLAMP0524P

A3V3_3000MA
A5V_3000MA
HDMI_RX_5V0
R122 1K
R124 1K
C78 0.1uF
R123 200K
C79 0.1uF
R96 1.5K
R97 1.5K
U8
GND
SREF
S1
S2
GREF
DREF
D1
D2
HDMI_RX_SCL
HDMI_RX_SDA
HDMI_SCL_LS
HDMI_SDA_LS
GTL2002D

R84 49R9
R85 49R9
R86 49R9
R87 49R9
R88 49R9
R89 49R9
R91 49R9
R92 49R9
R93 27K
D10 RB751S40
HDMI_RX_D2_P
HDMI_RX_D2_N
HDMI_RX_D1_P
HDMI_RX_D1_N
HDMI_RX_D0_P
HDMI_RX_D0_N
HDMI_RX_CLK_P
HDMI_RX_CLK_N
HDMI_RX_CEC
J19
D2+
D2S
D2-
D1+
D1S
D1-
D0+
D0S
D0-
CLK+
CLKS
CLK-
CEC
RES
SCL
SDA
DDC
+5V
HPD
HDMI-A-19

HDMI_RX_HPD
U9
/OE
A
GND
VCC
Y
NC7SZ125M5X
C77 0.1uF
R95 1K
R94 100K
R90 10K

A3V3_3000MA
A5V_3000MA
HDMI_TX_5V0
R140 1K
R141 1K
C81 0.1uF
R142 200K
C90 0.1uF
R138 1.5K
R139 1.5K
U10
GND
SREF
S1
S2
GREF
DREF
D1
D2
HDMI_TX_SCL
HDMI_TX_SDA
HDMI_TX_SCL_LS
HDMI_TX_SDA_LS
GTL2002D

R126 49R9
R127 49R9
R128 49R9
R129 49R9
R130 49R9
R131 49R9
R133 49R9
R134 49R9
R135 27K
D13 RB751S40
HDMI_TX_D2_P
HDMI_TX_D2_N
HDMI_TX_D1_P
HDMI_TX_D1_N
HDMI_TX_D0_P
HDMI_TX_D0_N
HDMI_TX_CLK_P
HDMI_TX_CLK_N
HDMI_TX_CEC
J14
D2+
D2S
D2-
D1+
D1S
D1-
D0+
D0S
D0-
CLK+
CLKS
CLK-
CEC
RES
SCL
SDA
DDC
+5V
HPD
HDMI-A-19

HDMI_TX_HPD
R143 15K
C80 0.1uF
Q1
SI2300,N_MOS_SOT23
R137 10K
L13 220@100MHz/2A
R136 10K
R132 10K

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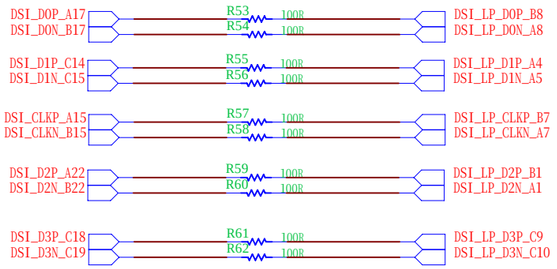
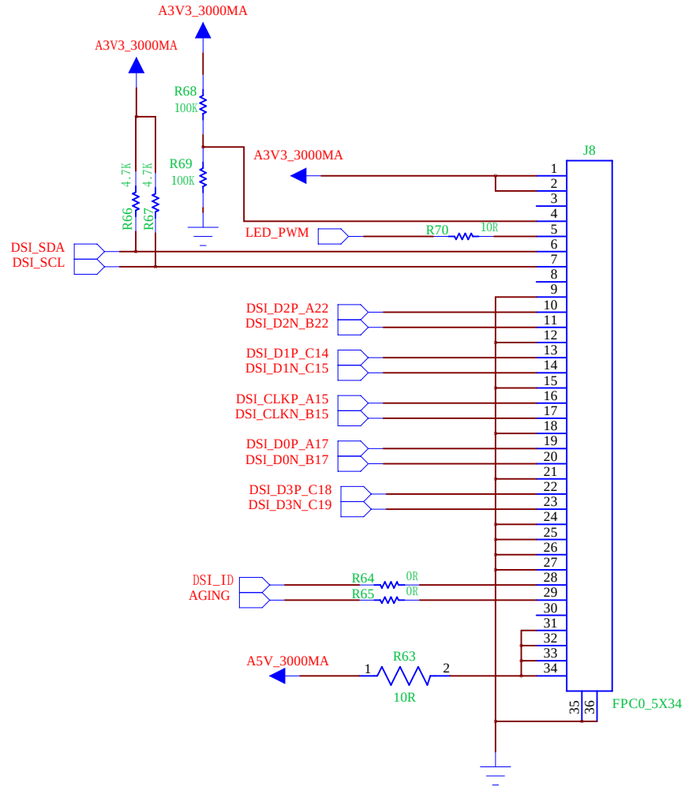
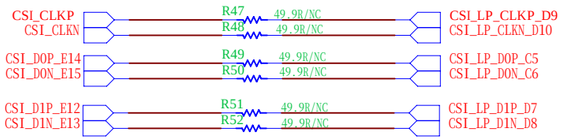
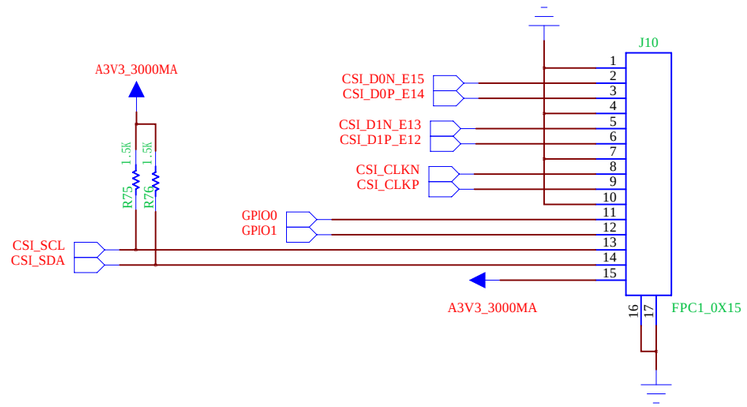
MIPI_DSI





MIPI_CSI





CSI端接电阻，靠近FPGA

Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
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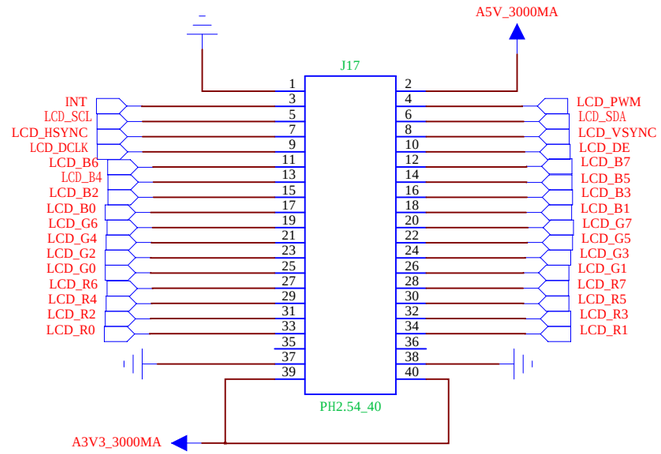
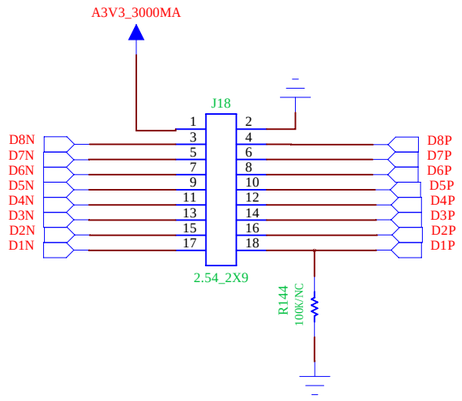
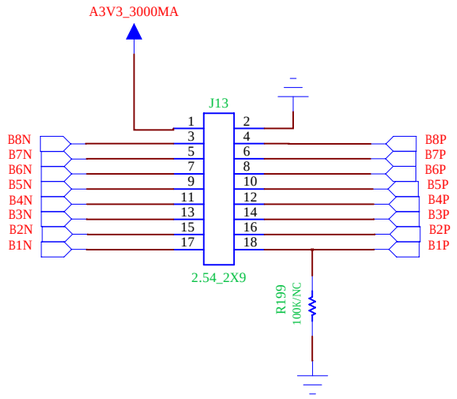
RGB_LCD



Pin11/12——Pin33/34是12对差分对引脚，单数pin为DP，双数pin为DN，可复用为LVDS_TX或其他

PMOS





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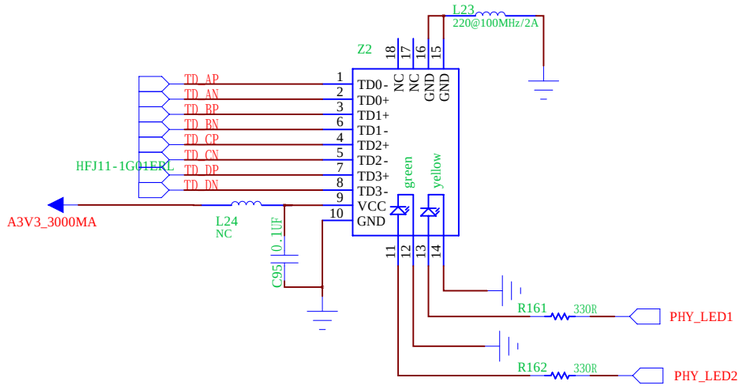
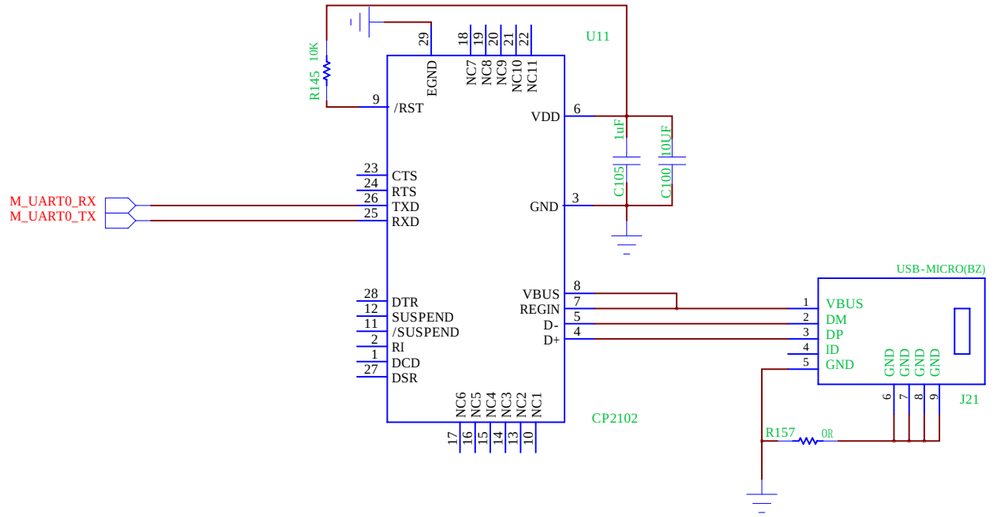
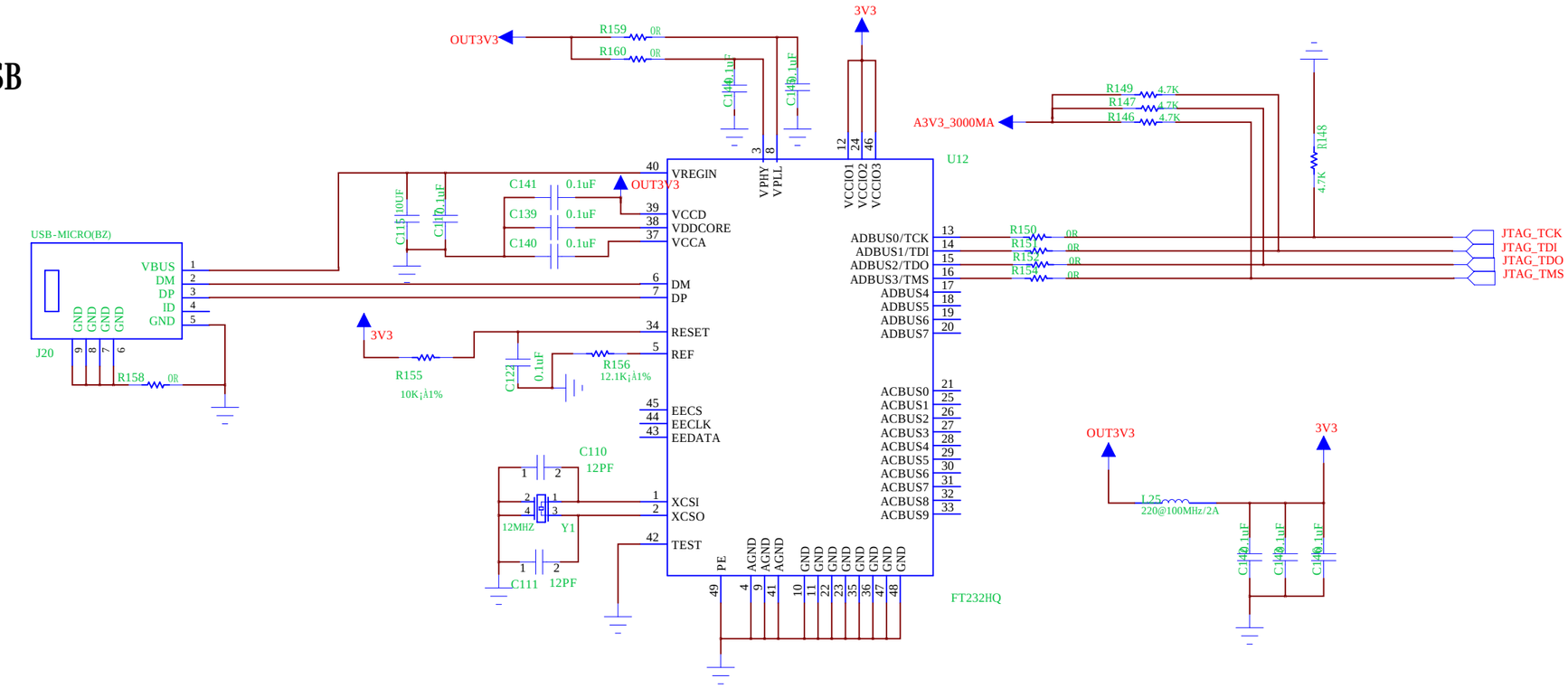
RJ45



UART-USB



JTAG-USB



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Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
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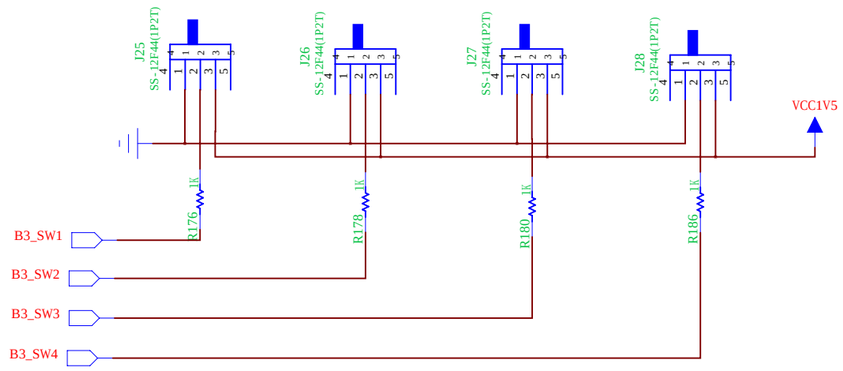
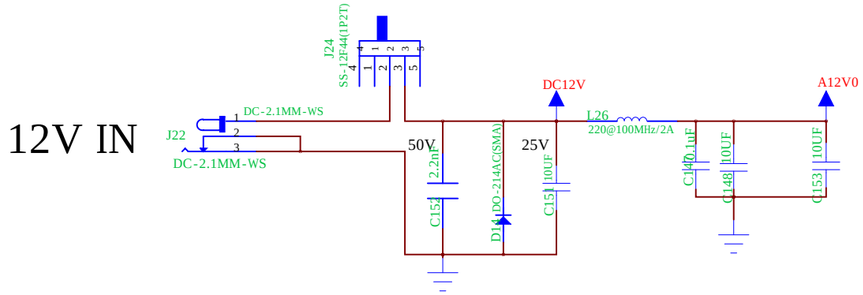
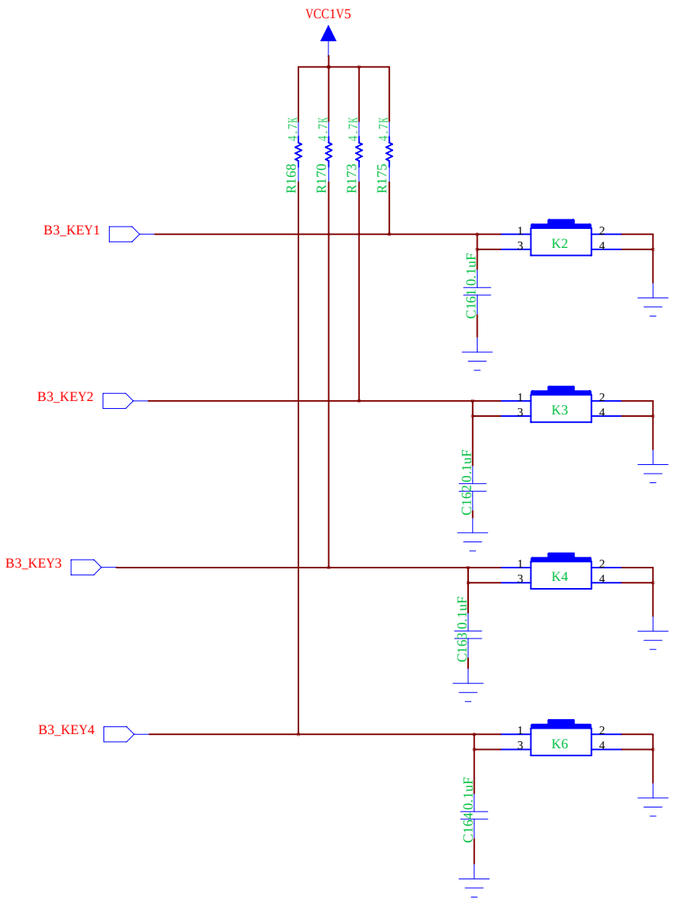
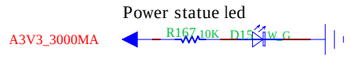
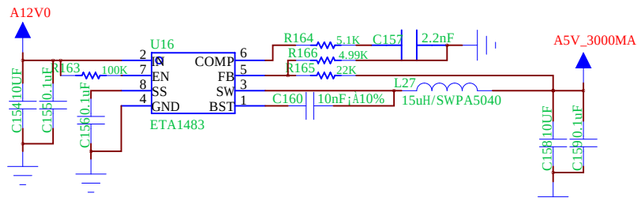
POWER Connector

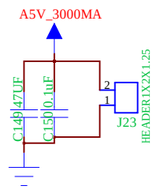












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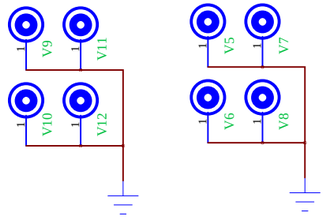

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Checked By:	Date:
Approved By:	Date:
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