

D

°æ±¼	ÈÕÆÚ	ÐÞ_ÄËµÃ÷
REV1.0	20200807	RELESE
REV1.0	20200813	°Ï≈c·.Û°a

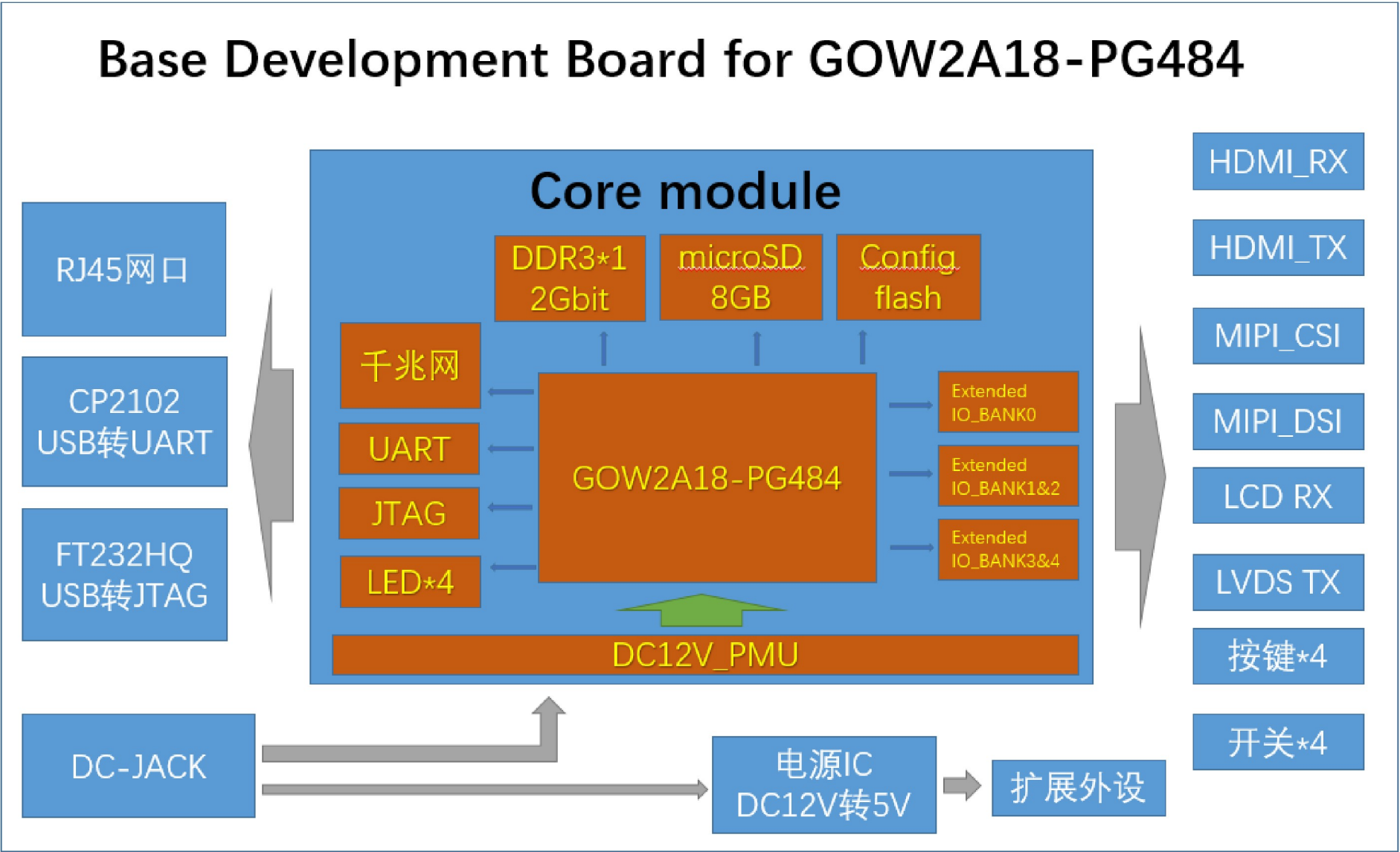
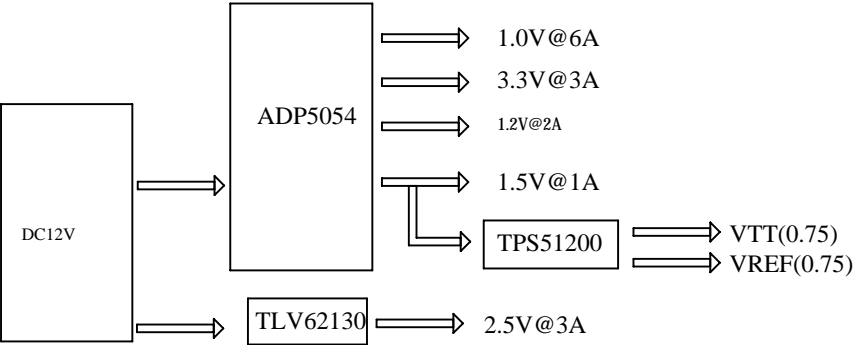
C

B

A

Block Diagram

POWERTREE



Title:MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size	A3 Sheet: 2/5

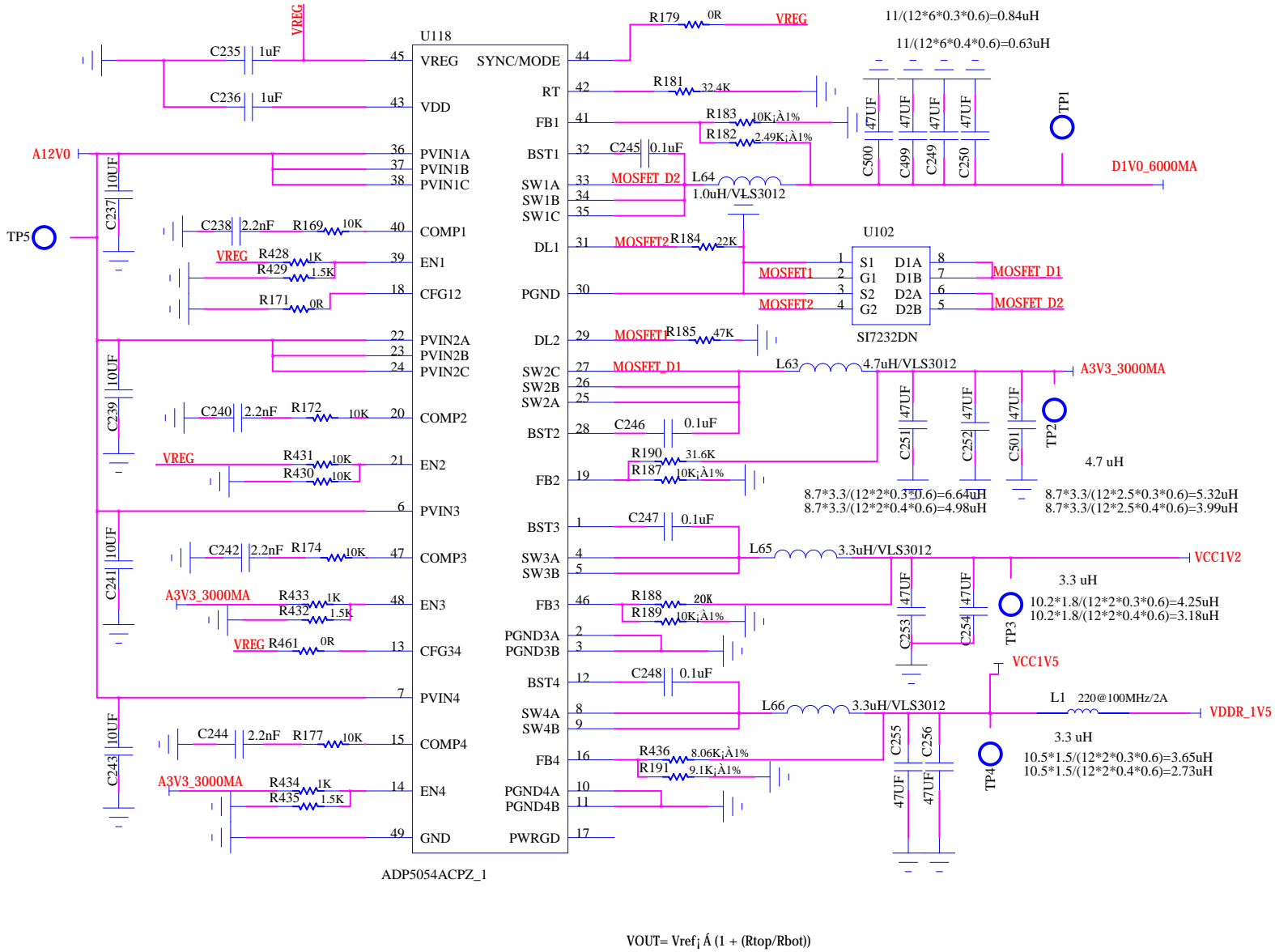
D

POWER Manage

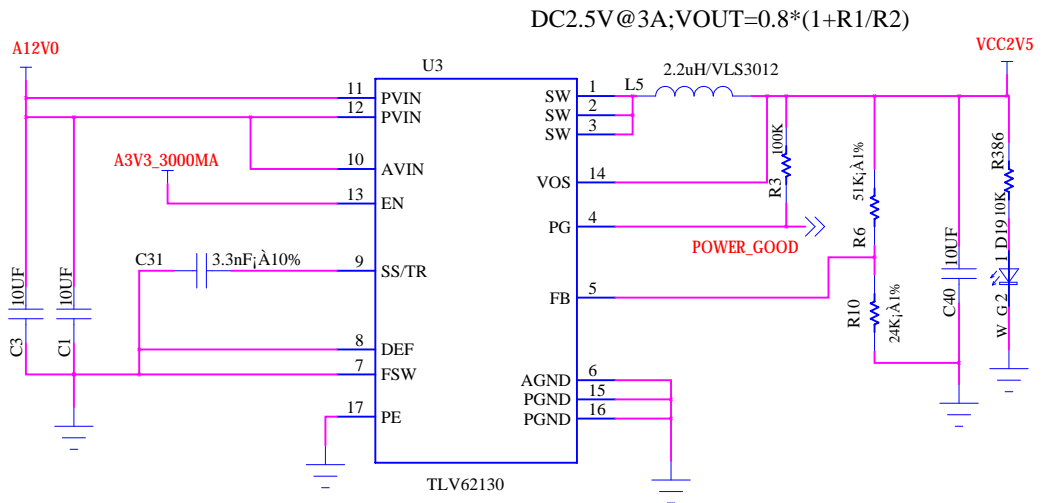
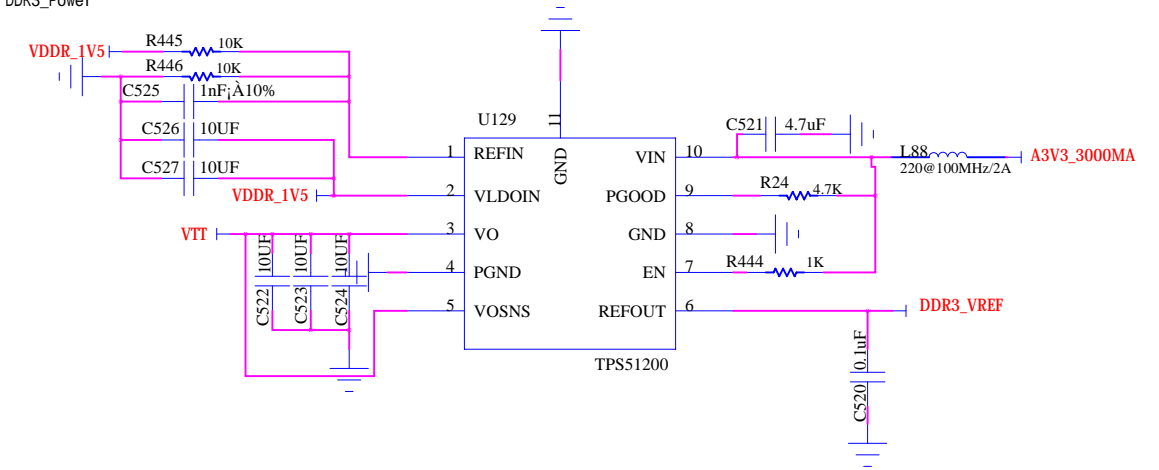
C

B

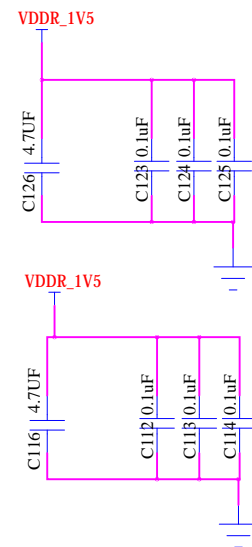
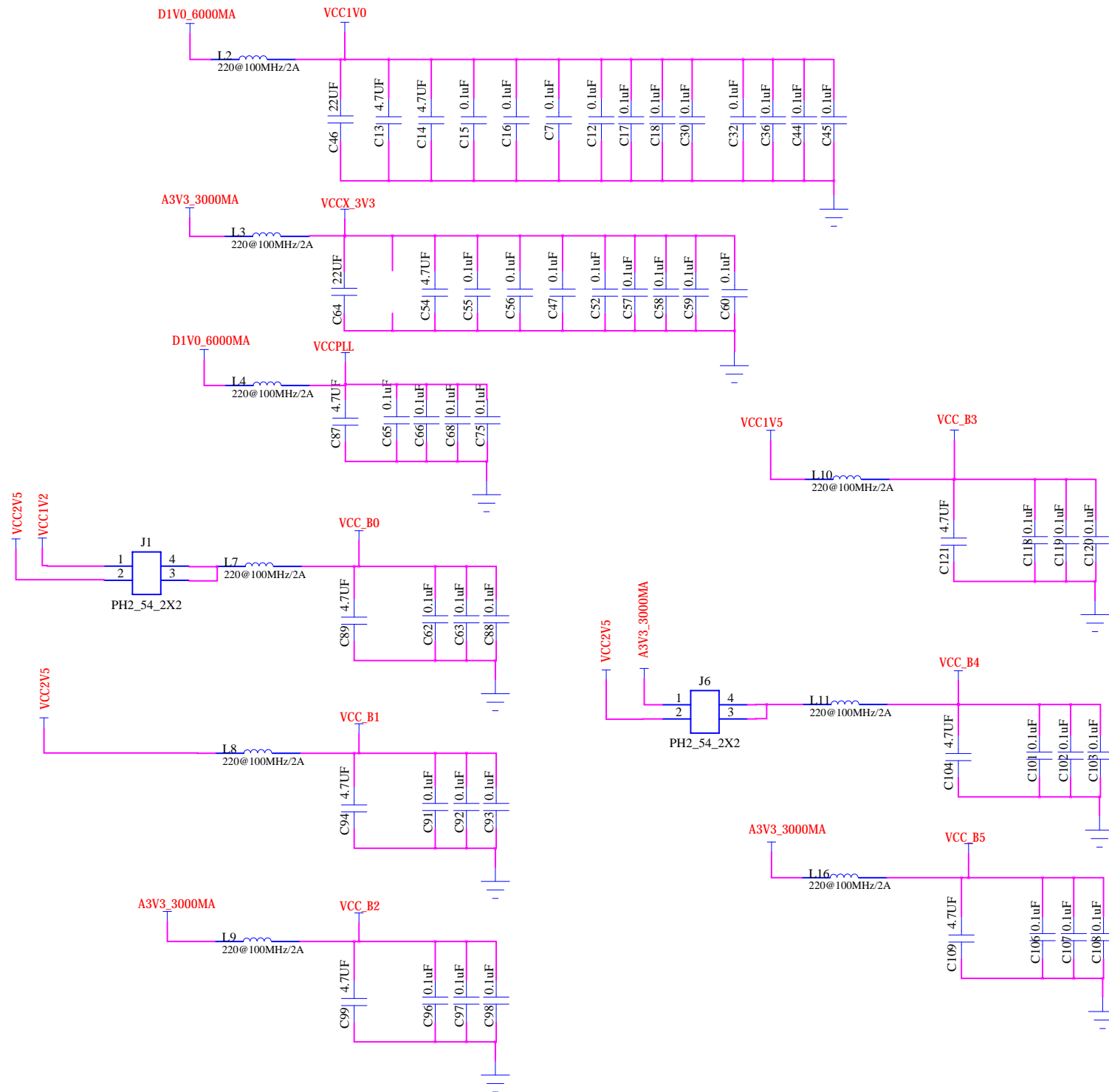
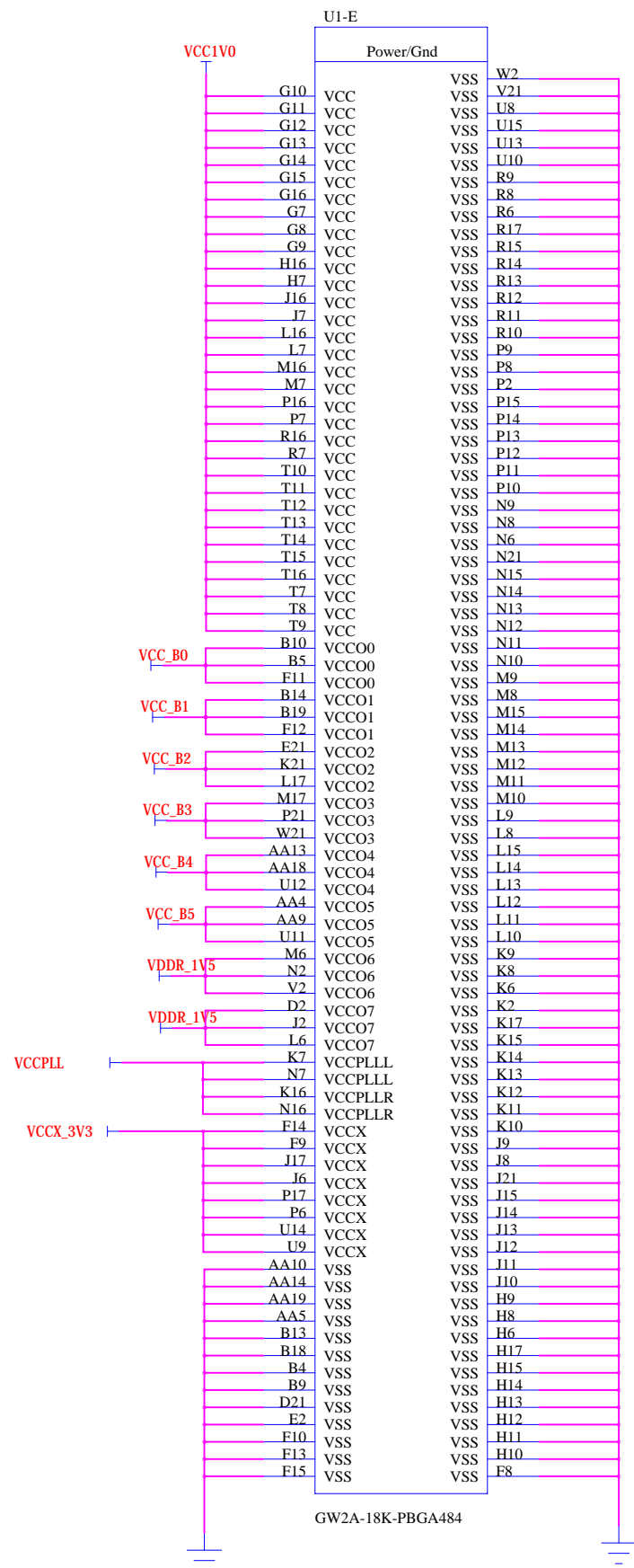
A



DDR3\_Power

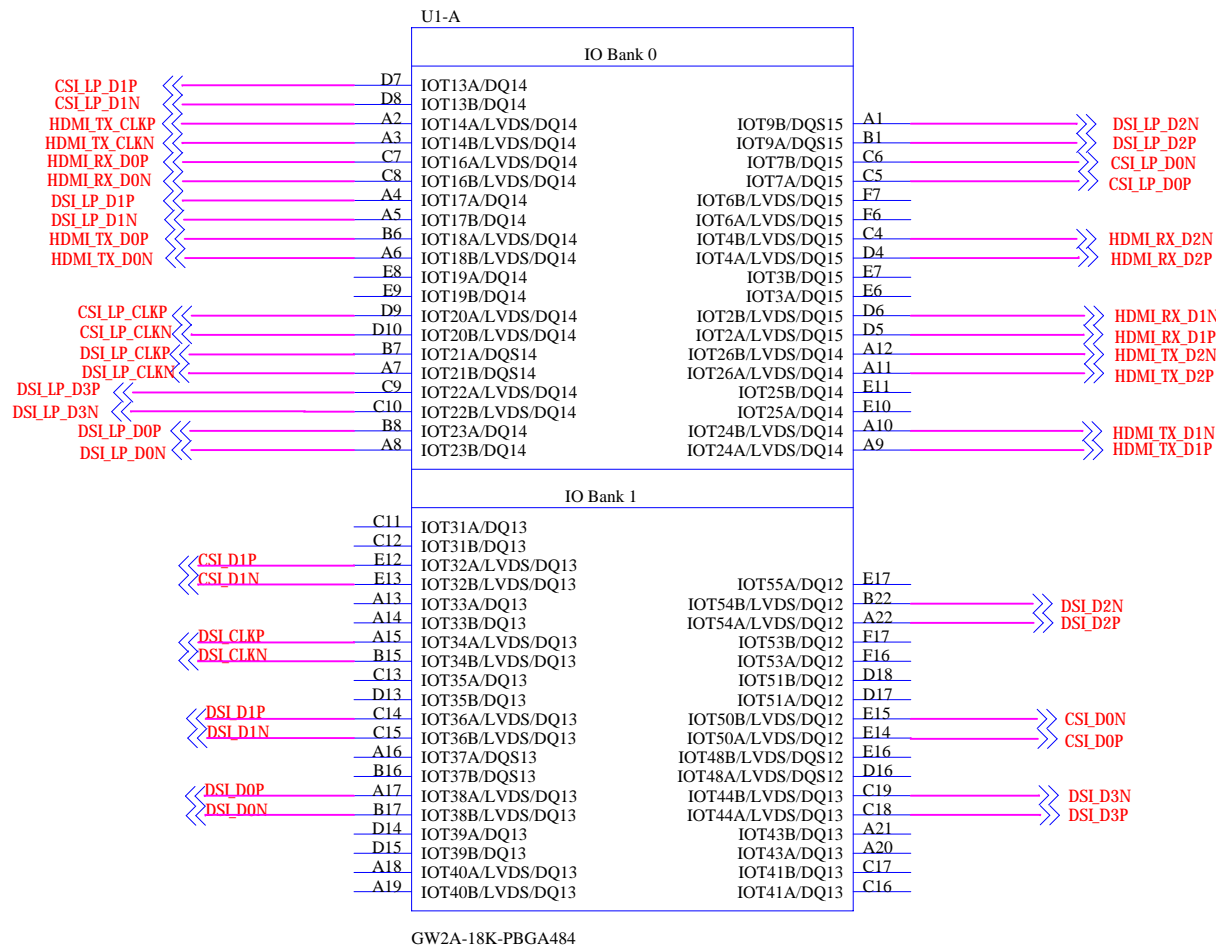


Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size	A3 Sheet: 2/5

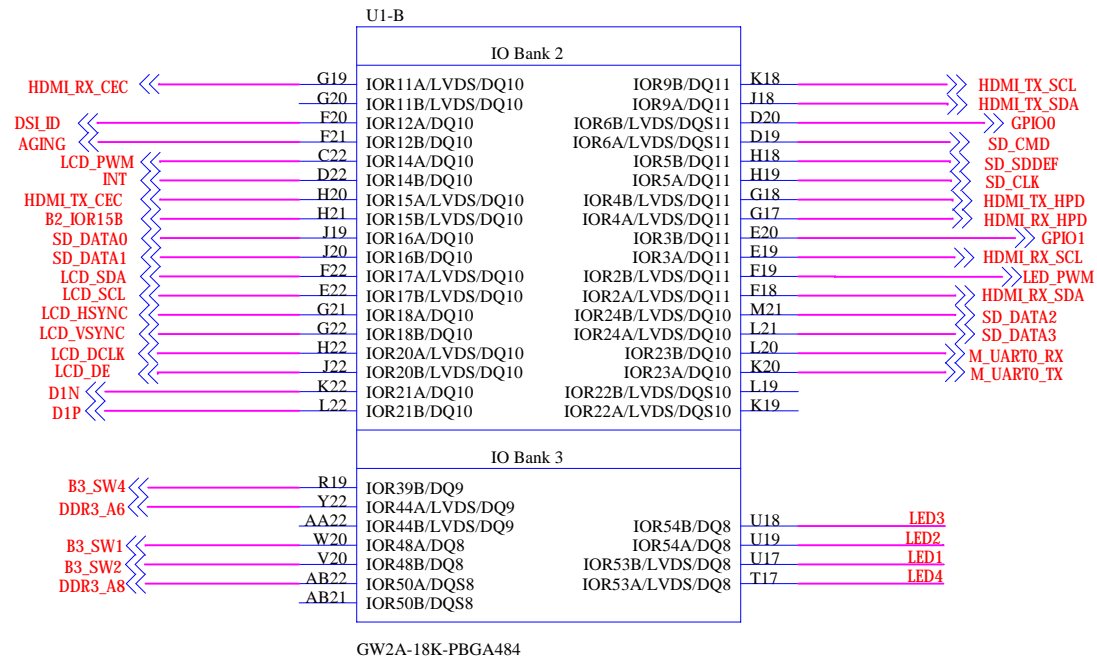


Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5

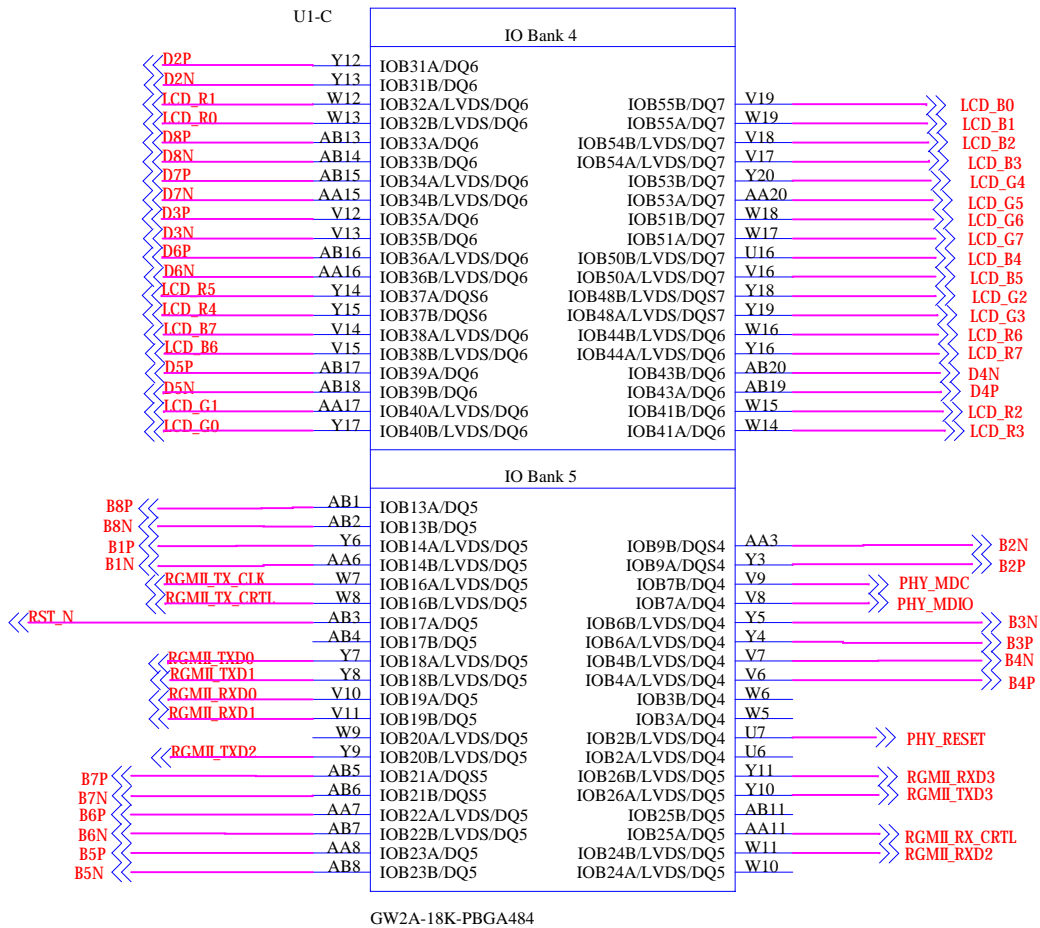
D



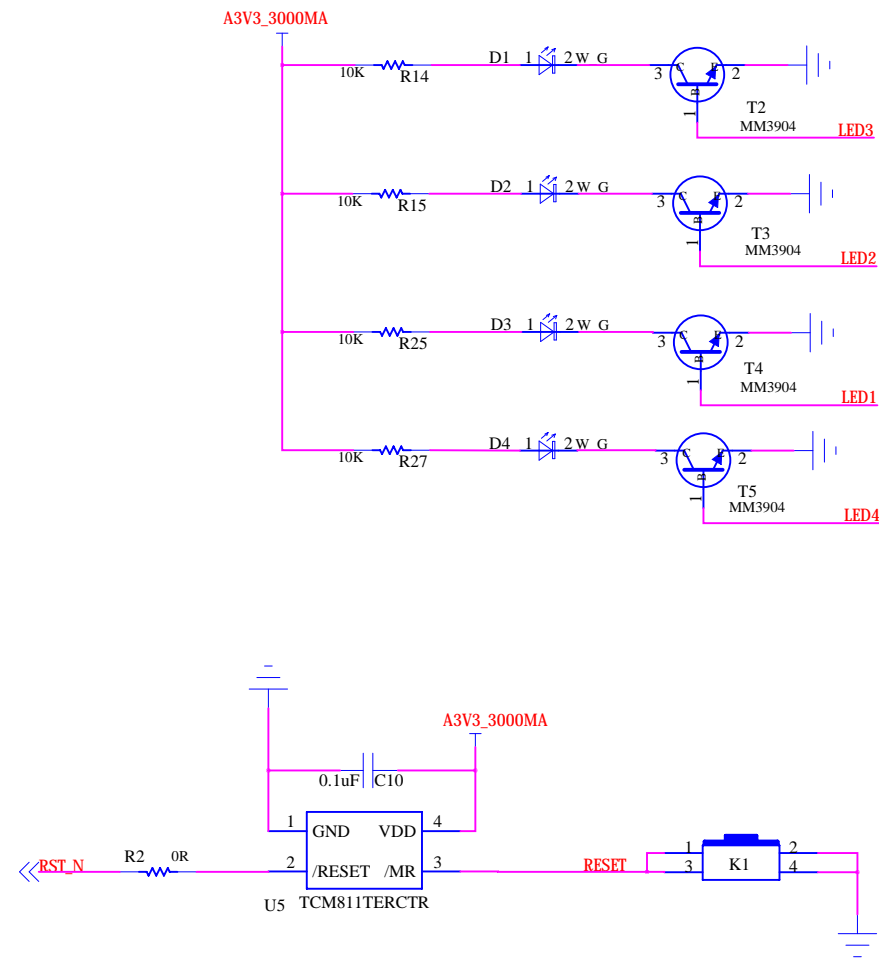
C



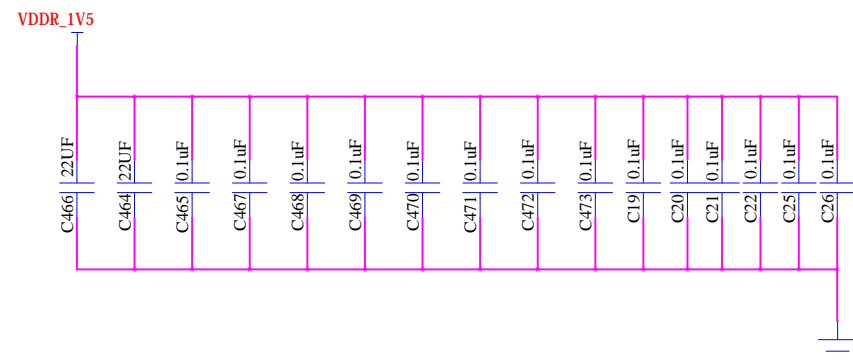
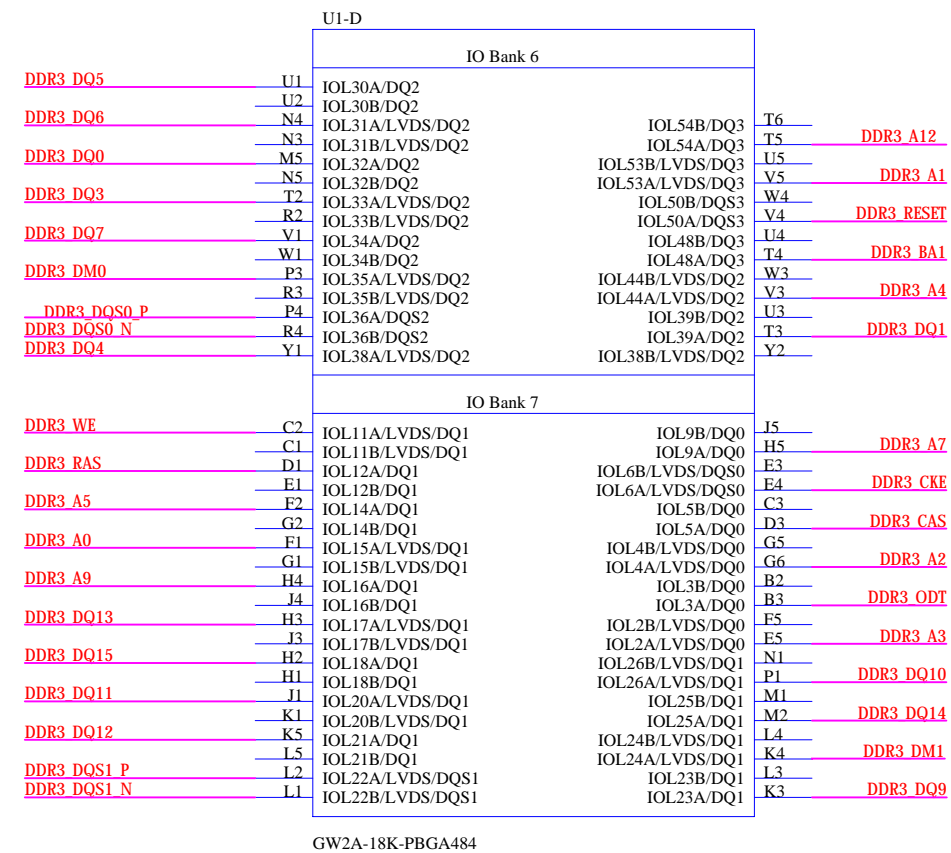
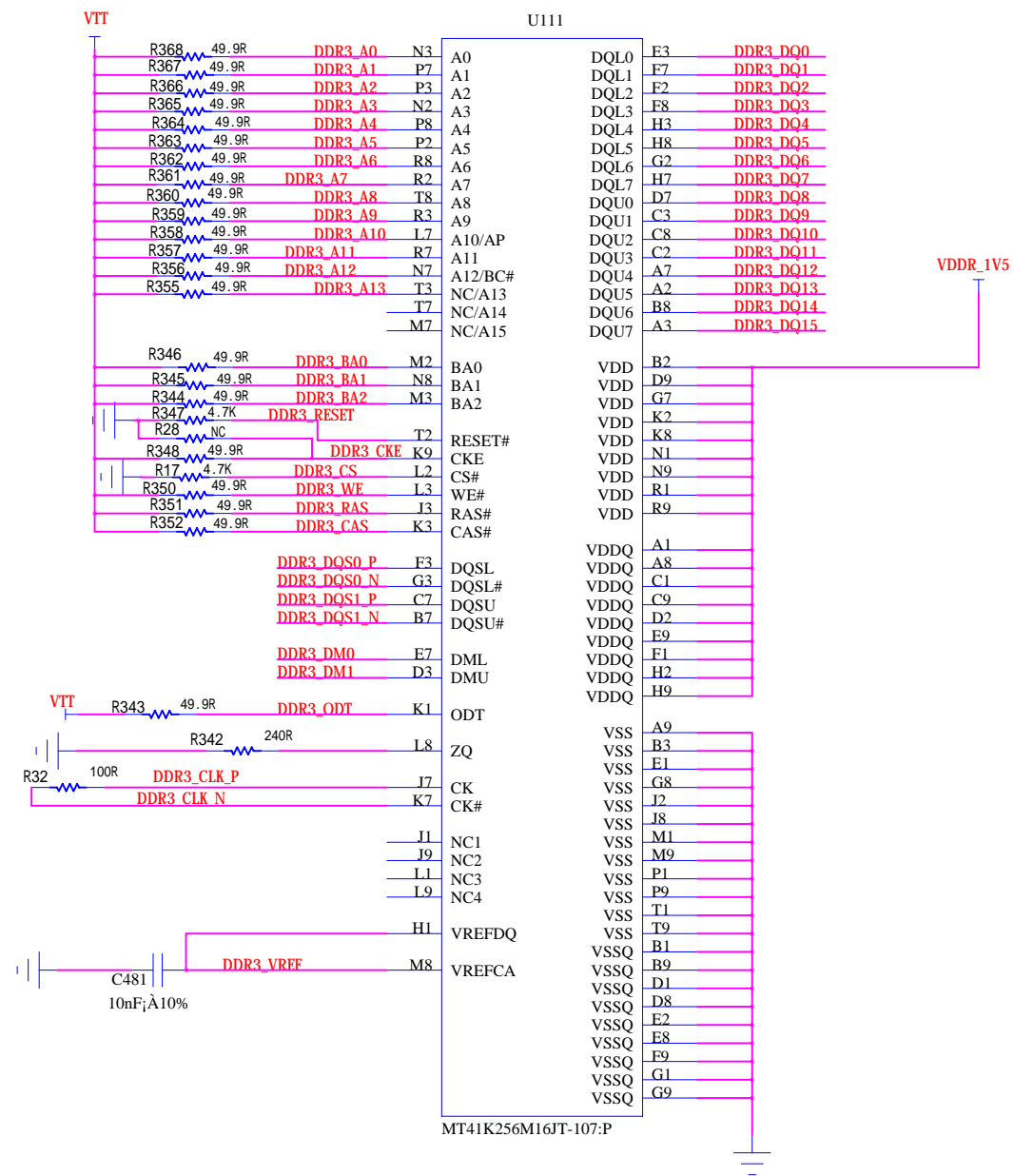
B



A

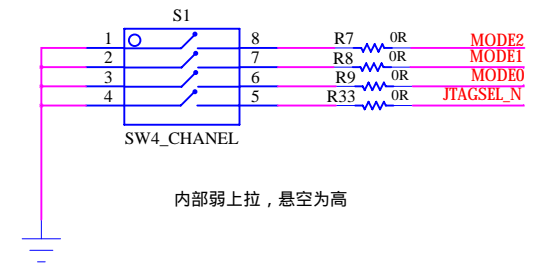
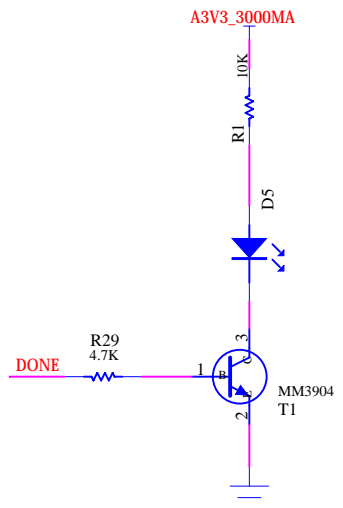
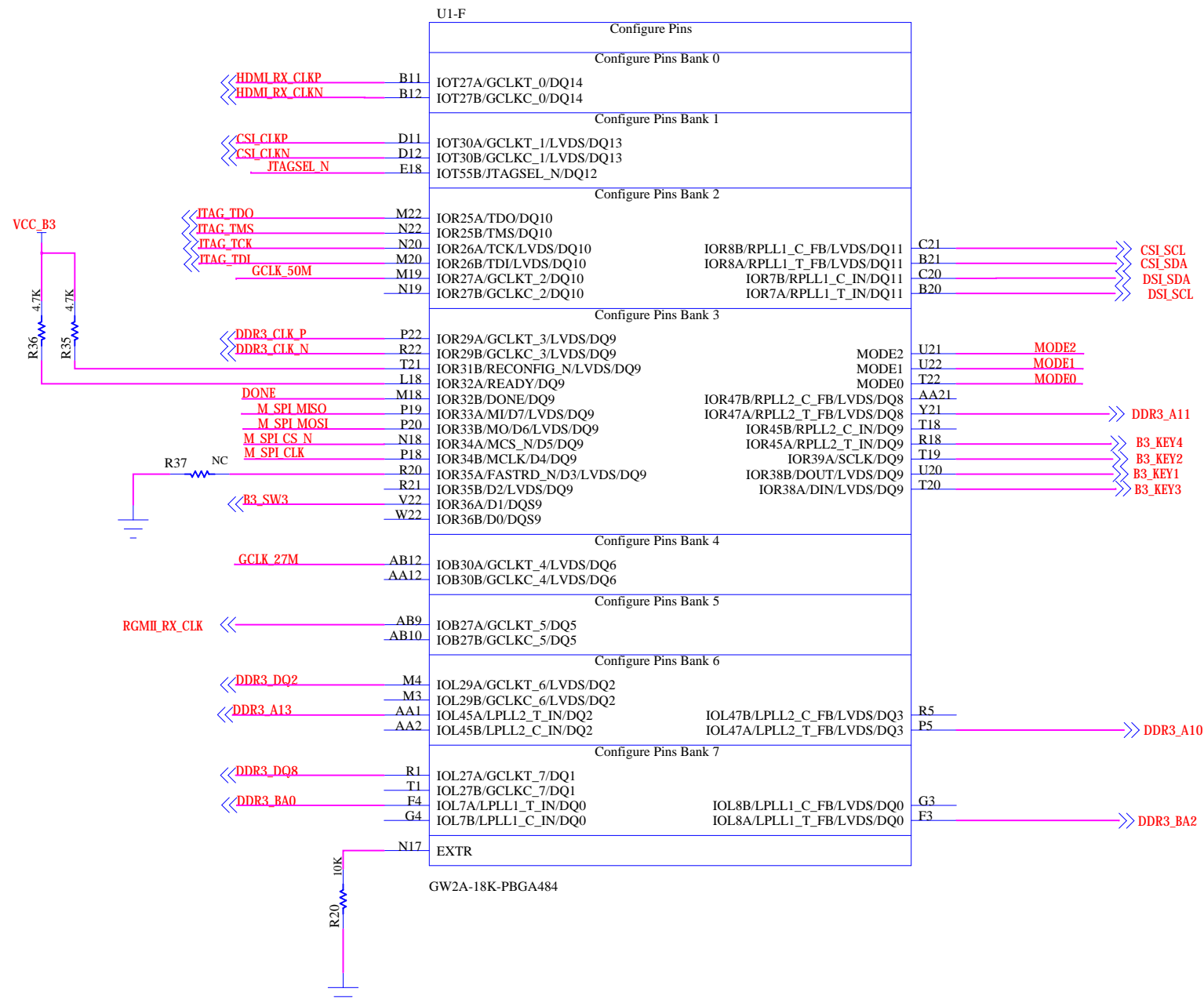
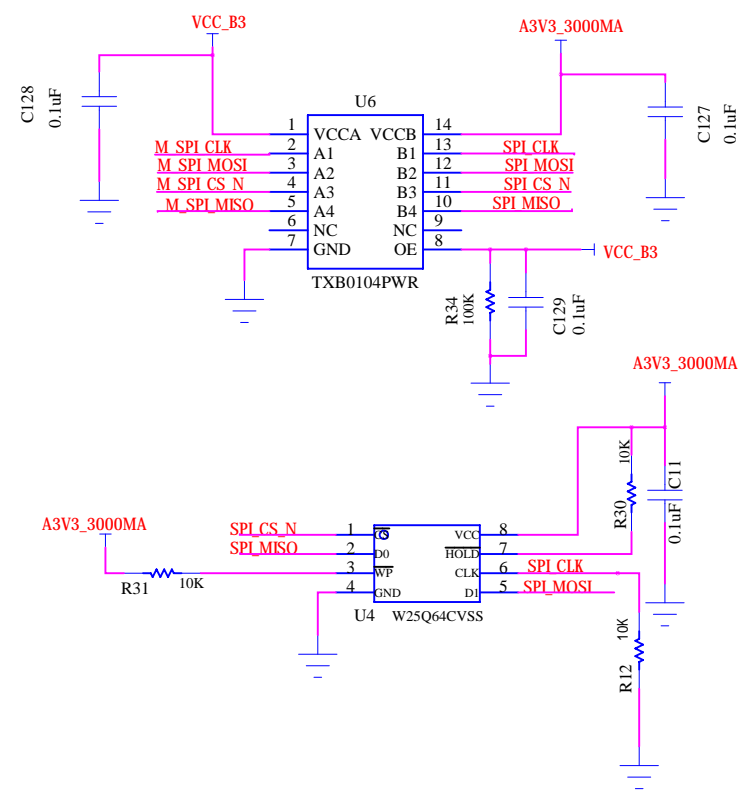
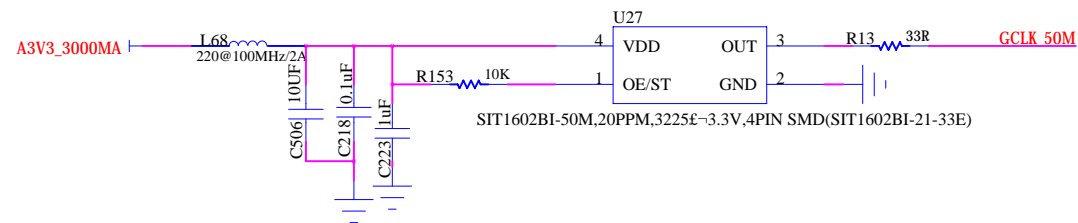
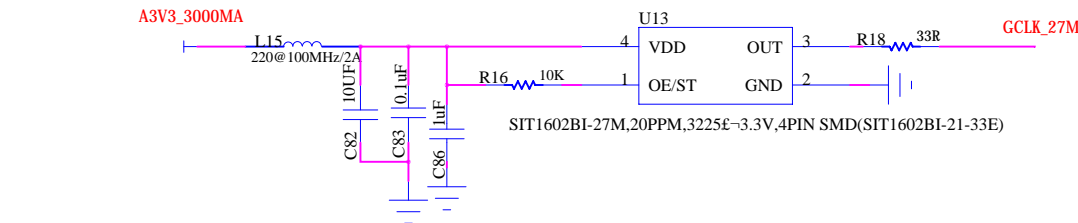


Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5



Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5

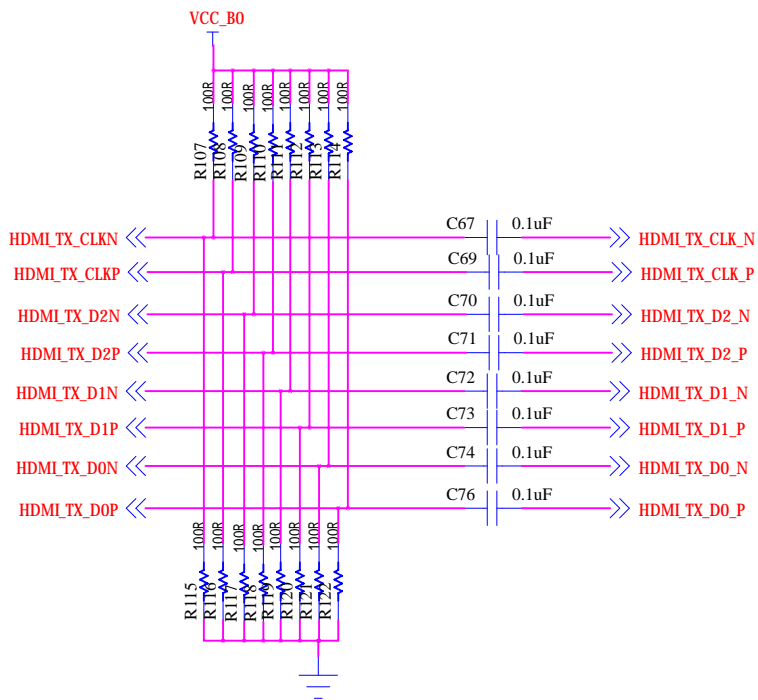
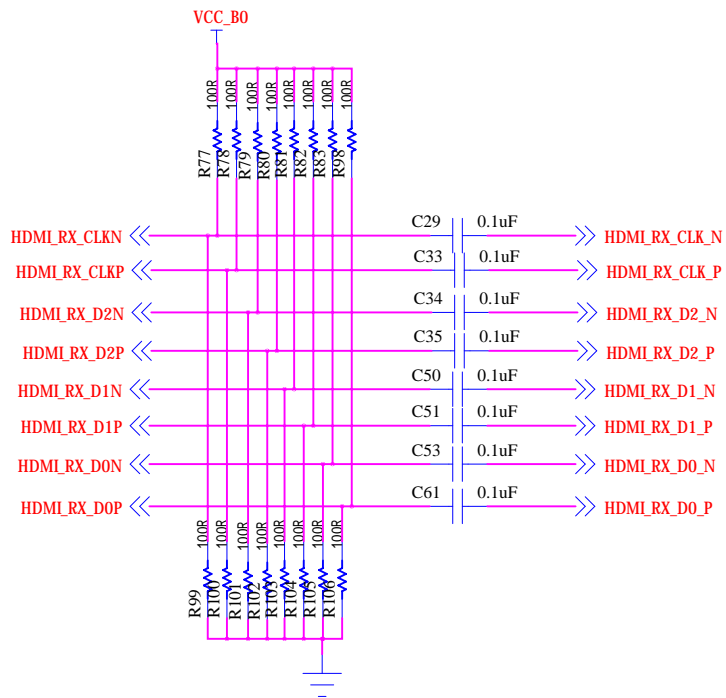
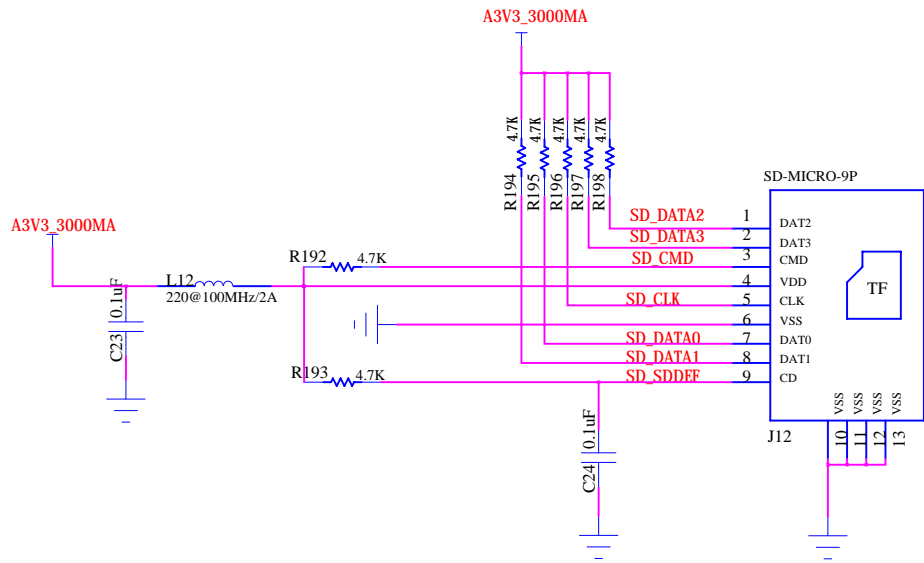




Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5



micro\_SD



Title: MINI\_EYE\_GW2A18\_PG484

Drawing No: Version: V1.0

Drawn By: Date: 20190312

Checked By: Date:

Approved By: Date:

Size A3 Sheet: 2/5



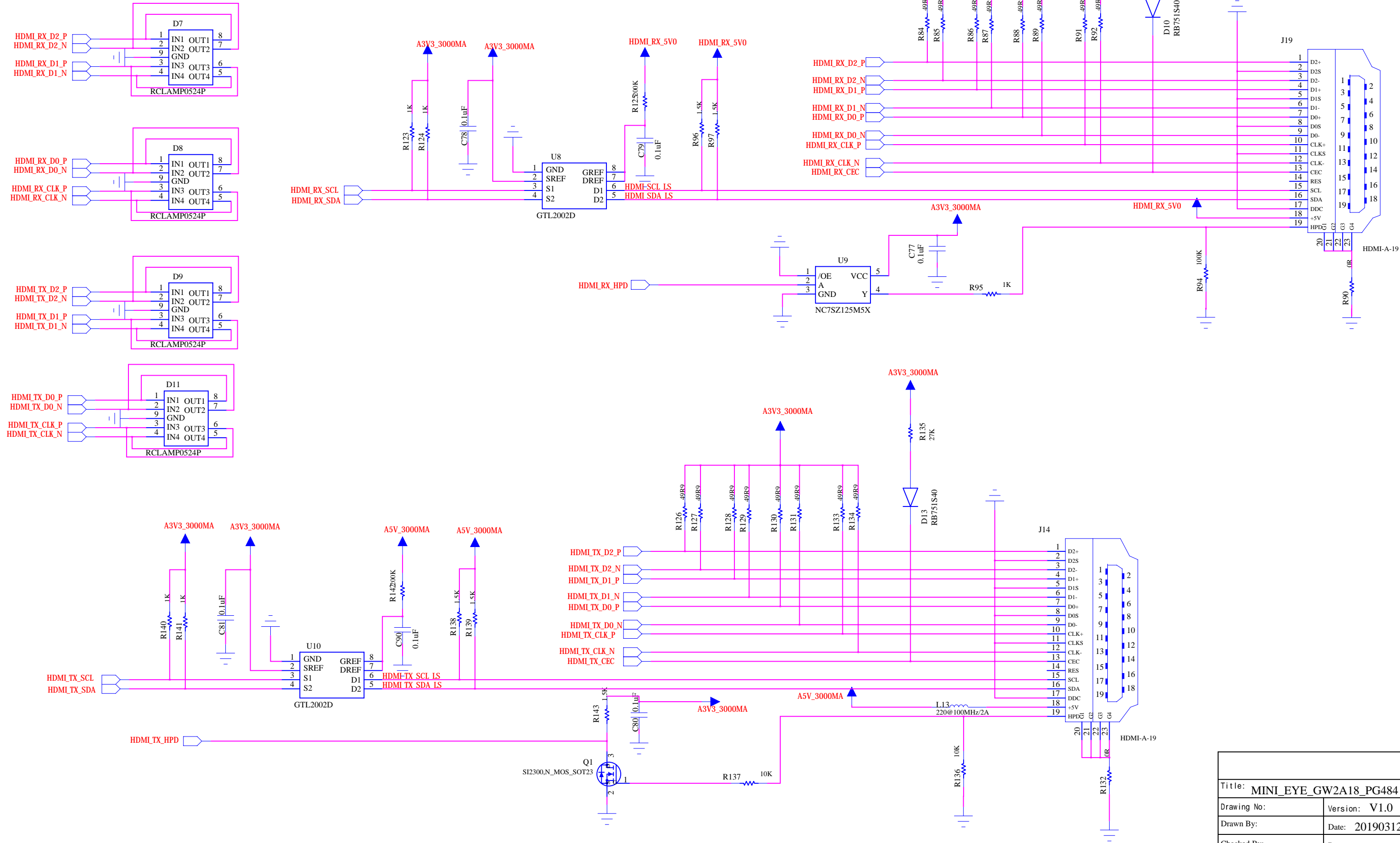
D

C

B

A

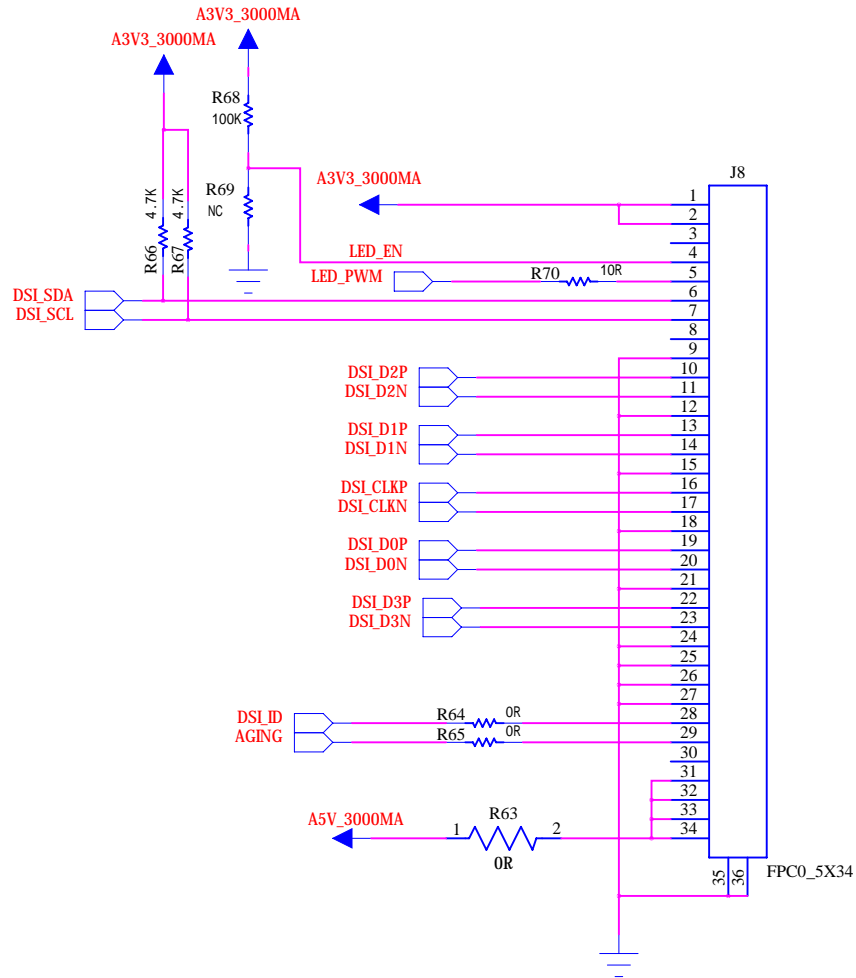
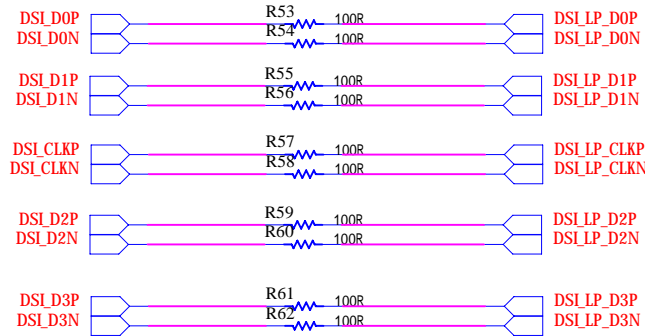
HDMI



Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Page: A2	Page: 2/5

D

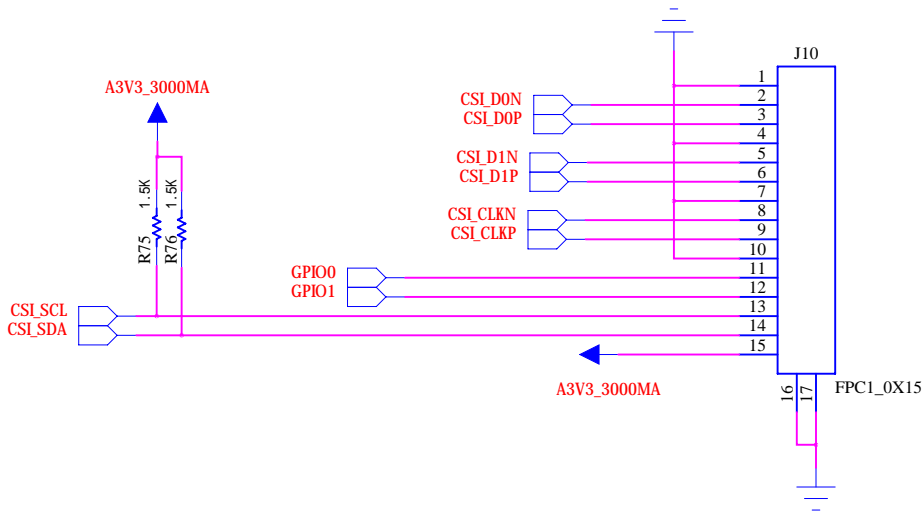
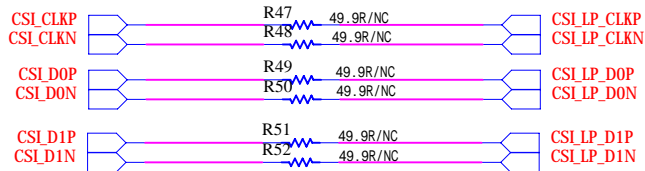
MIPI\_DSI



C

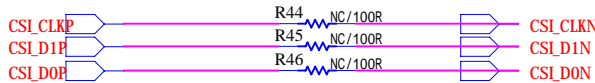
B

MIPI\_CSI



A

CSI端接电阻，靠近FPGA



Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5

6

5

4

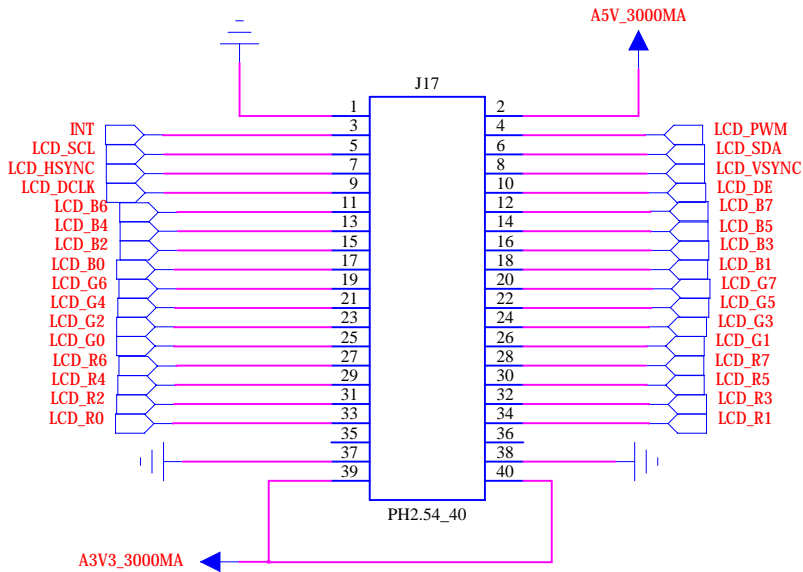
3

2

1

D

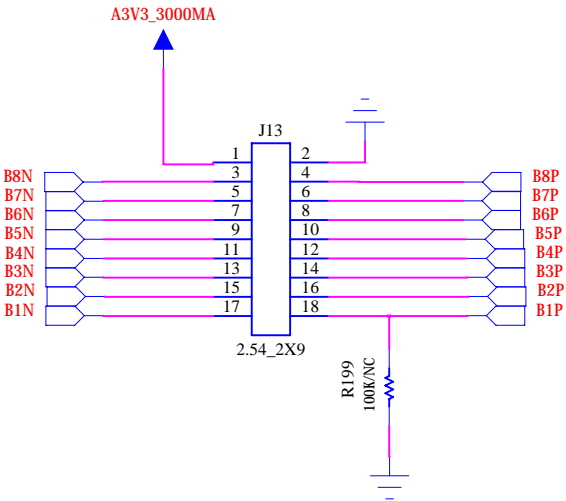
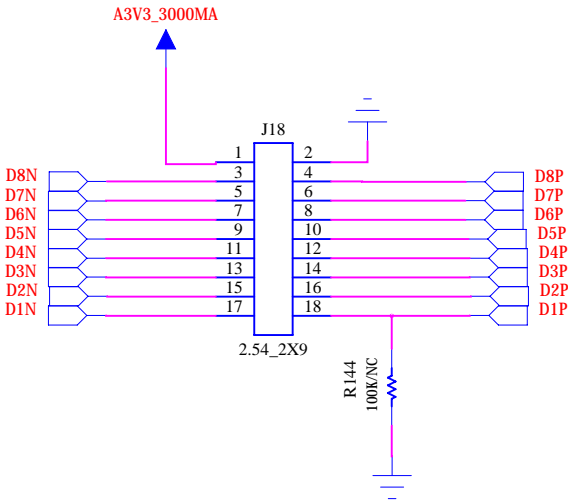
RGB\_LCD



Pin11/12——Pin33/34是12对差分对引脚，单数pin为DP，双数pin为DN，可复用为LVDS\_TX或其他

B

PMOS

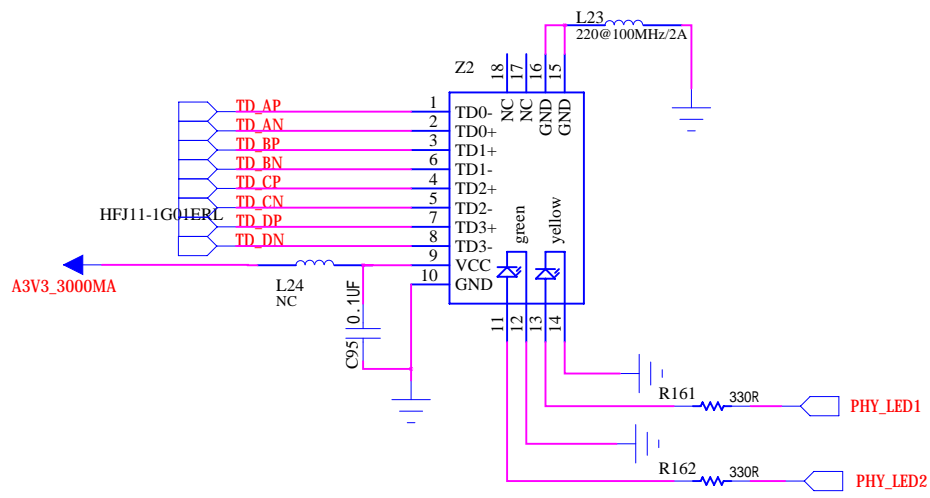


A

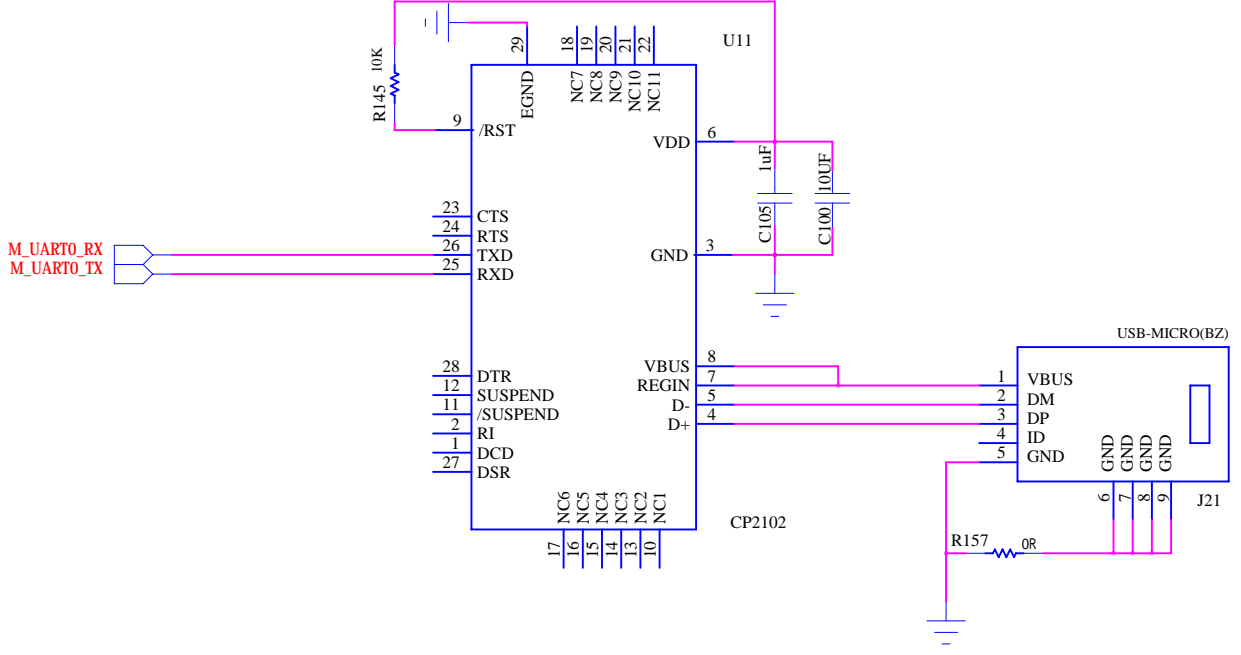
Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size A3	Sheet: 2/5

D

C

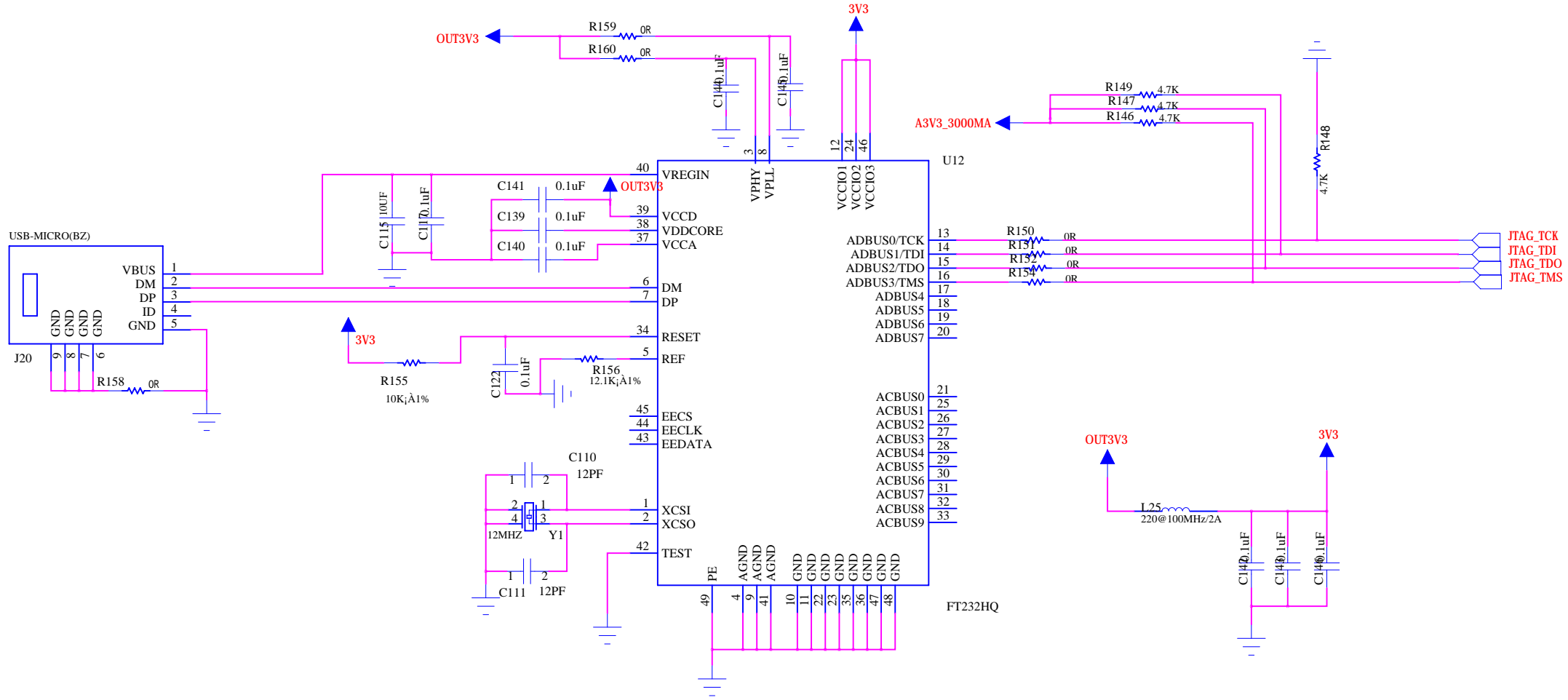


UART-USB



JTAG-USB

B



A

Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size	A3 Sheet: 2/5

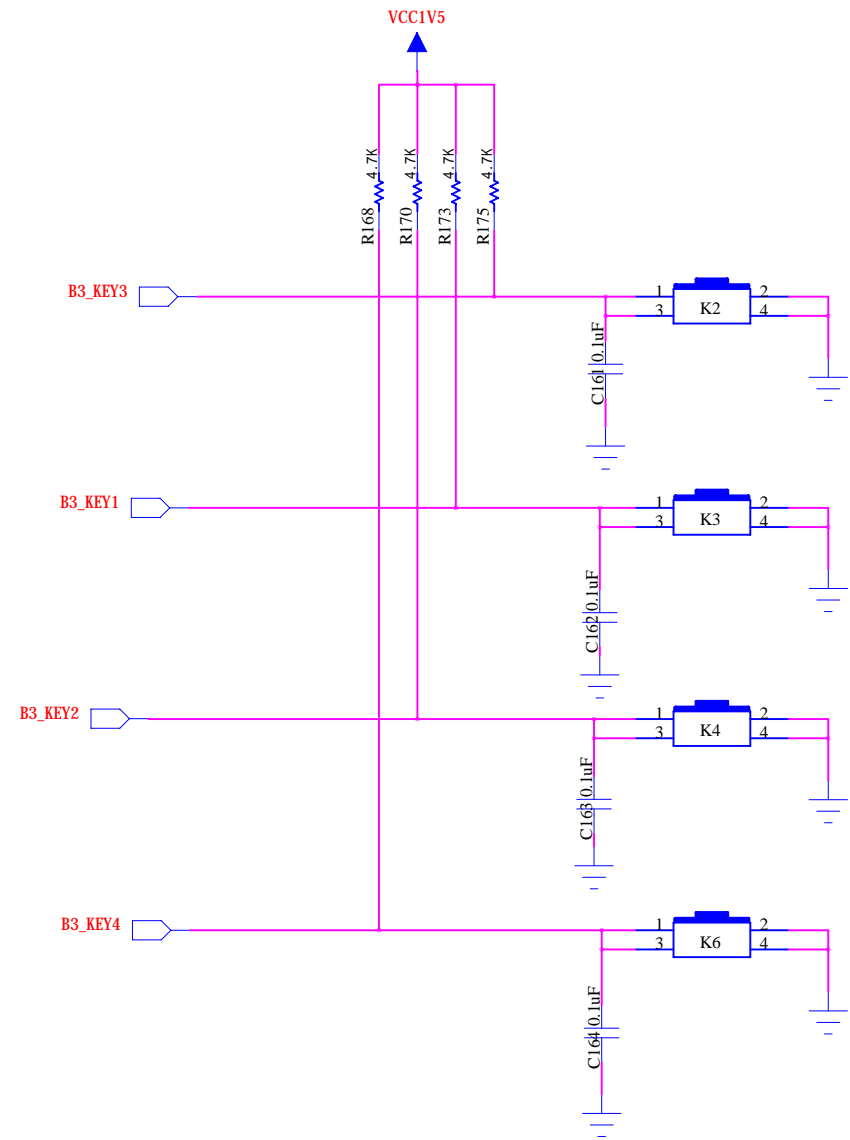
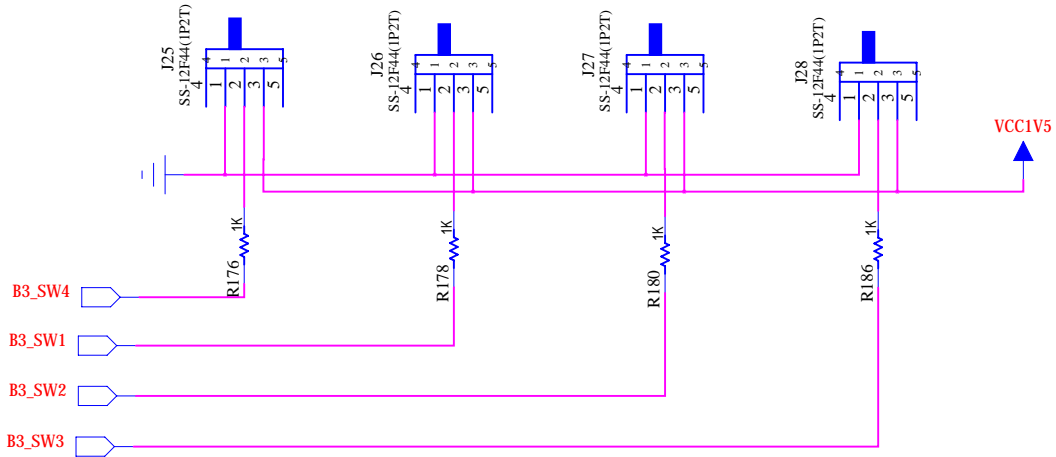
D

C

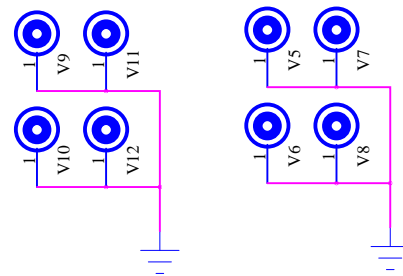
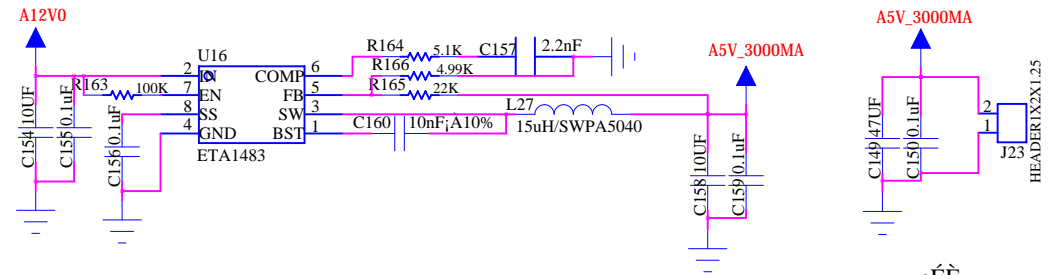
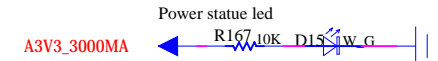
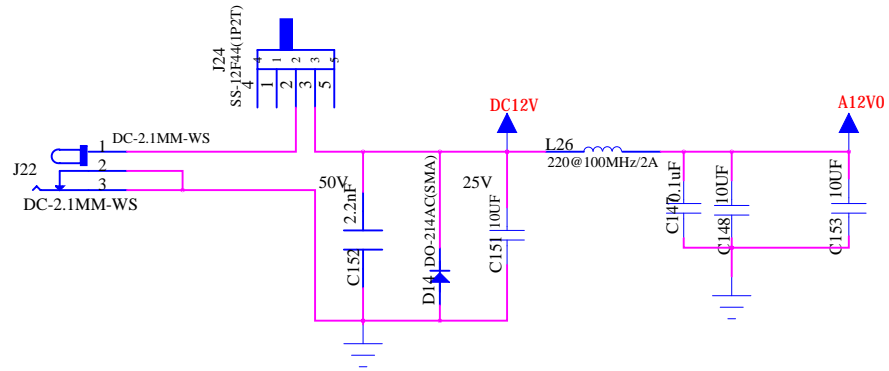
B

A

POWER Connector



12V IN



Title: MINI_EYE_GW2A18_PG484	
Drawing No:	Version: V1.0
Drawn By:	Date: 20190312
Checked By:	Date:
Approved By:	Date:
Size	A3
Sheet:	2/5