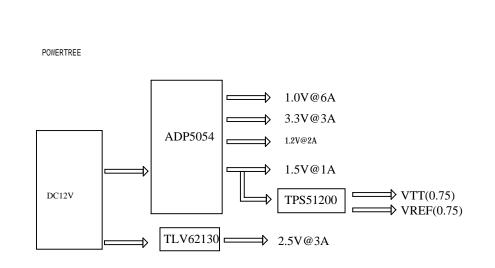
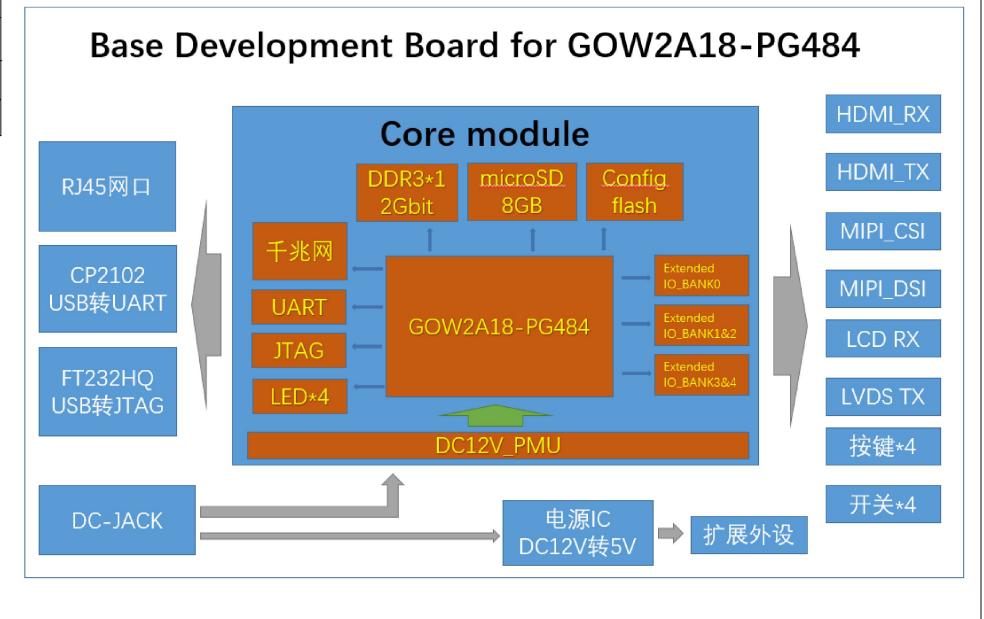
Block Diagram

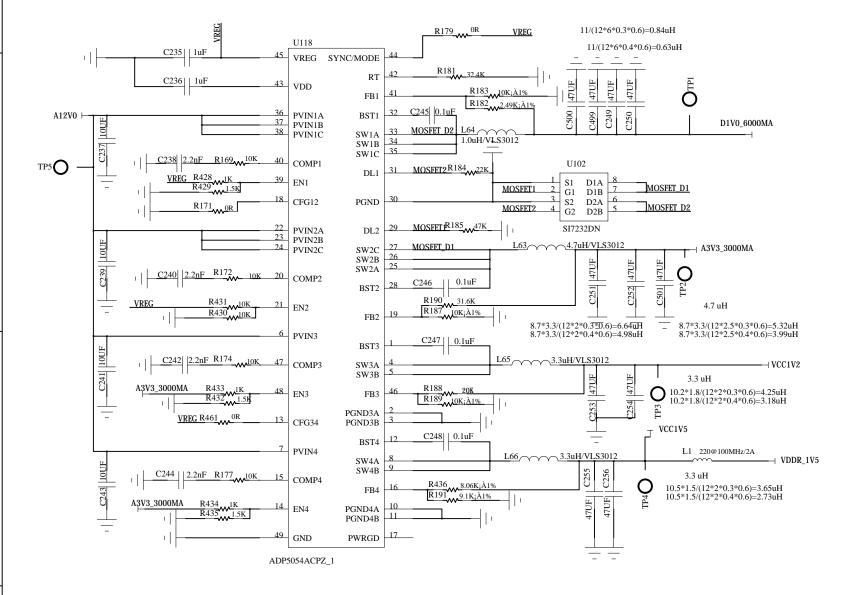
| °æ±¾ | ÈÕÆÚ | ĐÞ¸ÄËμÃ÷ |
|--------|----------|------------|
| REV1.0 | 20200807 | RELESE |
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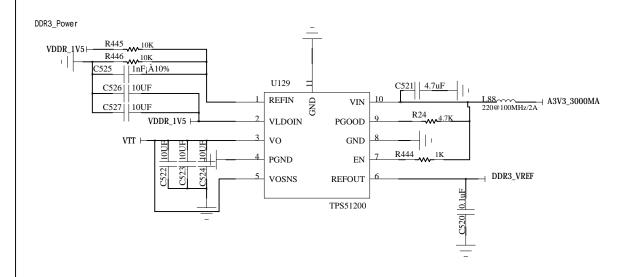
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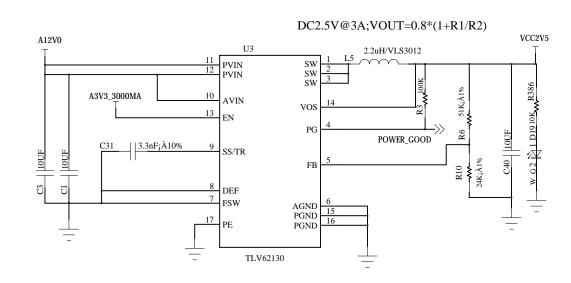




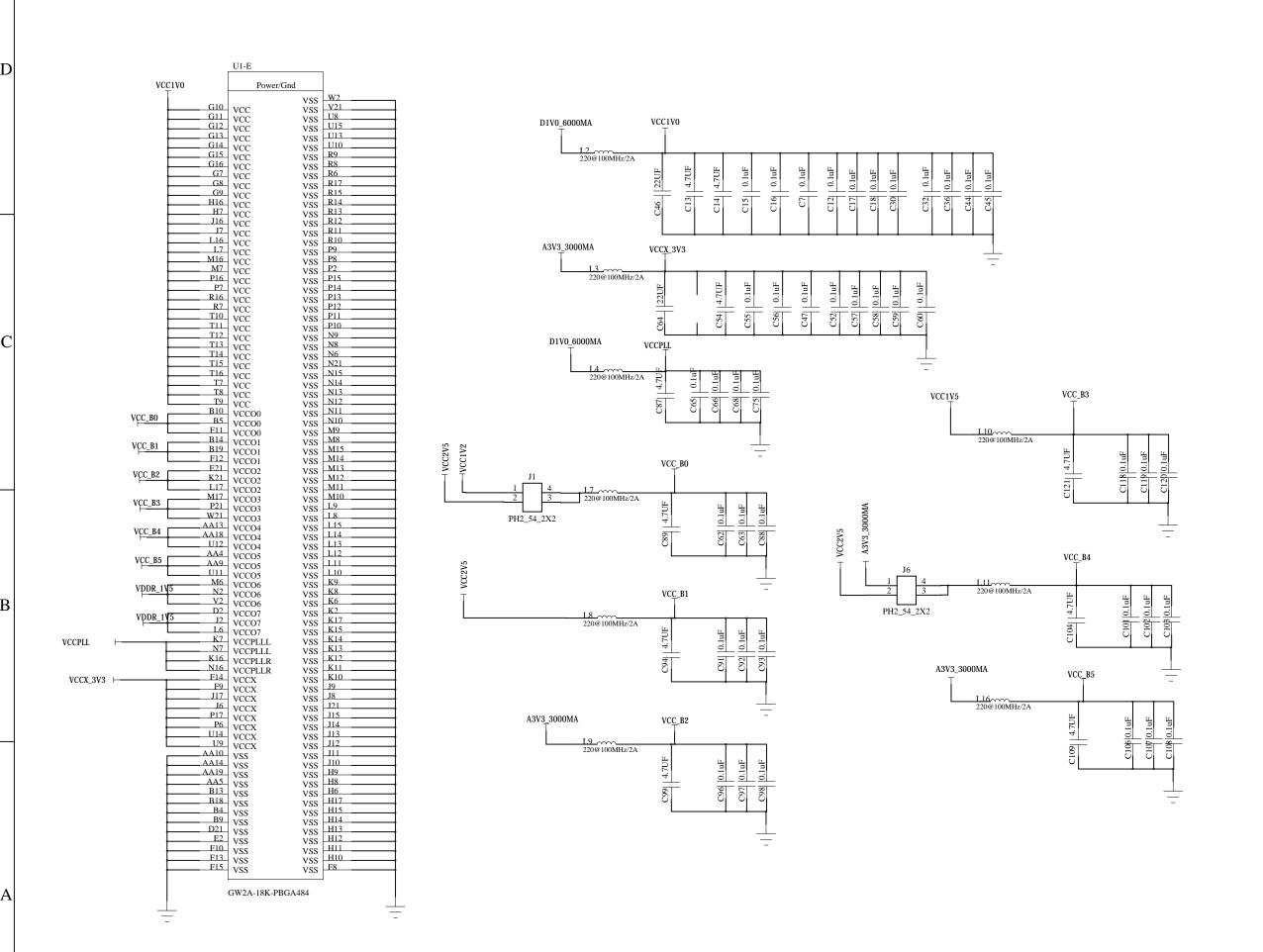


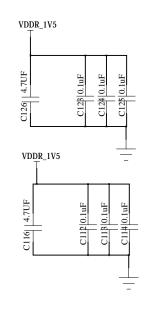
VOUT= Vref; Á (1 + (Rtop/Rbot))



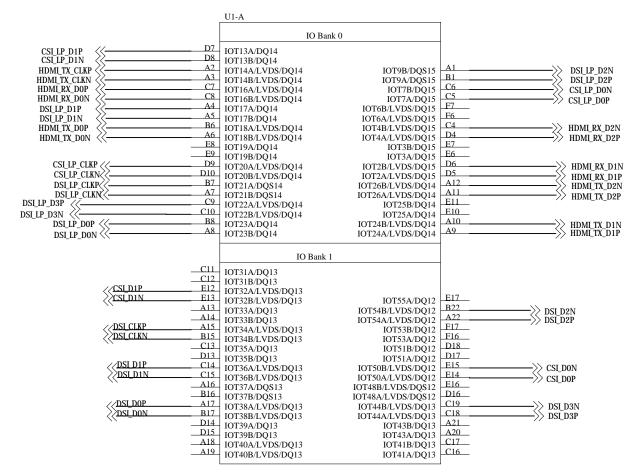


| Title: MINI_EYE_G | W2A18_PG484 |
|-------------------|-----------------|
| Drawing No: | Version: $V1.0$ |
| Drawn By: | Date: 20190312 |
| Checked By: | Date: |
| Approved By: | Date: |
| Size A3 | Sheet: 2/5 |





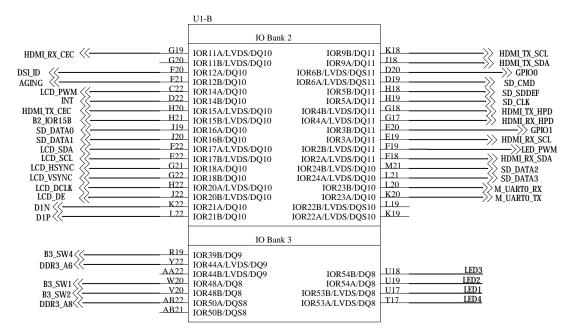
| Title: MINI_EYE_G | W2A18_PG484 | | |
|-------------------|----------------|--|--|
| Drawing No: | Version: V1.0 | | |
| Drawn By: | Date: 20190312 | | |
| Checked By: | Date: | | |
| Approved By: | Date: | | |
| Size A3 | Sheet: 2/5 | | |



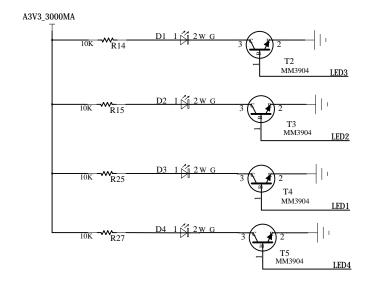
GW2A-18K-PBGA484

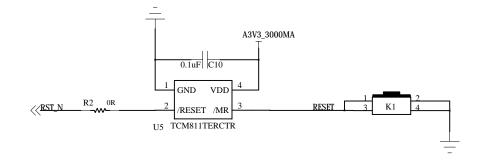
| Y | | | 1 |
|--|--|---|--|
| U1-C | IO B | ank A | |
| //D2P Y | | IIIK 4 | |
| D2N Y | 10B31A/DQ0 | | |
| LCD R1 W | 10B31B/DQ0 | | V19 \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ |
| LCD RO W | 10b32A/LVD3/DQ0 | IOB55B/DQ7 | |
| D8P AB | 10b32b/LVb3/bQ0 | IOB55A/DQ7 | // LCD_b1 |
| DBN AB | , IOBSSA/DQ0 | IOB54B/LVDS/DQ7 | 7717 CLU_B2 |
| D7P AB | _ IOB33B/DQ0 | IOB54A/LVDS/DQ7 | // LCD_D3 |
| D7N AA | _ 10D34A/L V D3/DQ0 | IOB53B/DQ7 | // LUD_G4 |
| \sum_{D3P} V | 10B34B/LVD3/DQ0 | IOB53A/DQ7 | W10 // LUD_G3 |
| D3N V | 1OB33A/DQ0 | IOB51B/DQ7 | |
| D6P AB | IOD33D/DQ0 | IOB51A/DQ7 | |
| D6N AA | IOB30A/LVD3/DQ0 | IOB50B/LVDS/DQ7 | 7/16 LUD_D4 |
| LCD R5 Y | 10D30D/LVD3/DQ0 | IOB50A/LVDS/DQ7 | // LUD_B3 |
| LCD R4 Y | _ IODS/A/DQS0 | IOB48B/LVDS/DQS7 | 7/ LCD_G2 |
| LCD B7 V | , 10b3/b/bQ30 | IOB48A/LVDS/DQS7 | |
| LCD B6 V | IOB38A/LVDS/DQ6 | IOB44B/LVDS/DQ6 | // LCD_K0 |
| D5P AB | TOBS8B/LVDS/DQ8 | IOB44A/LVDS/DQ6 | AD20 |
| D5N AB | o lob33M/DQ0 | IOB43B/DQ6 | / D4N |
| LCD G1 AA | _ 10b39b/bQ0 | IOB43A/DQ6 | W15 |
| >LCD_G0 Y | _ 10D40A/L v D3/DQ0 | IOB41B/DQ6 | // LCD_K2 |
| (\ <u></u> | IOB40B/LVDS/DQ6 | IOB41A/DQ6 | W14 — >>> LCD_R3 |
| | | | |
| | IO P | nde 5 | |
| | IO B | ank 5 | |
| B8P < <af< th=""><td>1 IOB13A/DO5</td><td>ank 5</td><td></td></af<> | 1 IOB13A/DO5 | ank 5 | |
| B8N AF | 1 IOB13A/DQ5 1OB13B/DQ5 | | |
| B8N AF | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 | IOB9B/DQS4 | |
| B8N AF | 1 IOB13A/DQ5 1OB13B/DQ5 1OB14A/LVDS/DQ5 1OB14B/LVDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 | Y3 |
| B8N AF B1P A B1N RGMI_TX_CLK W | 1 IOB13A/DQ5 22 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 | V9 → PHY_MDC B2P |
| B8N | 1 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 | Y3 |
| B8N AAB B1P AA B1N RGMI_TX_CIK W RGMI_TX_CRTI. W ARST_N AB | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1 IOB17A/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 | Y3 |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 6 IOB14B/LVDS/DQ5 8 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1OB17A/DQ5 1OB17A/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 | Y3 |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/L/VDS/DQ5 6 IOB14B/L/VDS/DQ5 7 IOB16A/L/VDS/DQ5 8 IOB16B/L/VDS/DQ5 1 IOB17A/DQ5 1 IOB17B/DQ5 1 IOB17B/DQ5 1 IOB18A/L/VDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 | Y3 |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB14B/LVDS/DQ5 8 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1 IOB17A/DQ5 1 IOB17B/DQ5 1 IOB18A/LVDS/DQ5 8 IOB18B/LVDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB4B/LVDS/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N V6 P4P |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1 IOB17A/DQ5 1 IOB17B/DQ5 1 IOB18A/LVDS/DQ5 1 IOB18B/LVDS/DQ5 1 IOB18B/LVDS/DQ5 1 IOB19A/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB4A/LVDS/DQ4 IOB3B/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N V6 B4P |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 10B17B/DQ5 10B17B/DQ5 10B18A/LVDS/DQ5 10B18B/LVDS/DQ5 10B18B/LVDS/DQ5 10B19A/DQ5 10B19A/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/JQQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB46A/LVDS/DQ4 IOB4A/LVDS/DQ4 IOB4A/LVDS/DQ4 IOB3A/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N W6 B4P |
| RST_N RGMI_TX_CRTI. W RGMI_TX_CRTI. W RGMI_TX_DRTI. W RGMI_TXD0 | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16A/LVDS/DQ5 1 IOB17A/DQ5 4 IOB17A/DQ5 4 IOB17B/DQ5 7 IOB18A/LVDS/DQ5 8 IOB18B/LVDS/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB4A/LVDS/DQ4 IOB3B/DQ4 IOB3A/DQ4 IOB3B/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V6 B4N W6 W5 U7 PHY_RDIO B4N B4N W6 B4P |
| RST_N RGMI_TX_CRTL RGMI_TX_CRTL RGMI_TX_CRTL RGMI_TX_DO RGMI_TXD0 RGMI_TXD0 RGMI_RXD0 RGMI_RXD0 RGMI_RXD1 V RGMI_TXD2 | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB14B/LVDS/DQ5 8 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 9 IOB19A/DQ5 1OB19B/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7A/DQ4 IOB7A/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB3B/DQ4 IOB3B/DQ4 IOB2B/LVDS/DQ4 IOB2B/LVDS/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3P Y4 B3P V6 B4N W6 W5 U17 PHY_RESET |
| RST_N RGMI_TX_CRTI. W RGMI_TX_CRTI. W RGMI_TXD0 RGMI_TXD1 RGMI_RXD0 RGMI_RXD1 RGMI_RXD1 RGMI_RXD1 RGMI_RXD1 RGMI_RXD1 AF | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1 IOB17B/DQ5 1 IOB17B/DQ5 1 IOB18A/LVDS/DQ5 1 IOB18A/LVDS/DQ5 1 IOB19A/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB20A/LVDS/DQ5 1 IOB20B/LVDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7A/DQ4 IOB6B/L/VDS/DQ4 IOB6A/L/VDS/DQ4 IOB4A/L/VDS/DQ4 IOB4A/L/VDS/DQ4 IOB3B/DQ4 IOB3B/DQ4 IOB2B/L/VDS/DQ4 IOB2A/L/VDS/DQ4 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N W6 B4P W5 PHY_RESET U17 PHY_RESET V11 PHY_RESET |
| B8N | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 1 IOB17B/DQ5 1 IOB17B/DQ5 1 IOB18B/LVDS/DQ5 1 IOB18B/LVDS/DQ5 1 IOB19A/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB20B/LVDS/DQ5 1 IOB21A/DQS5 1 IOB21B/DQS5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7B/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB3A/DQ4 IOB3A/DQ4 IOB2B/LVDS/DQ4 IOB26/LVDS/DQ4 IOB26B/LVDS/DQ4 IOB26B/LVDS/DQ5 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N W6 B4P W5 U7 U6 PHY_RESET Y11 RGMII_RXD3 Y10 RGMII_TXD3 |
| RST_N RGMII_TX_CIK RGMII_TX_CRTI. RGMII_TX_CRTI. RGMII_TX_DO RGMII_TXDO RGMII_RXDI RGMII_RXDI RGMII_RXDI RGMII_RXDI RGMII_RXDI AB RGMII_TXDP AB RGMII_TXDP AB RGMII_TXDP AB RGMII_TXDP AB B7P AB B7P AB B7P AB B6P | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14A/LVDS/DQ5 7 IOB16A/LVDS/DQ5 7 IOB16A/LVDS/DQ5 8 IOB16A/LVDS/DQ5 8 IOB16B/LVDS/DQ5 9 IOB18A/LVDS/DQ5 1 IOB18A/LVDS/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB20A/LVDS/DQ5 1 IOB21A/DQS5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7B/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB3B/DQ4 IOB3B/DQ4 IOB2B/LVDS/DQ4 IOB26B/LVDS/DQ4 IOB26B/LVDS/DQ5 IOB26B/LVDS/DQ5 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V6 B4N W6 W5 U7 PHY_RESET U6 Y11 Y10 RGMII_RXD3 Y10 RGMII_TXD3 |
| RST_N RGMI_TX_CLK RGMI_TX_CRTI. RGMI_TX_CRTI. RGMI_TXD0 RGMI_TXD1 RGMI_RXD0 V RGMI_RXD0 V RGMI_RXD1 V RGMI_TXD2 AF B7N AF AF B6P B6N AF | 1 | IOB9B/DQS4 IOB9A/DQS4 IOB7A/DQ4 IOB7A/DQ4 IOB6B/L/VDS/DQ4 IOB6A/L/VDS/DQ4 IOB4A/L/VDS/DQ4 IOB4A/L/VDS/DQ4 IOB3B/DQ4 IOB2B/L/VDS/DQ4 IOB2B/L/VDS/DQ4 IOB26B/L/VDS/DQ5 IOB26A/L/VDS/DQ5 IOB26A/L/VDS/DQ5 IOB25A/DQ5 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V7 B4N W6 W5 U7 PHY_RESET U6 Y11 Y10 RGMII_RXD3 AB11 AB11 AA31 RGMII_RX CRTL |
| RST_N RGMII_TX_CIK RGMII_TX_CRTI. RGMII_TX_CRTI. RGMII_TX_DO RGMII_TXDO RGMII_RXDI RGMII_RXDI RGMII_RXDI RGMII_RXDI RGMII_RXDI AB RGMII_TXDP AB RGMII_TXDP AB RGMII_TXDP AB RGMII_TXDP AB B7P AB B7P AB B7P AB B6P | 1 IOB13A/DQ5 2 IOB13B/DQ5 6 IOB14B/LVDS/DQ5 7 IOB16A/LVDS/DQ5 1 IOB16A/LVDS/DQ5 1 IOB16A/LVDS/DQ5 1 IOB17B/DQ5 1 IOB17B/DQ5 1 IOB17B/DQ5 1 IOB19A/DQ5 1 IOB19A/DQ5 1 IOB19A/DQ5 1 IOB19A/DQ5 1 IOB19A/DQ5 1 IOB19B/DQ5 1 IOB19B/DQ5 1 IOB20B/LVDS/DQ5 1 IOB20B/LVDS/DQ5 1 IOB21A/DQS5 1 IOB21A/DQS5 1 IOB22B/LVDS/DQ5 | IOB9B/DQS4 IOB9A/DQS4 IOB7B/DQ4 IOB7B/DQ4 IOB6B/LVDS/DQ4 IOB6A/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB4B/LVDS/DQ4 IOB3B/DQ4 IOB3B/DQ4 IOB2B/LVDS/DQ4 IOB26B/LVDS/DQ4 IOB26B/LVDS/DQ5 IOB26B/LVDS/DQ5 | Y3 B2P V9 PHY_MDC V8 PHY_MDIO Y5 B3N Y4 B3P V6 B4N W6 W5 U7 PHY_RESET U6 Y11 Y10 RGMII_RXD3 Y10 RGMII_TXD3 |

GW2A-18K-PBGA484

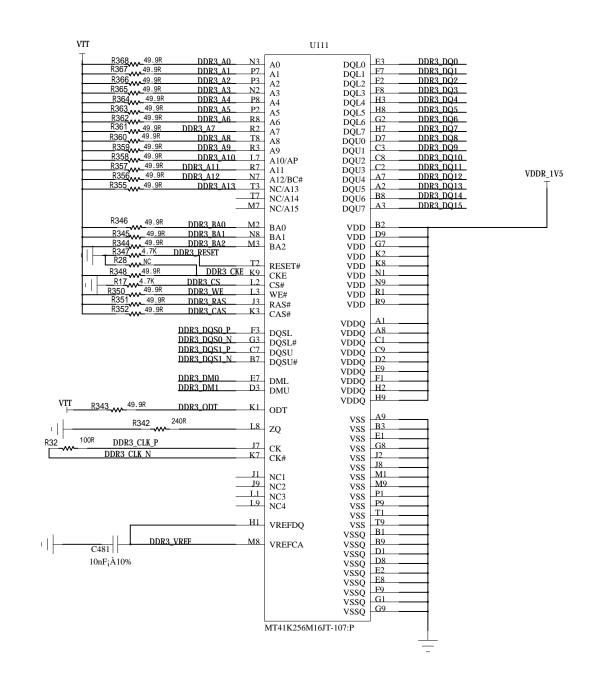


GW2A-18K-PBGA484





| Title: MINI_EYE_G | W2A18_PG484 |
|-------------------|----------------|
| Drawing No: | Version: V1.0 |
| Drawn By: | Date: 20190312 |
| Checked By: | Date: |
| Approved By: | Date: |
| Size A3 | Sheet: 2/5 |



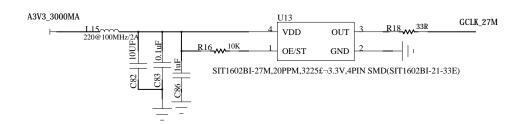
| | , | U1-D | | 1 | |
|----------------------------|-----------|------------------------------------|------------------------------------|-----------|------------|
| | | IO Bank | 6 | | |
| DDR3 DQ5 | U1 | IOL30A/DQ2 | | | |
| DDR3 DQ6 | U2N4 | IOL30B/DQ2 | | m.c | |
| מאמת פאמת | N4 N3 | IOL31A/LVDS/DQ2 | IOL54B/DQ3 | T6 T5 | DDR3 A12 |
| DDR3 DQ0 | M5 | IOL31B/LVDS/DQ2 IOL32A/DQ2 | IOL54A/DQ3 IOL53B/LVDS/DQ3 | U5 | DDIIO_III |
| | N5 | IOL32B/DQ2 | IOL53B/LVDS/DQ3 IOL53A/LVDS/DQ3 | V5 | DDR3 A1 |
| DDR3_DQ3 | T2 | IOL33A/LVDS/DQ2 | IOL50B/DQS | W4 | |
| DDD9 DO7 | R2 | IOL33B/LVDS/DQ2 | IOL50A/DQS3 | V4 | DDR3_RESET |
| DDR3 DQ7 | V1W1 | IOL34A/DQ2 | IOL48B/DQ3 | _U4 T4 | DDR3 BA1 |
| DDR3 DM0 | | IOL34B/DQ2 | IOL48A/DQ3 | W3 | DDR3 BA1 |
| DDNO DINO | R3 | IOL35A/LVDS/DQ2 IOL35B/LVDS/DQ2 | IOL44B/LVDS/DQ2 IOL44A/LVDS/DQ2 | V3 | DDR3 A4 |
| DDR3 DQS0 P | P4 | IOL36A/DQS2 | IOL44A/LVDS/DQ2 IOL39B/DQ2 | U3 | |
| DDR3 DQS0 N | R4 | IOL36B/DOS2 | IOL39A/DQ2 | T3 | DDR3 DQ1 |
| DDR3 DQ4 | Y1 | IOL38A/LVDS/DQ2 | IOL38B/LVDS/DO2 | Y2 | |
| | | | | | |
| | | IO Bank | 7 | | |
| DDR3_WE | C2 | IOL11A/LVDS/DQ1 | IOL9B/DQ0 | J5 | |
| DDD0 DAC | C1_ | IOL11B/LVDS/DQ1 | IOL9A/DQ0 | H5 | DDR3 A7 |
| DDR3 RAS | D1 | IOL12A/DQ1 | IOL6B/LVDS/DQS0 | E3 E4 | DDR3 CKE |
| DDR3 A5 | E1 F2 | IOL12B/DQ1 | IOL6A/LVDS/DQS0 | C3 | DDR3 CRE |
| DDIO NO | G2 | IOL14A/DQ1 | IOL5B/DQ0 | D3 | DDR3 CAS |
| DDR3 A0 | F1 | IOL14B/DQ1 IOL15A/LVDS/DQ1 | IOL5A/DQ0 IOL4B/LVDS/DQ0 | G5 | |
| | G1 | IOL15B/LVDS/DQ1 | IOL4A/LVDS/DQ0 | G6 | DDR3 A2 |
| DDR3_A9 | H4 | IOL16A/DQ1 | IOL3B/DQ0 | B2 | |
| DDD0 DO10 | J4_ | IOL16B/DQ1 | IOL3A/DQ0 | B3 | DDR3_ODT |
| DDR3 DQ13 | H3 | IOL17A/LVDS/DQ1 | IOL2B/LVDS/DQ0 | _F5 E5 | DDR3 A3 |
| DDR3 DQ15 | J3_ H2 | IOL17B/LVDS/DQ1 | IOL2A/LVDS/DQ0 | NI | DDK2 A3 |
| DDR3 DQ13 | H1 | IOL18A/DQ1 | IOL26B/LVDS/DQ1 | Pl | DDR3 DQ10 |
| DDR3 DQ11 | | IOL18B/DQ1 IOL20A/LVDS/DQ1 | IOL26A/LVDS/DQ1 IOL25B/DQ1 | M1 | |
| | K1 | IOL20B/LVDS/DQ1 | IOL25B/DQ1 IOL25A/DO1 | M2 | DDR3 DQ14 |
| DDR3 DQ12 | K5 | IOL20B/LVDS/DQ1 IOL21A/DQ1 | IOL23A/DQ1 IOL24B/LVDS/DQ1 | I.4 | |
| DDDG DOGL D | L5_ | IOL21B/DQ1 | IOL24A/LVDS/DQ1 | K4 | DDR3 DM1 |
| DDR3 DQS1 P DDR3 DQS1 N | L2 | IOL22A/LVDS/DQS1 | IOL23B/DQ1 | L3 | DDD2 DO2 |
| ארק פאחת P | L1 | IOL22B/LVDS/DQS1 | IOL23A/DQ1 | K3 | DDR3_DQ9 |
| | | | | | |

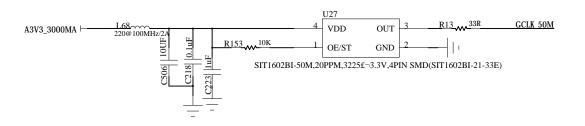
GW2A-18K-PBGA484

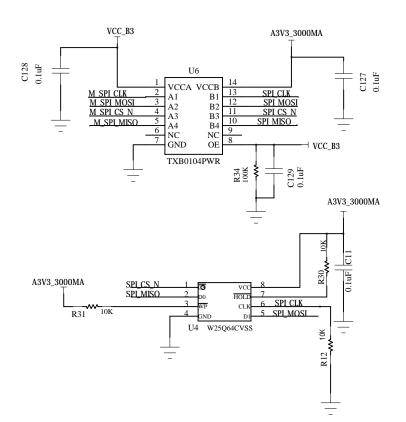
| C466 22UF C464 22UF C465 0.1uF C468 0.1uF C470 0.1uF C470 0.1uF C471 0.1uF C472 0.1uF C20 0.1uF C22 0.1uF C22 0.1uF | DDR_1V5 | | | - | | • | | | 1 | | - | - | 1 | • | • | |
|--|---------|-----|-----|-----|----------|----------|-----|-----|-----|-----|----|----|---|---|---|--------------|
| 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 | | | 0 | | <u> </u> | <u>o</u> | 0 | 0.1 | | 0 | | | | | | _ <u>o</u> _ |
| | C46 | C46 | C46 | C46 | C46 | C46 | C47 | C47 | C47 | C47 | IJ | CZ | 5 | 2 | S | 2 |

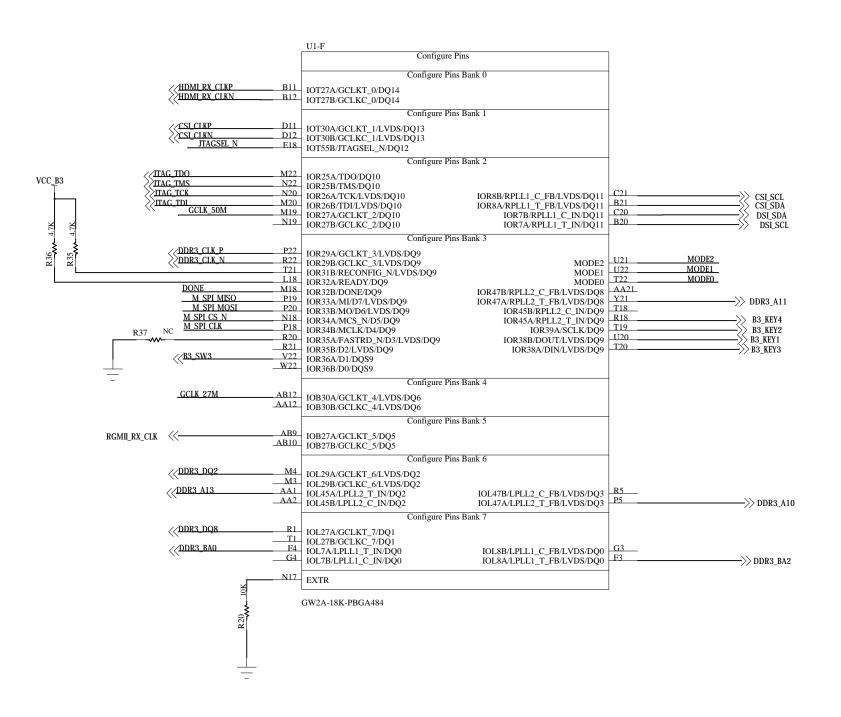
| DDR3_DQ2 | // | DDR3_DQ2 |
|------------|---------------|------------|
| DDR3_DQ2 | * * | DDR3 DQ8 |
| | | DDR3 BA0 |
| DDR3_BA0 | < | DDR3 BA2 |
| DDR3_BA2 | | DDR3 A6 |
| DDR3_A6 | ` ` | |
| DDR3_A8 | << | DDR3 A8 |
| DDR3_A10 | << | DDR3 A10 |
| DDR3_A11 | << | DDR3 A11 |
| DDR3_A13 | << | DDR3 A13 |
| DDR3_CLK_N | << | DDR3_CLK_N |
| DDR3_CLK_P | << | DDR3 CLK P |

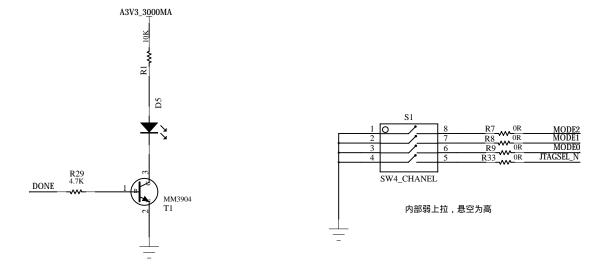
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|-------------------|----------------|--|--|
| Drawing No: | Version: V1.0 | | |
| Drawn By: | Date: 20190312 | | |
| Checked By: | Date: | | |
| Approved By: | Date: | | |
| Size A3 | Sheet: 2/5 | | |

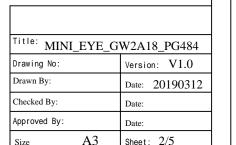


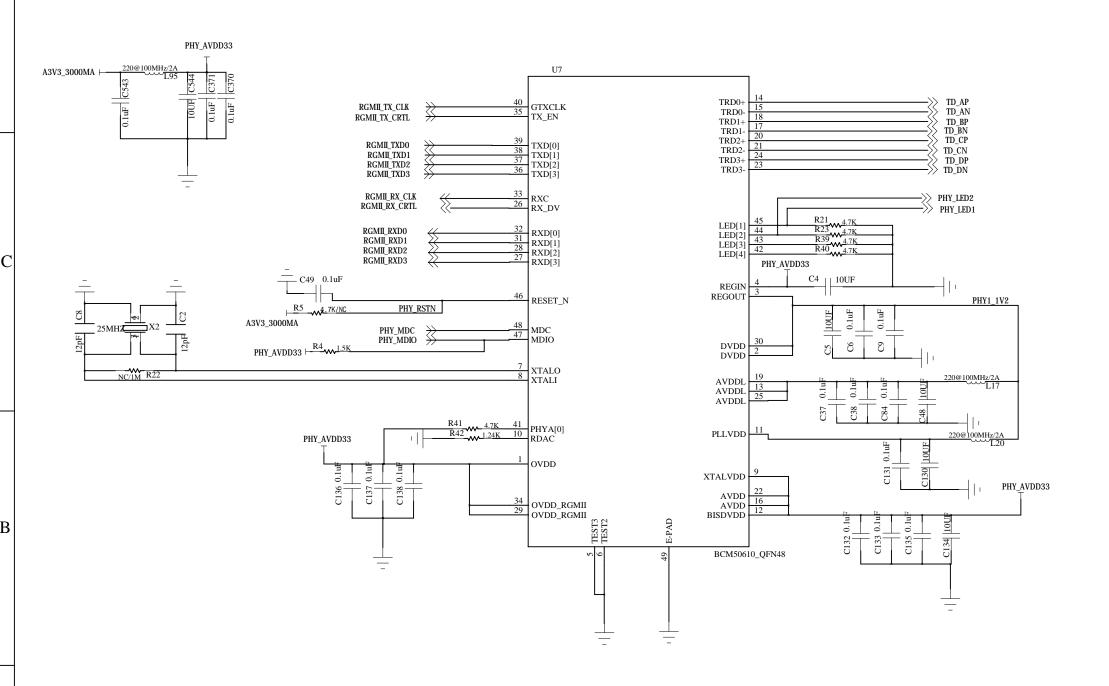


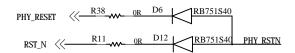






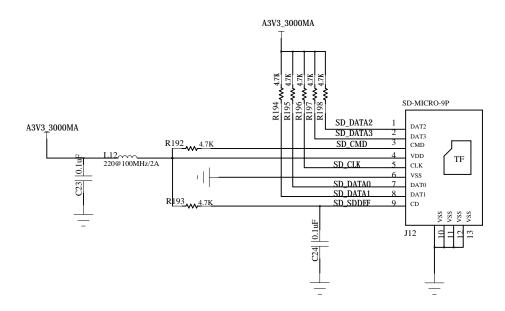


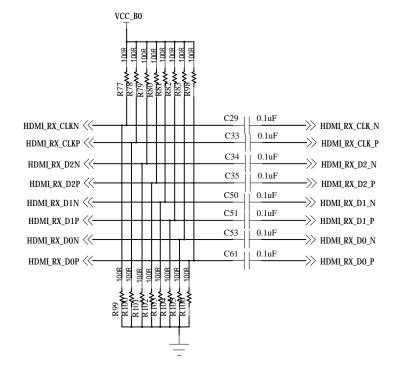


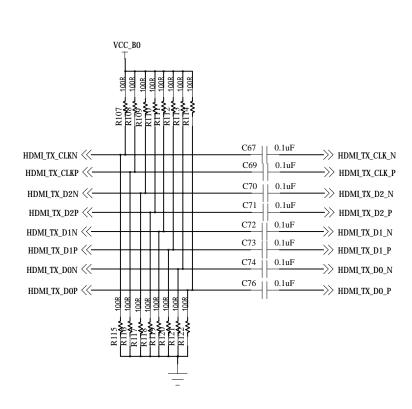


| Title: MINI_EYE_G | W2A18_PG484 |
|-------------------|----------------|
| Drawing No: | Version: V1.0 |
| Drawn By: | Date: 20190312 |
| Checked By: | Date: |
| Approved By: | Date: |
| Size A3 | Sheet: 2/5 |

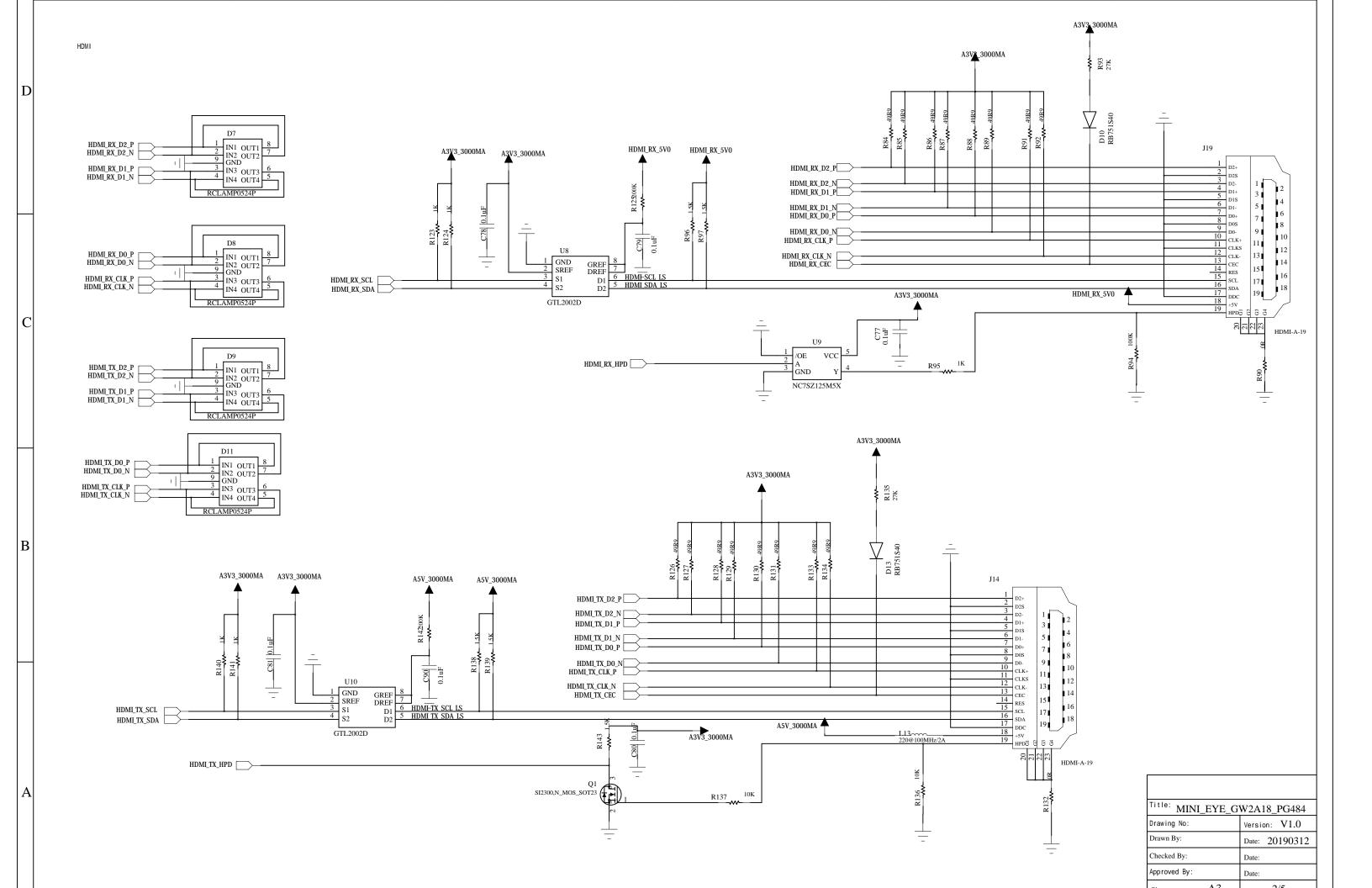
micro_SD

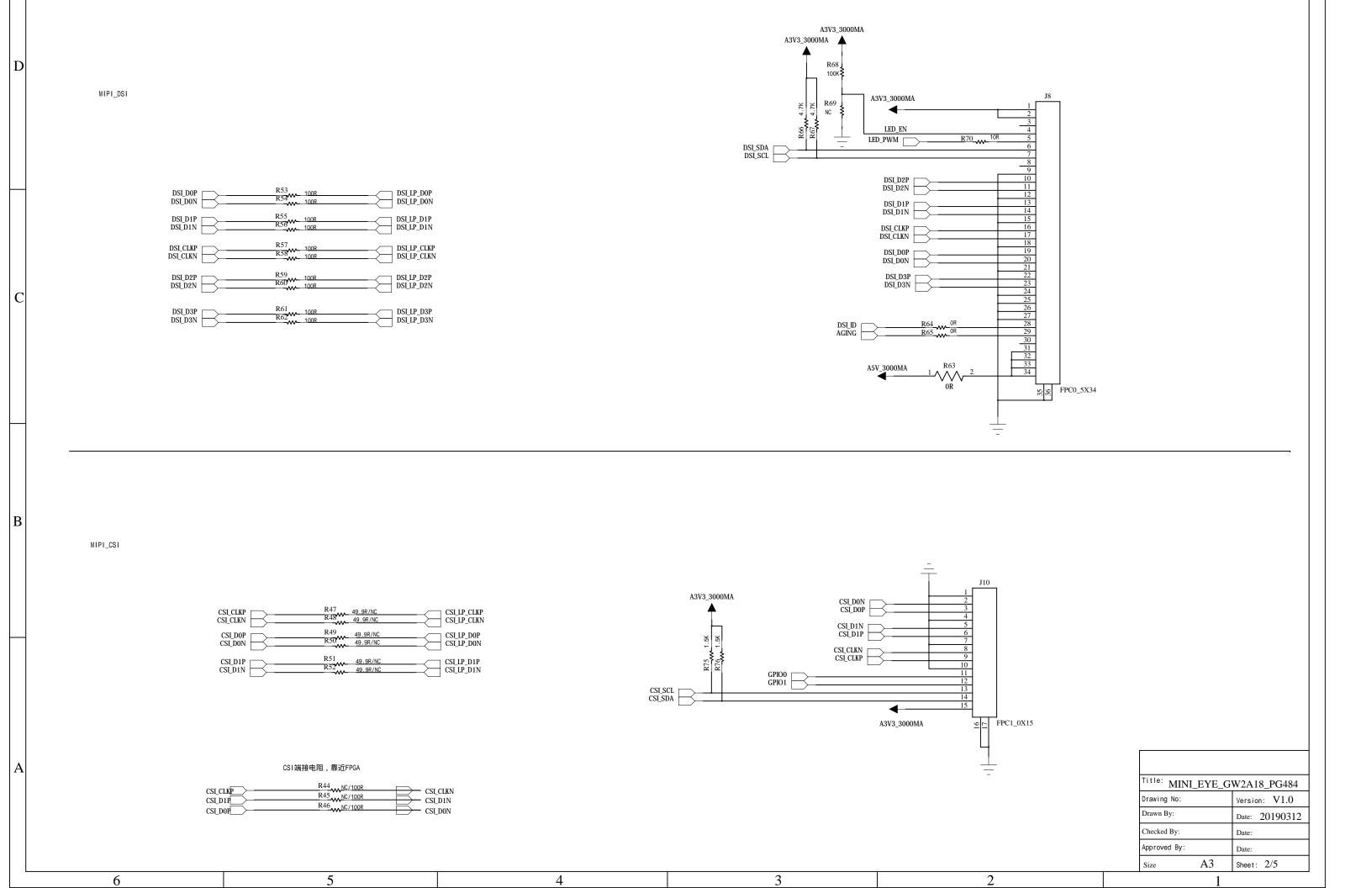


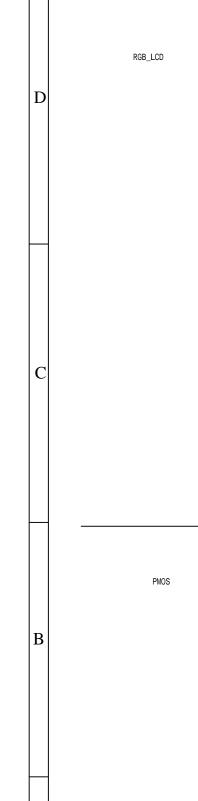


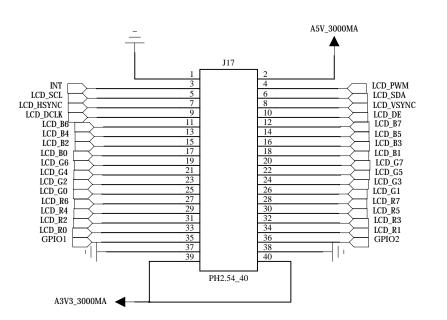


| Title: MINI_EYE_G | W2A18_PG484 | | |
|-------------------|----------------|--|--|
| Drawing No: | Version: V1.0 | | |
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| Checked By: | Date: | | |
| Approved By: | Date: | | |
| Size A3 | Sheet: 2/5 | | |

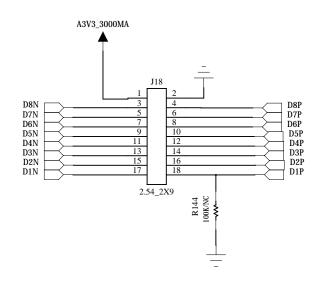


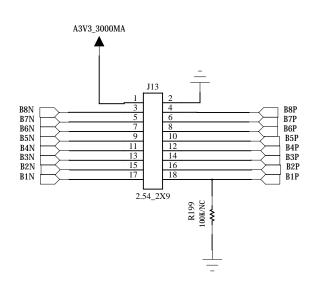






Pin11/12——Pin33/34是12对差分对引脚,单数pin为DP,双数pin为DN,可复用为LVDS_TX或其他





| Title: MINI_EYE_G | W2A18_PG484 | | |
|-------------------|----------------|--|--|
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| Checked By: | Date: | | |
| Approved By: | Date: | | |
| Size A3 | Sheet: 2/5 | | |

