## MAZEN ADEL

Giza 01152794877

> LinkedIn GitHub

I am a highly motivated individual with effective communication skills and a strong work ethic. I am a third-year computer engineering student at Cairo University Faculty Of Engineering Computer Department. I've always been interested in the world of computer engineering and programming.

## **EDUCATION**

**JULY 2026** 

BACHELOR OF COMPUTER ENGINEERING, FACULTY OF ENGINEERING CAIRO UNIVERSITY

I gained knowledge in various fields, such as Software engineering, Problem solving, Basic Hardware designing using Verilog, and Git. I am maintaining a good grade through my academic years, achieving Excellent overall Grade up until the second year.

### TRAINING AND COURSES

**JULY 2023** 

## **INTERNSHIP, ITI "IOT SUMMER TRAINING"**

"I learned a variety of skills while being an intern in the ITI, including web design, Data bases and Using MOT platform (Master Of Things) to Implement real life solutions using IOT (Internet Of Things) technology.

## **SKILLS**

- Proficient in English.
- Proficient in using all of Microsoft programs
- Leadership skills
- Ambitious

- Hardworking
- Clear communicator
- Problem solving skills
- Time Management

### **PROJECTS**

### Draw and Paint Game

Using Object-Oriented Programming (OOP) concepts in C++, my team and I implemented a Draw and Paint game. The game allows users to draw shapes, lines, and text, and to save their creations.

### • Arithmetic Circuit

Using simple AND, OR, and NOT gates, my team and I designed and built an electronic circuit that performs addition, subtraction, multiplication, and division.

### Processor and Process Simulation

Using data structures and algorithms in C++, my team and I created a simulation of processors and processes in a computer. The simulation allows users to experiment with different scheduling algorithms and to see how they affect the performance of the system.

# • AES Encryption Project

Using Verilog (HDL), my team and I created a project that implements the AES (Rijndael) 256, 192, and 128-bit encryption algorithms. We then tested the code on an FPGA (field-programmable gate array).