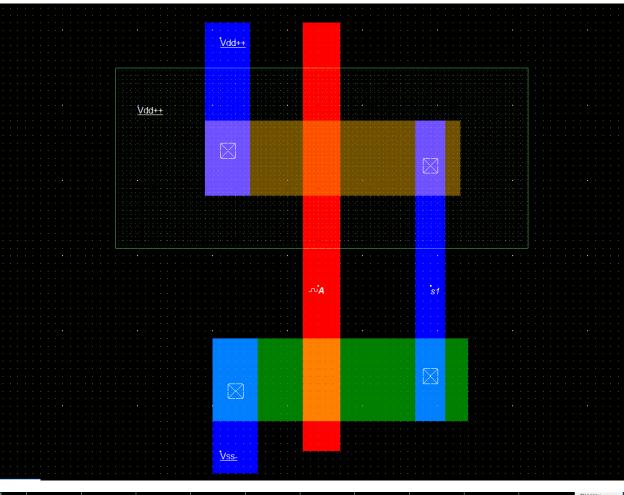
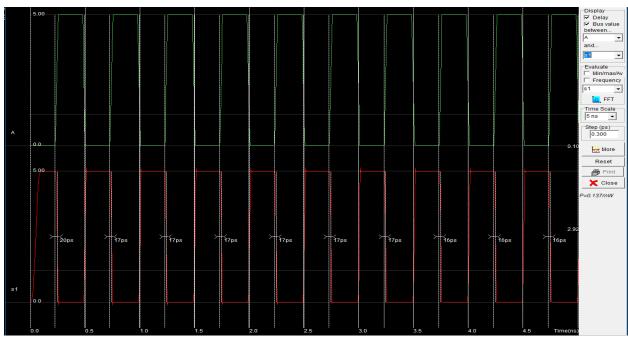
## Inverter





## AND Gate

