

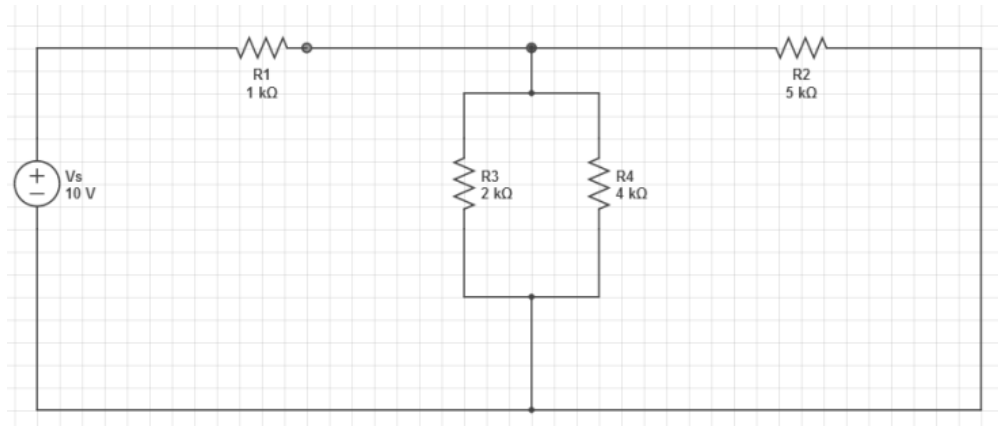
CSE250: Electrical Circuits

(Tuesday 08.00am-11.00am and Wednesday 2.00pm-5.00pm slot)

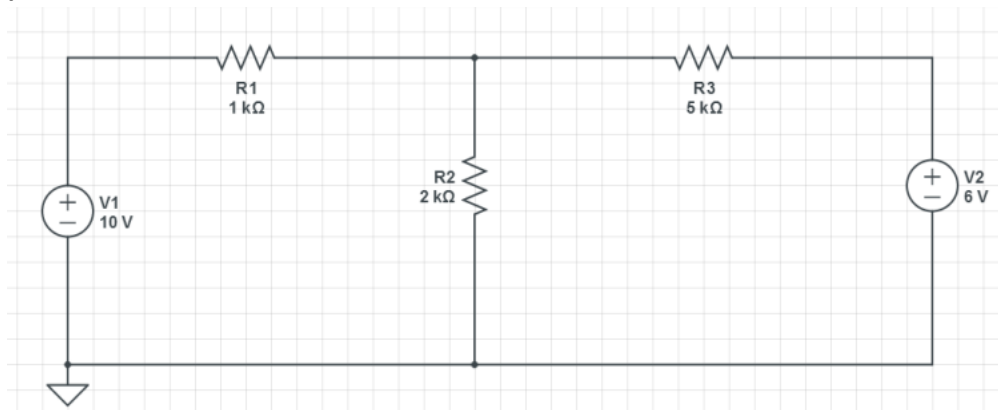
PSPICE ASSIGNMENT

Submission Deadline: 11 March 2019 Monday

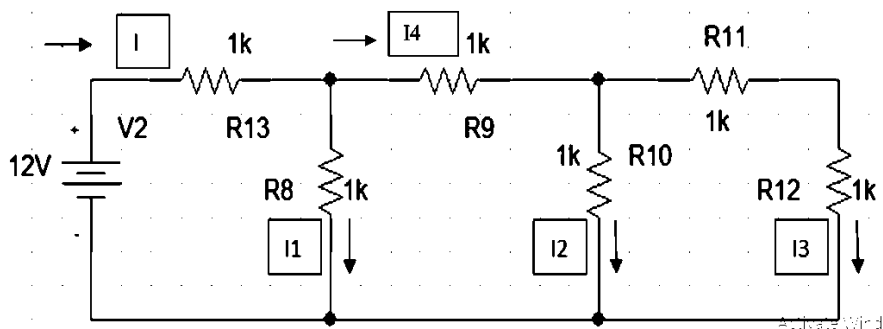
1. Implement the following circuit and verify KVL and KCL



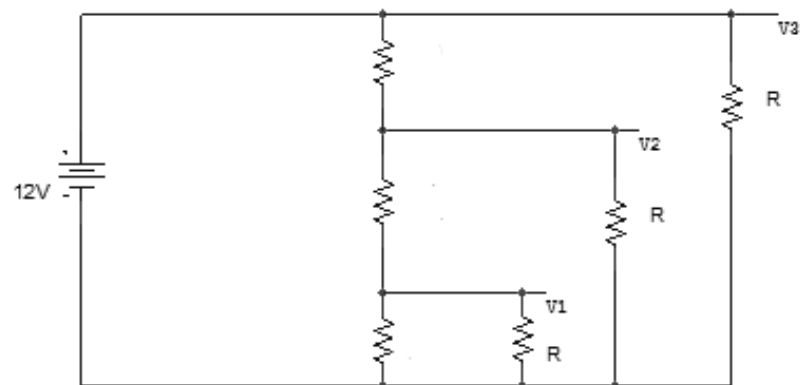
2. Find out the current through the R_2 resistor in the following network. Then use appropriate steps to verify SUPERPOSITION theorem in the network. (find the currents when each source is active, deactivating the other; then perform algebraic sum)



3. Find out all the voltages and Currents through each of the resistor.



4. For the figure given, the value of the series resistances are 220Ω each. Find out V_1 , V_2 , V_3 & I_1 , I_2 , I_3 for $R = 80\Omega$, $1k\Omega$, $100k\Omega$.



5. Using superposition, find the current through R_1 for the network of following Figure:

