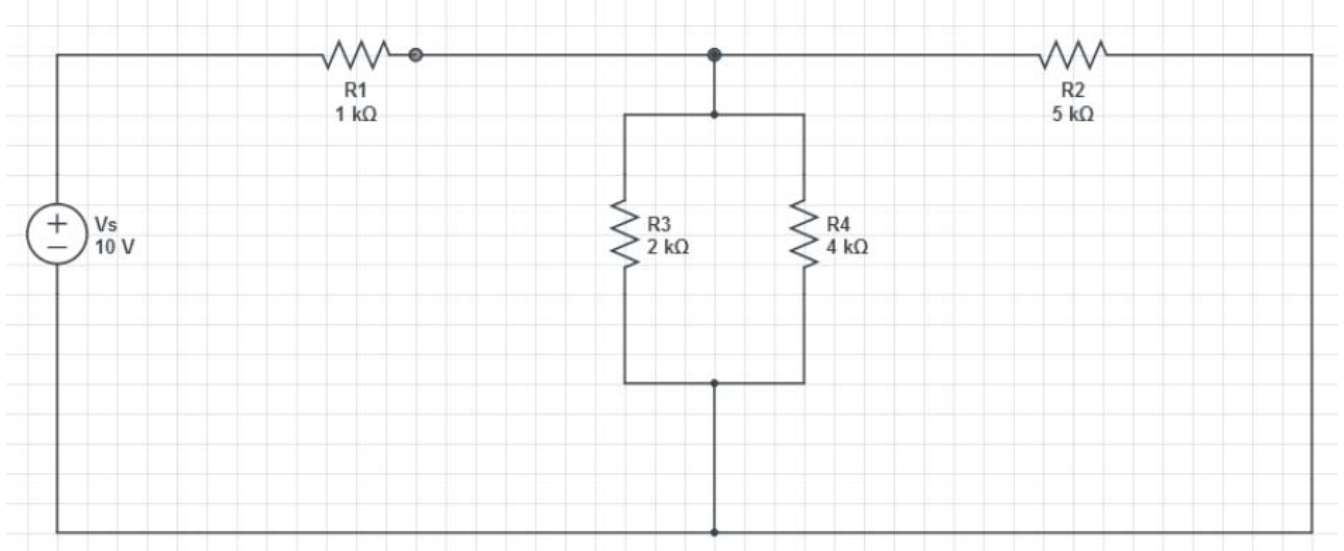
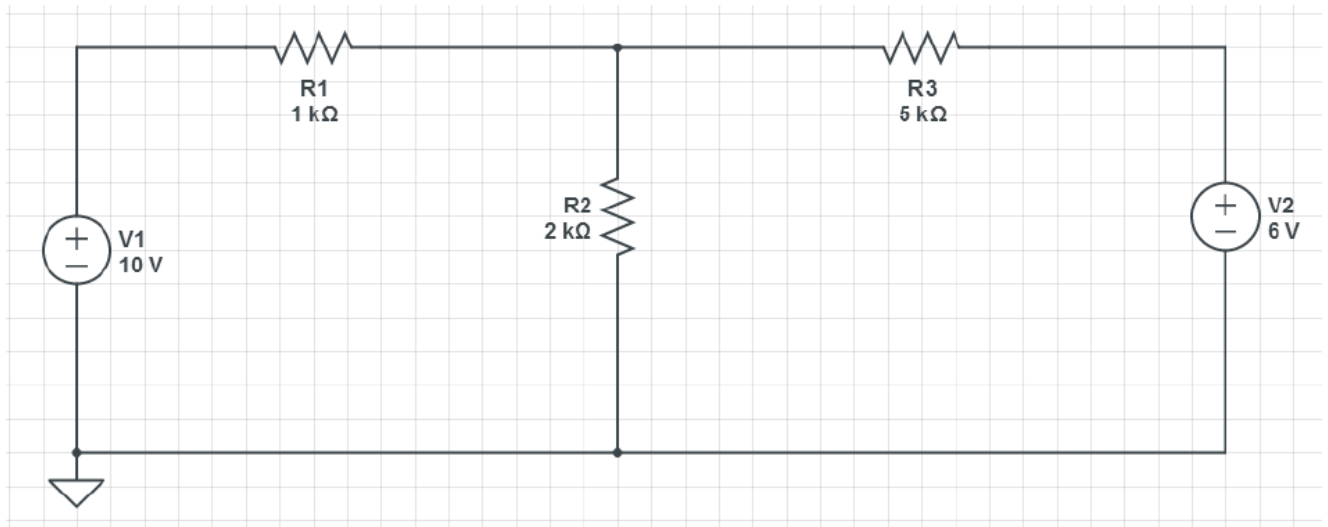


**BRAC UNIVERSITY**  
**COMPUTER SCIENCE AND ENGINEERING**  
**CSE250: CIRCUITS AND ELECTRONICS**

1. Implement the following circuit and verify **KVL** and **KCL**.



2. Find out the current through the **R2** resistor in the following network.  
Then use appropriate steps to verify **SUPERPOSITION** theorem in the network.  
( find the currents when each source is active, deactivating the other; then perform algebraic sum)



3. Find out the current through the **Rload** of the following network.  
 Use appropriate simulation results to find out the **THEVENIN's EQUIVALENT** circuit with respect to the A-B terminals.  
 Redraw and re-simulate the equivalent circuit to recover the value of **current through the load**.  
 Compare the results found from both the methods.

