February 25-March 1, 2019 Spokane Convention Center Spokane, Washington, USA

SIAM Conference on

Exploiting Nested Task-based Parallelism in the Factorization of Hierarchical Matrices

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Steffen Börm Sven Christophersen



Vicenç Beltran



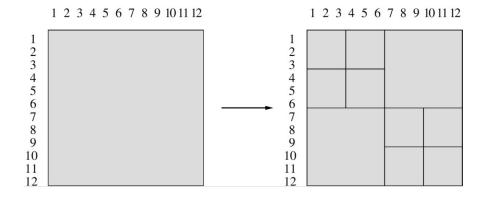
DENSE MATRICES # - MATRICES

SPARSE MATRICES

DENSE MATRICES

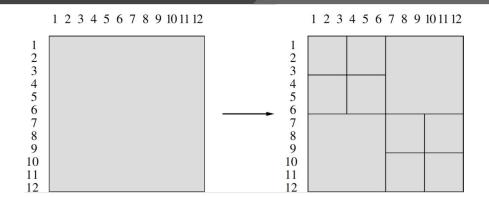
H - MATRICES

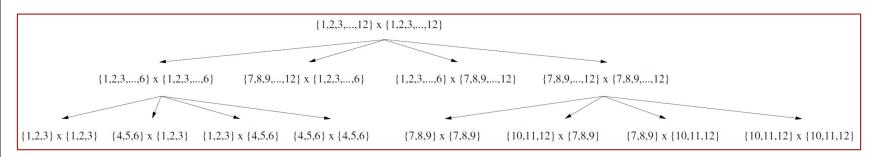
SPARSE MATRICES



DENSE MATRICES

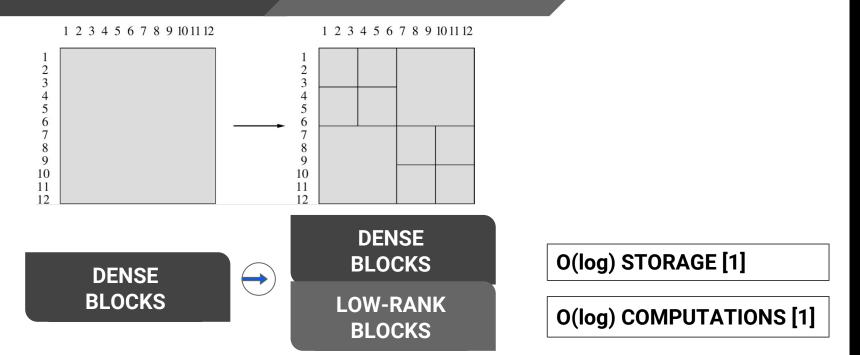
H - MATRICES





DENSE MATRICES

H - MATRICES



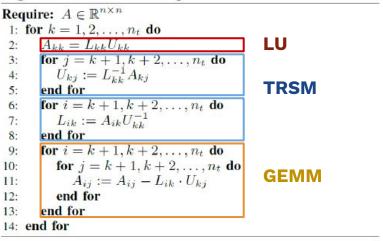
[1] W. Hackbusch. A sparse matrix arithmetic based on H-matrices. Computing (1999), 62:89-108

*H***-LU factorization**

$$\left(egin{array}{cccc} A_{11} & A_{12} & A_{13} \ A_{21} & A_{22} & A_{23} \ A_{31} & A_{32} & A_{33} \end{array}
ight) = \left(egin{array}{cccc} L_{11} & & & \ L_{21} & L_{22} & \ L_{31} & L_{32} & L_{33} \end{array}
ight) \left(egin{array}{cccc} U_{11} & U_{12} & U_{13} \ & & U_{22} & U_{23} \ & & & U_{33} \end{array}
ight)$$

*H***-LU factorization**

Algorithm 1 Blocked RL algorithm for the LU factorization.



$$K = 1$$
 $K = 2$ $K = 3$



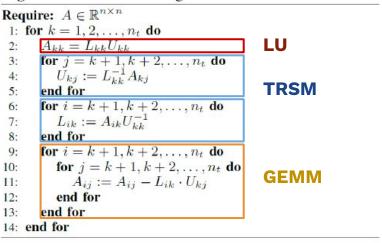




*H***-LU factorization**

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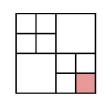
H-Matrices



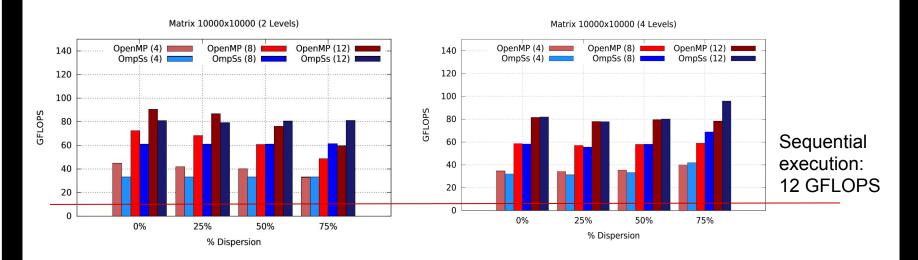








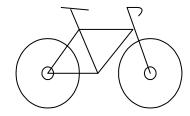
*H***-LU factorization preliminar results [2]**



[2] J. I. Aliaga, **R. Carratalá-Sáez**, R. Kriemann, E. S. Quintana-Ortí, *Task-parallel LU factorization of hierarchical matrices using OmpSs*, in: 2017 IEEE International Parallel and Distributed Processing Symposium Workshops (IPDPSW), 2017, pp. 1148–1157. doi:10.1109/IPDPSW.2017.124.



"There's no need to reinvent the wheel"





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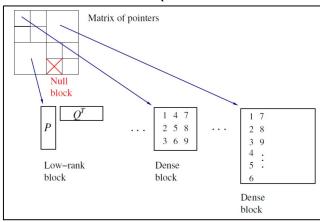


http://www.h2lib.org
Prof. Dr. Steffen Börm
Christian-Albrechts-Universität zu Kiel

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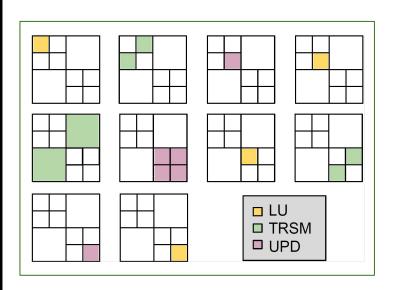


- Open Source (https://github.com/H2Lib)
- C language
- H-Matrices stored as a matrix of (nested) pointers
 - 1st limitation: data location (not continuous in memory)
- Recursive algorithms

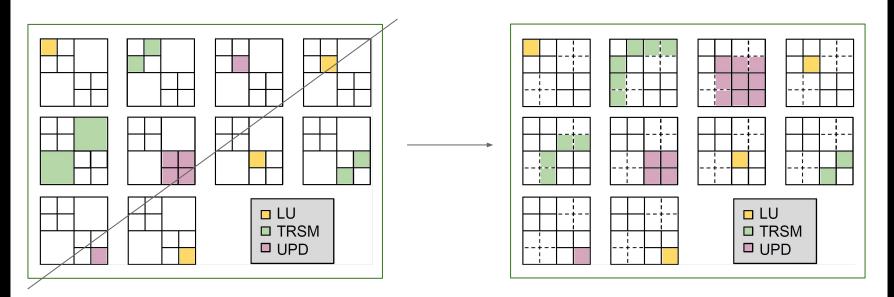
- Open Source (https://github.com/H2Lib)
- C language
- *H*-Matrices stored as a matrix of (nested)
 - 1st limitation: data location (not conting)
- Recursive algorithms
 - 2nd limitation: nested dependencies

```
void RECURSIVE LU( A )
     if ( isLeaf( A ) )
          LU( A );
     else
          for (i = 0 : n row sons)
               RECURSIVE_LU( son_ii );
               for (j = 0 : n_{col_{sons}})
                    TRSM( son_ij );
                    TRSM( son ji );
               for (j = i+1 : n row sons)
                    for (k = i+1 : n_{col_{sons}})
                         UPD( son jk );
```

Data location limitations



Data location limitations



Forces to partition every leaf block into blocks that present the **SMALLEST** block size **INTERVENING** in the operation.

Recursion (nested dependencies) limitations

- H2Lib H-LU is **recursive**.
- Nested parallelism.

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void RECURSIVE_LU( A )
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```

Recursion (nested dependencies) limitations

- H2Lib H-LU is recursive.
- **Nested** parallelism.
- Taskwait is necessary.
- Parallelism is **limited**.

```
void RECURSIVE LU( A )
     if ( isLeaf( A ) )
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The OmpSs * Programming Model & Runtime

- Task-based programming model
- Data-dependences among tasks expressed with clauses
- Discovers the data-flow parallelism at execution time

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- Task-based programming model
- Data-dependences among tasks expressed with clauses
- Discovers the data-flow parallelism at execution time
- OmpSs-2 & Nanos6: new features
 - Nested regions dependencies
 - Early release
 - Weak dependencies

OmpSs-2 & Nanos6 novelties: nested dependencies

- Solved OmpSs overlapping PO and regions troubles
- H2Lib limitation: data location
 - Without nested dependencies: partition needed to smallest block size in the matrix.

OmpSs-2 & Nanos6 novelties: weak dependencies & early release

- H2Lib limitation: recursion
- With weak deps & early release:
 - No taskwait needed.
 - Anticipation of children tasks.

```
#pragma oss task inout(A)
void RECURSIVE LU( A )
     if ( isLeaf( A ) )
          #pragma oss task inout(A)
          LU( A );
     else
          for (k = 0 : n row sons)
               RECURSIVE LU( son kk );
               for (j = 0 : n col sons)
                    #pragma oss task in(A) inout(son kj)
                    TRSM( son kj );
                    #pragma oss task in(A) inout(son jk)
                    TRSM( son jk );
               for (j = k+1 : n row sons)
                    for (i = k+1 : n col sons)
                         #pragma oss task in(son ik, son kj) inout(son ij)
                         UPD( son jk );
     #pragma oss taskwait
```

OmpSs-2 & Nanos6 novelties: weak dependencies & early release

- H2Lib limitation: recursion
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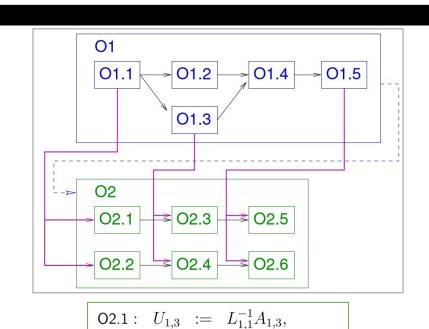
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```

Sequence of operations for the
$$\mathcal{H}$$
-LU factorization of A :

O1.1: $A_{1.1} = L_{1.1}U_{1.1}$

 $O5.5: A_{4,4} = L_{4,4}U_{4,4}$



Tests & results

- Integral equations, BEM
- Underlying kernel function: Laplace equation

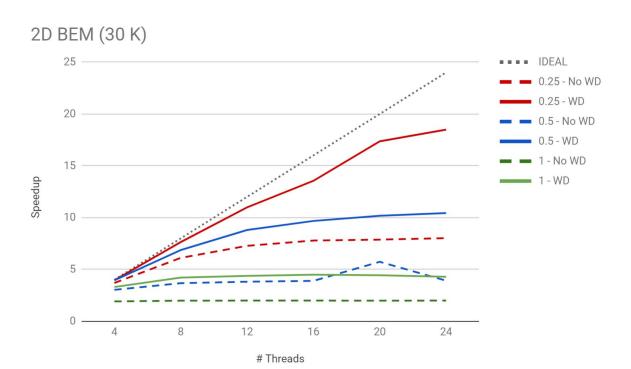
$$g: \mathbb{R}^d \times \mathbb{R}^d \to \mathbb{R}, \quad g(x,y) = \begin{cases} -\log|x-y| & : d=1, \\ -\frac{1}{2\pi}\log||x-y||_2 & : d=2, \\ \frac{1}{4\pi}||x-y||_2^{-1} & : d=3. \end{cases}$$

- Compression with SVD.
- Admissibility condition $\max\{\operatorname{diam}(\mathcal{B}_t),\operatorname{diam}(\mathcal{B}_s)\} \leq \eta \operatorname{dist}(\mathcal{B}_t,\mathcal{B}_s) \text{ where } \eta \in \mathbb{R}_{>0}$

SETUP:

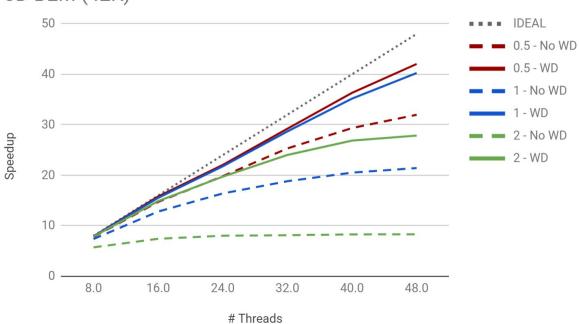
- IEEE 754 double precision arithmetic.
- Single node of the MareNostrum 4 system at BSC, with 2 Intel Xeon Platinum 8160 sockets, 24 cores per socket, 96 Gbytes of DDR4 RAM.
- GCC 4.8.5, Intel MKL 2017.4 (AVX2 instructions enabled), OmpSs-2 (mcxx 2.1.0).

Tests & results



Tests & results





Conclusions

From previous [2] work...

• **Task-based parallelism** seems to offer good performance for H-structures.

From using OmpSs 2 [3] on H2Lib H-LU...

- Good parallel performance with OmpSs 2 task-based parallelism.
- New features (weak dependencies & early release) avoid detected limitations.
- [2] R. Carratalá-Sáez et al. *Task-parallel LU factorization of hierarchical matrices using OmpSs.* Proceedings of the 19th IEEE Workshop on Parallel and Distributed Scientific and Engineering Computing, PDSEC 2017, 2017.
- [3] R. Carratalá-Sáez et al. *Exploiting Nested Task-Parallelism in the H-LU Factorization*. Journal of Computational Science (Elsevier), February 2018. Accepted, pending of publication.

Future work

- Related to H2Lib:
 - Maybe go deeper in parallelism.
 - Study how to improve parallel efficiency in 2D.
 - More test cases (different applications).
 - Other operations.

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- Implement distributed memory H-LU.
- Chameleon extension (low-rank support).



Exploiting Nested Task-based Parallelism in the Factorization of Hierarchical Matrices



Thanks for your attention!





Christian-Albrechts-Universität zu Kie



You are more than welcome to contact us:

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