



Copyright Paul Janicki 2019  
Licensed under the TAPR Open Hardware License ([www.tapr.org/OHL](http://www.tapr.org/OHL)). This documentation  
is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF  
MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.  
**Open Hardware DSP Platform – [www.ohdsp.org](http://www.ohdsp.org)**

Sheet:

File: CoreOne-xCORE200-2.1.kicad\_pcb

**Title: CoreOne – xCORE200 Platform**

Size: A4 Date: 2019-04-04

KiCad E.D.A. kicad (5.1.0)-1

**Rev: 2.1**

Id: 1/1