

Copyright Paul Janicki 2019

Licensed under the TAPR Open Hardware License (www.tapr.org/OHL). This documentation is distributed WITHOUT ANY EXPRESS OR IMPLIED WARRANTY, INCLUDING OF MERCHANTABILITY, SATISFACTORY QUALITY AND FITNESS FOR A PARTICULAR PURPOSE.

Open Hardware DSP Platform — www.ohdsp.org

Sheet:

File: CoreOne-xCORE200-2.0.kicad_pcb

Title: CoreOne — xCORE200 Platform		
Size: A4	Date: 2019-04-02	Rev: 2.0
KiCad E.D.A.	kicad (5.1.0)-1	ld: 1/1

1 2 3 4 10: 1/1 5