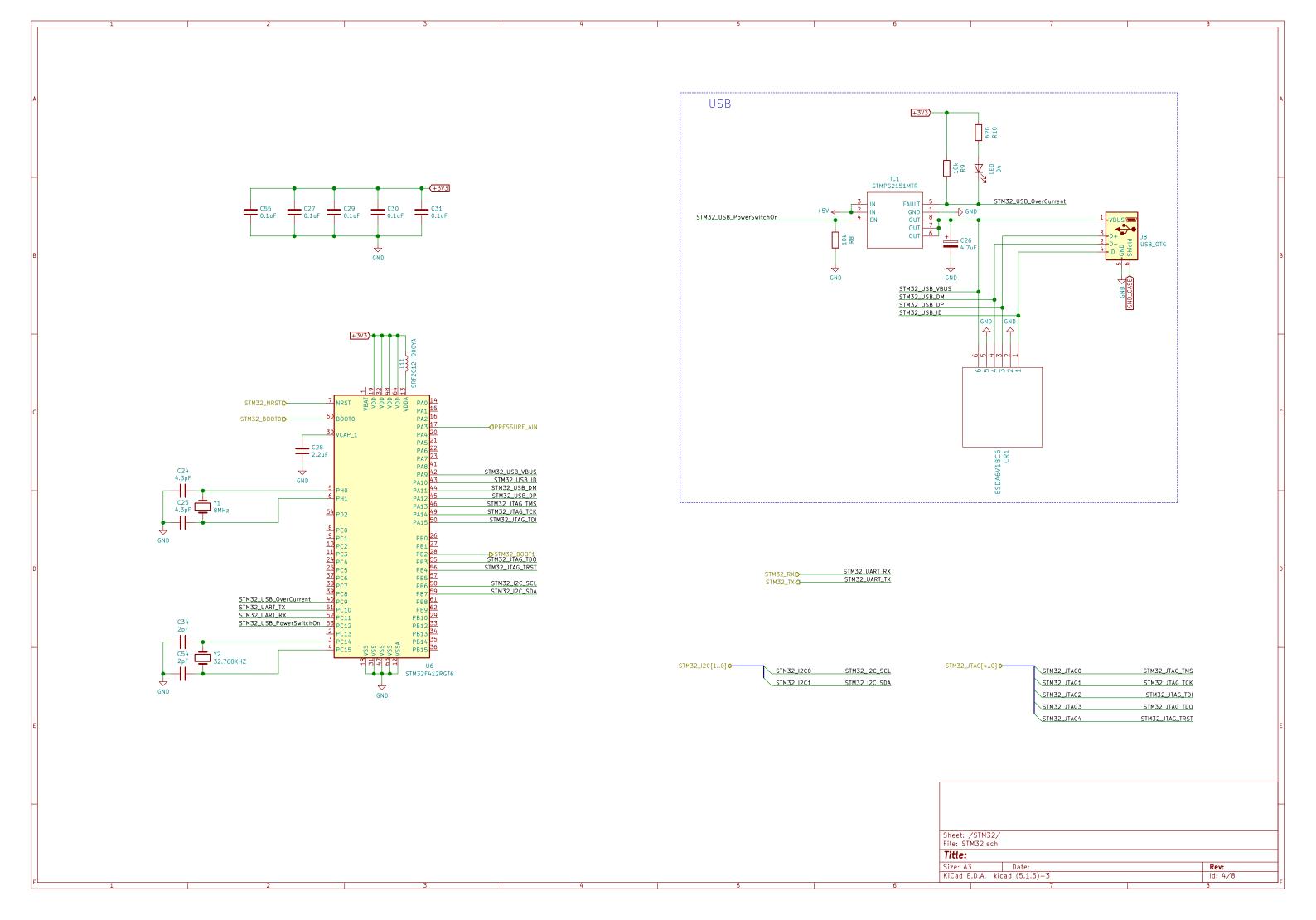
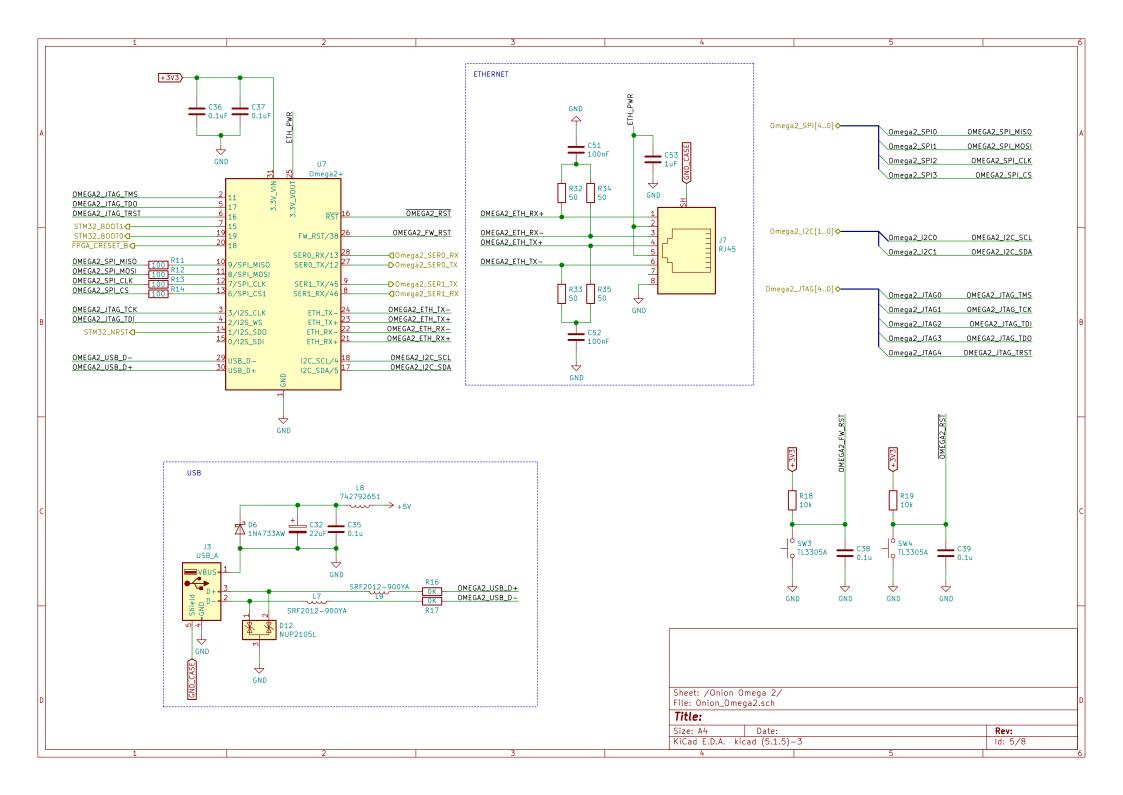


FPGA_SPI[30]♦	FPGA_SPI0	FPGA_SPI_SO
	FPGA_SPI1	FPGA_SPI_SI
	FPGA_SPI2	FPGA_SPI_SCK
	FPGA_SPI3	FPGA_SPI_SS
FPGA_12C[10]�	FPGA_I2C0 FPGA_I2C1	FPGA_12C_SCL FPGA_12C_SDA
FPGA_CONTROL[50]D	FPGA_CONTROLO	FPGA_CTRL_NAVD
	FPGA_CONTROL1	FPGA_CTRL_FMC_CLK
	FPGA_CONTROL2	FPGA_CTRL_NOE
	FPGA_CONTROL3	FPGA_CTRL_NWE
	FPGA_CONTROL4	FPGA_CTRL_NWAIT
	FPGA_CONTROL5	FPGA_CTRL_NE

Sheet: /FPGA/				
File: FPGA.sch				
Title:				
Size: A3	Date:		Rev:	
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SOLENOID 1 SOLENOID 2 +24V U8 4N25-X007T U9 4N25-X007T R20 330 R23 330 **R24** 100k R21 100k SOLENOID_1D-SOLENOID_2D Q1 IRF9540STRLPBF Q2 IRF9540STRLPBF GNDPWR GND nh GNDPWR R22 1k R25 1k D9 CDBU0520 TED D8 D10 LED GNDPWR GNDPWR Sheet: /Solenoids/ File: Solenoids.sch Title: Size: A4 Date: KiCad E.D.A. kicad (5.1.5)-3 **Rev:** Id: 6/8

