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	<title></td><td></td><td></td><td></td><td></td></tr><tr><th>Size</th><th>Document Number</th><th></th><th></th><th></th><th>Rev</th></tr><tr><th>A3</th><th><Doc></th><th></th><th></th><th></th><th><RevCode></th></tr><tr><td>Date:</td><td>Tuesday, December 14, 2021</td><td>Sheet</td><td>1</td><td>of</td><td>4</td></tr></tbody></table></title>				

LCD Display BACK LIGHT XS2 VCC_3V3 L3 WPN252012H4R7MT R17 NC VCC_3V3 V5 B0540 FMARK R18 NÇ SDI RS/SCL C25 C26 10uF 0.1uF L2r5x2r0 ->>LCD_RS ->>LCD_WR ->>LCD_RD VCC 3V3 N2 1uF/50V SW ->>LCD_RST | RESET IM0 R26 NC VCC_3V3 R27 ___0R LCD_BL_DIM <<-IM1 IM2 Apply a 200Hz to lkHz square waveform to the C28 EN pin to implement PRM dimming of the LEDs. The minimum recommended amplitude of the PRW signal is 1.5V. For high frequency PRM dimming (>1kHz), it is also recommended that the dimming control be implemented as shown in Figure 3. The cut off frequency for the RC filter should be 10 times lower than that of the input PRM signal F = 1/(2nRC) DB23 DB22 DB21 DB20 DB19 DB18 DB17 DB16 DB15 DB14 DB13 DB12 DB11 DB10 DB9 DB8 DB7 DB6 DB5 DB4 DB3 DB2 DB3 DB2 DB3 R29 ____0R LEDK VCC_3V3 GND R30 NC MP3302DJ 10R/1% R=200mV/I(led) 根据LCD背光电流选择 LCD_D15 -> LCD_D14 -> LCD_D13 -> LCD_D12 -> LCD_D10 -> LCD_D9 -> LCD_D9 -> LCD_D6 -> LCD_D5 -> LCD_D4 -> LCD_D5 -> LCD_D5 -> LCD_D4 -> LCD_D10 VCC 3V3 ΤP XS3 VCC INT SDA SCL GND DB0 DE PCLK HSYNC FT5216 I2C Address: 0'b 0111 000x VCC_3V3 FC07-S06FCC-00 钜硕电子 抽拉式 下接 VCC_3V3 VSYNC IOVCC VCC GND LEDK VCC_3V3 TK043F1508 Driver IC:RM68120 [C75| 0.1uF C36 VCC ->>TCA9554_INT_L LEDA GND -> ICM_INT -> ESP_SPI_CS_CNN -> EXT_IO0 -> EXT_IO1 -> LCD_BL_DIM -> NS_PA_CTRL -> TP_INT -> TP_RST SDA 0.1uF FH26W-45S-0.3SHW I2C Address: 0'b 0100 000x Motion D5 Temp. & Humidity VCC_3V3 VCC_3V3 Optical VCC_3V3 C49 D7 VCC SCL SCL SDA NC1 NC2 BH1750_DVI ADDR DVI EP VDD VCC_3V3 GND SDA GND BH1750FVI-TR HDC1080DMBR I2C Address: 0'b 1011 100x 0.1uF I2C Address: 0'b 1000 000x I2C Address: 0'b 1101 000x 0.1uF <Title> Document Numbe <RevC А3 Sunday, December 19, 2021

