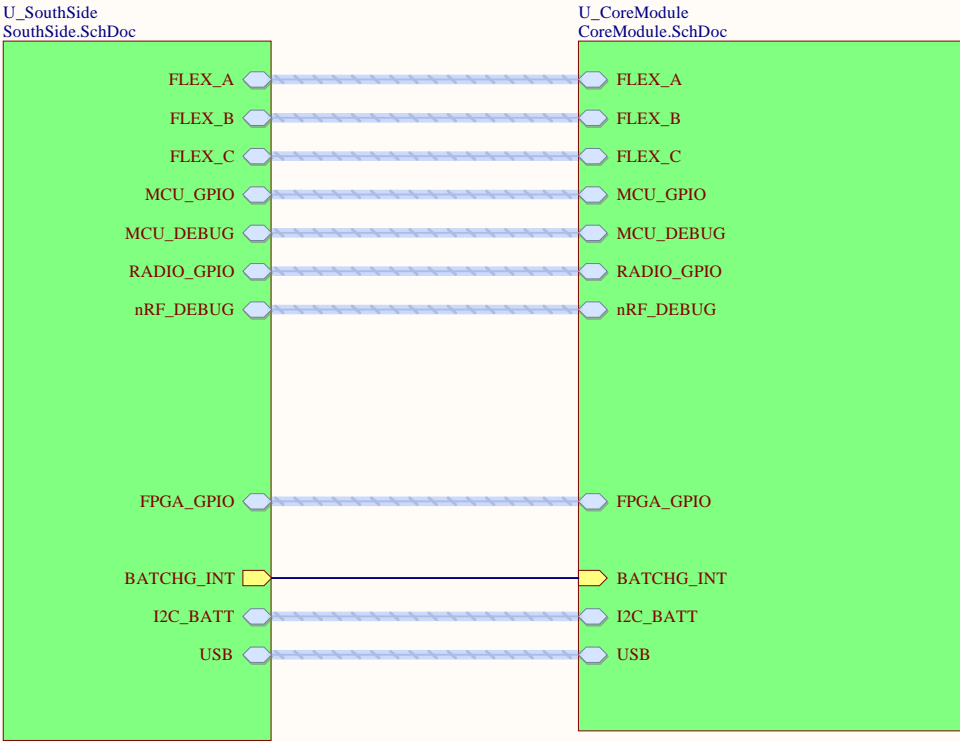


CHANGE HISTORY			
REV	PAGE/ZONE	DESCRIPTION OF CHANGE	DATE
R01	Alpha	Release	07/20/2016
R02	Production	Release	08/10/2016
R03	Design Validation	Release	09/21/2016

U\_RevisionLog  
RevisionLog.SchDoc



Design Notes:

Layout Notes:

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Project: Ventriloquist

PCBA: EVM Application Board

Sheet Title: Block Diagram

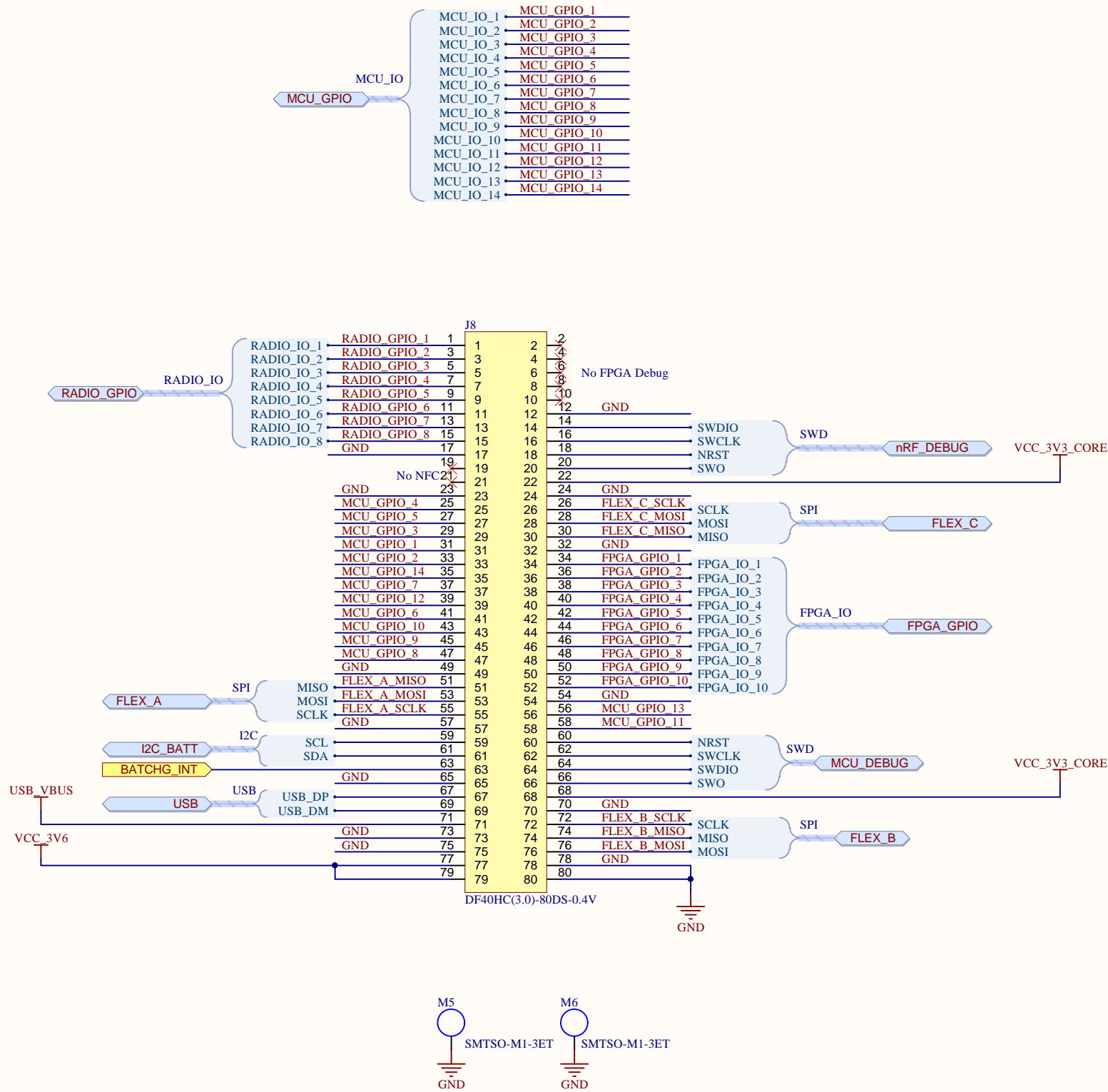
Drawn by: D. Bruey, M. Ciuffo

Schematic Part Number: 602-100862

Schematic Revision: 03

Date: 9/19/2016

Sheet: 1 / 7



Design Notes:

Layout Notes:

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Project: Ventriloquist

PCBA: EVM Application Board

Sheet Title: Core Module

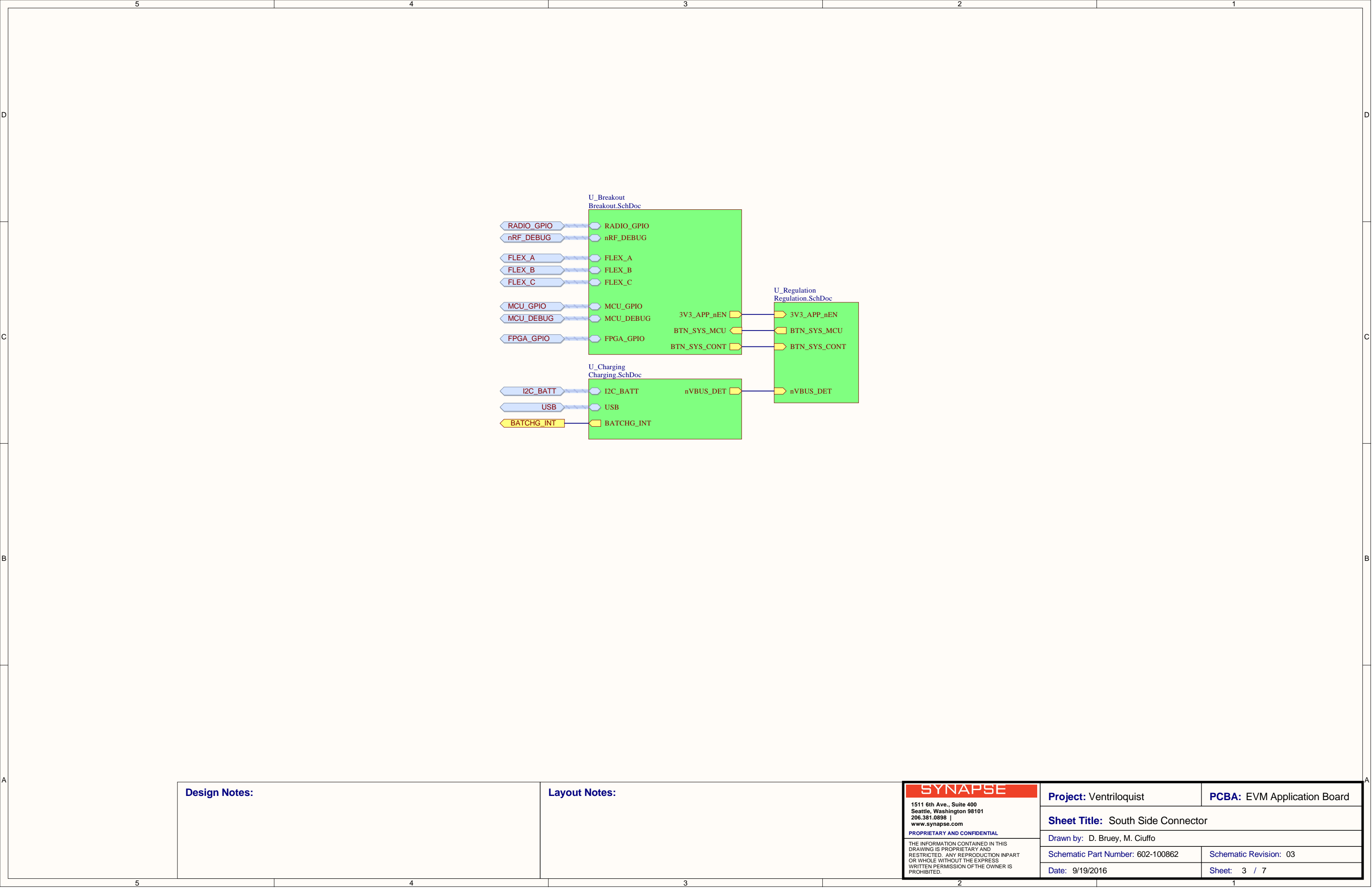
Drawn by: D. Bruey, M. Ciuffo

Schematic Part Number: 602-100862

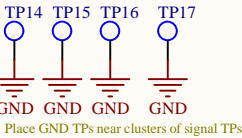
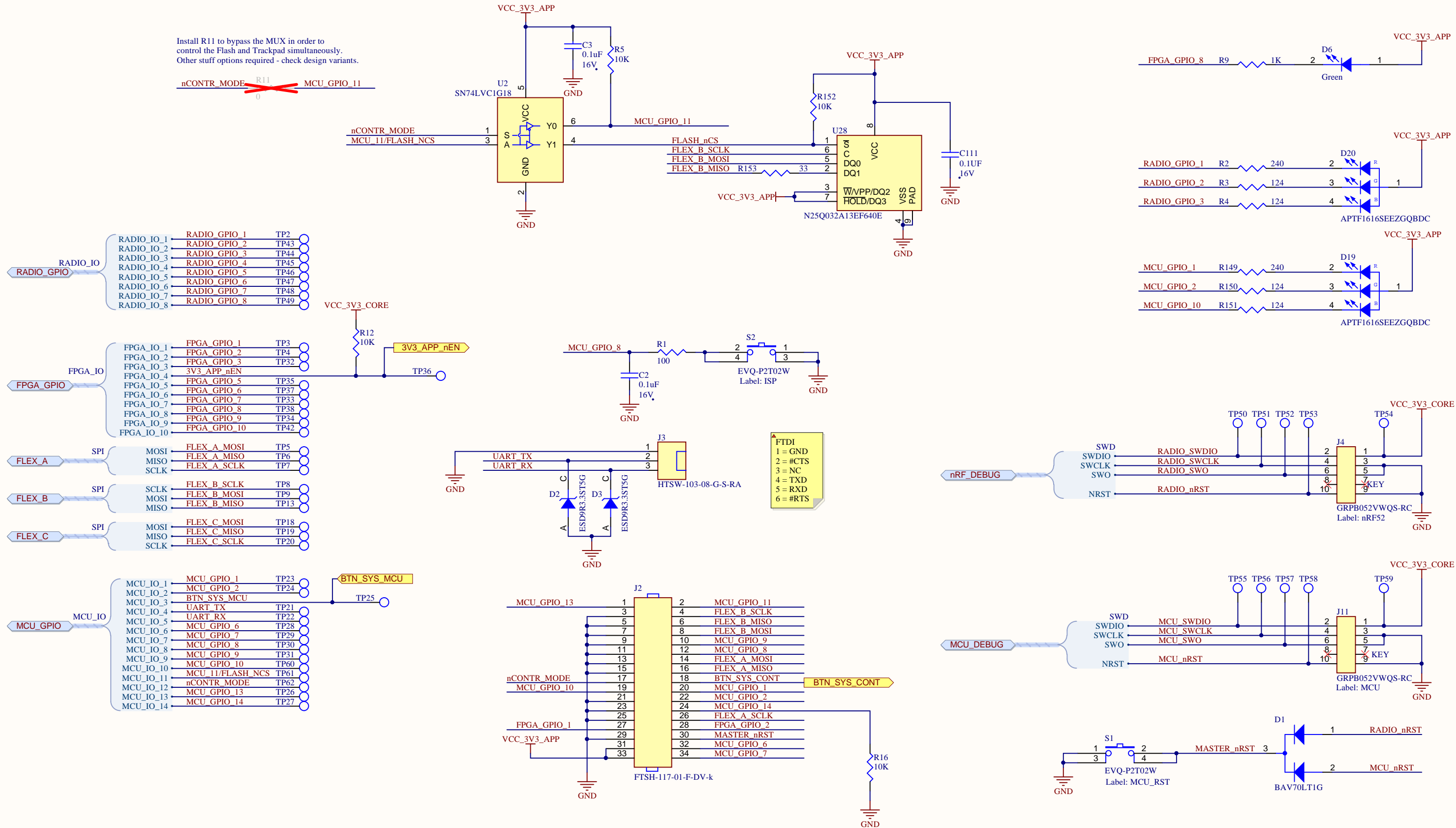
Schematic Revision: 03

Date: 9/19/2016

Sheet: 2 / 7



Install R11 to bypass the MUX in order to control the Flash and Trackpad simultaneously.  
Other stuff options required - check design variants.



#### Design Notes:

#### Layout Notes:

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Project: Ventriloquist

PCBA: EVM Application Board

Sheet Title: MCU Breakout

Drawn by: D. Bruey, M. Ciuffo

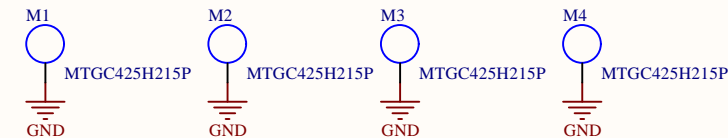
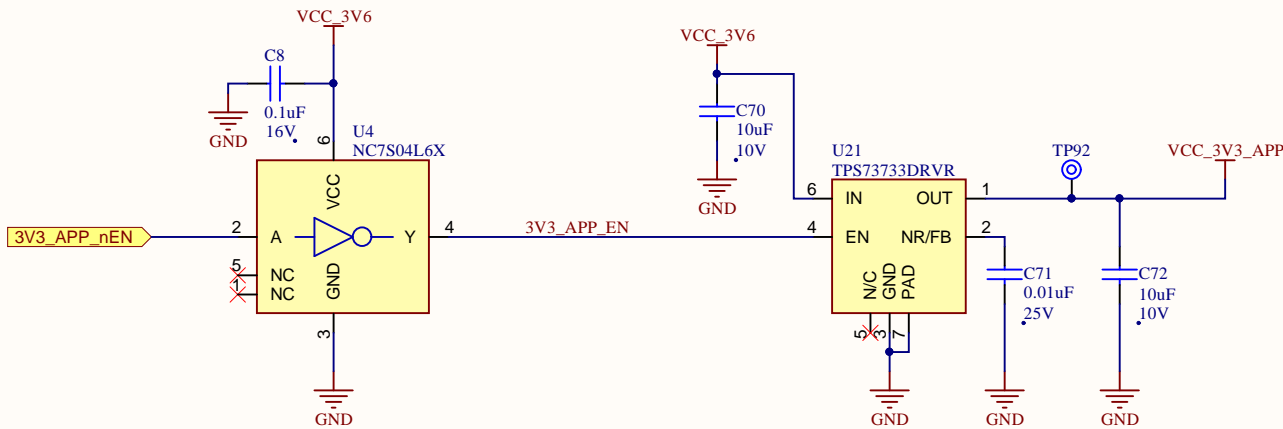
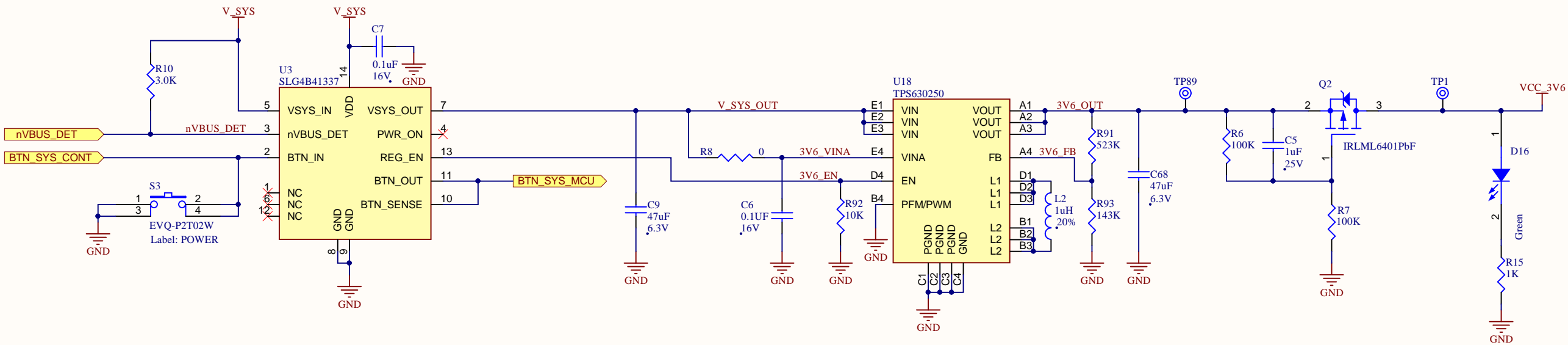
Schematic Part Number: 602-100862

Date: 9/19/2016

Schematic Revision: 03

Sheet: 4 / 7





#### Design Notes:

TPS630252 Assumptions:  
Minimum battery voltage: 3.0V  
Peak current @ 3.6V: 1A  
Efficiency: 90%  
Calculated Peak inductor current: 1.428A  
Calculated Frhpz: 398.5kHz (7)  
Used (5)(6)(7) on TPS630252 datasheet  
(.8V/150k)\*(150k+523k) = 3.589V

#### Layout Notes:

Minimize current loop of input/output caps of TPS630252

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**Project:** Ventriloquist

**PCBA:** EVM Application Board

**Sheet Title:** Regulation

**Drawn by:** D. Bruey, M. Ciuffo

**Schematic Part Number:** 602-100862

**Date:** 9/19/2016

**Schematic Revision:** 03

**Sheet:** 6 / 7

CHANGE HISTORY		
REV	DESCRIPTION OF CHANGE	DATE
R01	Alpha Release	07/20/2016
R02	Moved Q1 gate from REGN to VBUS Removed U1 from VBUS Added D7 and C10 to VBUS Changed D4 TVS to 7.5 VDC Added R10 to nVBUS_DET Added R12 from 3V3_APP_EN to VCC_3V3_CORE Updated U3 programmed part number	08/10/2016
R03	Design Validation Release Add R16 to mute floating analog input MCU_GPIO_14	09/21/2016

Design Notes:

Layout Notes:

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Project: Ventriloquist

PCBA: EVM Application Board

Sheet Title: Revision Log

Drawn by: D. Bruey, M. Ciuffo

Schematic Part Number: 602-100862

Schematic Revision: 03

Date: 9/19/2016

Sheet: 7 / 7