

FT232H

The diagram shows a complex PCB layout for an FT232H to SPI to FPGA interface. The central component is the FT232H IC (U4), which is connected to a USB_C_Recept_16Pin (J1) and an SPI interface (U1B, U1C) connected to an FPGA (U1A). The layout includes various power planes, decoupling capacitors, and test points. The title "FT232H" is prominently displayed at the top center.

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FPGA to HUB75E

Level shifters

Configuration flash

Sheet: /
File: ICE40Module.sch

Title:

Size: A3 Date: Rev:
KiCad E.D.A. kicad 5.1.5+dfsg1-2build2 Id: 1/1

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