深圳秦唐盛世科技有限公司

Part Name: 7.00 inch TFT Display Module

Part ID: QT0700F8-IH (IPS&High Brightness)

Ver: A

Customer:
Approved by

Revision History

Rev.	Date	Contents	Written	Approved
A	2015/08/25	Preliminary Specification	Bin	Kevin

Contents

1. OVERVIEW

This is 7.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) Module composed of LCD panel, driver ICs ,control circuit and LED backlight . By applying 1024×600 images are displayed on the 7.0" diagonal screen. Display 16.7M colors by R.G.B signal input.

General specifications are summarized in the following table：

ITEM	SPECIFICATION			
Display Area (mm)	154.2144(H) × 85.92(V)			
Number of Pixels	1024(H) × 3(RGB) ×600(V)			
Pixel Pitch (mm)	0.1506(H) × 0.1432(V)			
Color Pixel Arrangement	RGB vertical stripe			
Display Mode	Normally Black			
Number of Colors	16.7M			
Brightness (cd/m^2)	1400nits(typ)/1200nits(min)			
Response Time (ms)	25(typ.)			
NTSC	50%			
Contrast Ratio	800(typ)；600 (min)			
Interface connection	LVDS			
Module Size (mm)		Min.	Typ.	Max
	Horizontal (H)	164.7	165	165.3
	Vertical (V)	99.4	99.6	99.8
	Depth (D)	6.3	6.5	6.7
Module Weight (g)	155 (typ)			
Backlight Unit	LED			
Surface Treatment	Anti-Glare			

2. ABSOLUTE MAXIMUM RATINGS

The following are maximun values which, if exceeded, may cause faulty operation or damage to the unit.

Item	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	DVDD DVDD_LVDS	-0.3	3.96	V	
Analog Supply Voltage	AVDD	-0.5	14.85	V	
Gate On Voltage	VGH	-0.3	40	V	
Gate Off Voltage	VGL	-20	0.3	V	
Gate On-Gate Off Voltage	VGH-VGL	12	40	V	
Signal Input Voltage	NIND0 ~ NIND3 PIND0 ~ PIND3 NINC,PINC	-0.5	5	V	
Forward Current（per LED）	If	-	100	mA	
Reverse Voltage（per LED）	VR	-	5	V	
Operating Temperature	Topa	-20	70	℃	Note 1
StorageTemperature	Tstg	-30	80	℃	Note 1

Note1：If users use the product out off the environmemtal operation range（temperature and humidity, it will have visual quality concerns.

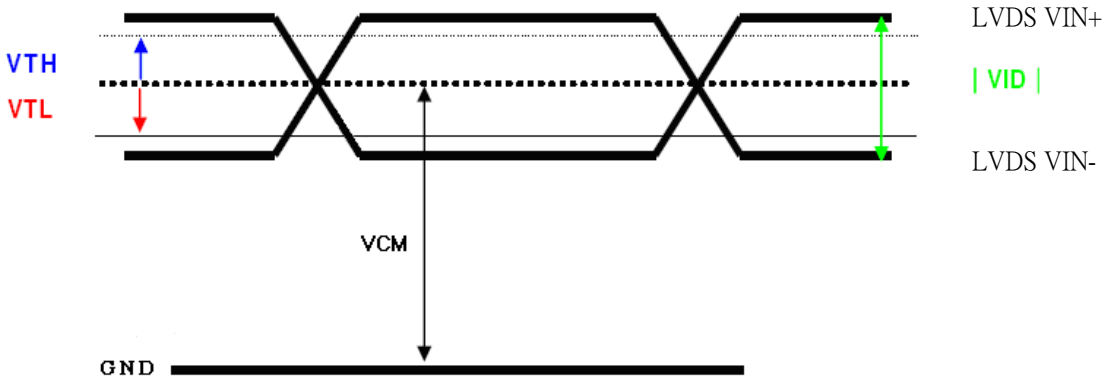
3. ELECTRICAL CHARACTERISTICS

3.1 Typical Operation Conditions

Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Digital Power Supply Voltage For LCD	DVDD	3	3.3	3.6	V	
Logic Input Voltage (LVDS:IN+,IN-)	VCM	$\frac{\lvert VID \rvert}{2}$	-	$2.4-\frac{\lvert VID \rvert}{2}$	V	Note1
	$\lvert VID \rvert$	200	-	600	mV	Note1
	VTH	-	-	100	mV	VCM=1.2V Note1
	VTL	-100	-	-	mV	
Analog Power Supply Voltage	AVDD	9.4	9.6	9.8	V	
Gate On Power Supply Voltage	VGH	17	18	19	V	
Gate Off Power Supply Voltage	VGL	-6.6	-6	-5.4	V	
Common Power Supply Voltage	VCOM	3.15	3.3	3.4	V	Note2
Logic Input Voltage	VIH	0.7*DVDD	-	DVDD	V	
	VIL	GND	-	0.3*DVDD	V	

【Note1】LVDS signal



【Note2】Please adjust VCOM to make the flicker level be minimum.

3.2 Current Consumption

ITEM	SYMBOL	CONDITION	MIN	TYPE	MAX	UNIT	NOTE
Gate On Power Current	IVGH	VGH =18V	- -	0.5	1	mA	Note1
Gate Off Power Current	IVGL	VGL= -6V	- -	0.5	1	mA	Note1
Digital Power Current	IDVDD	DVDD = 3.3V	- -	30	45	mA	Note1
Analog Power Current	IAVDD	AVDD = 9.6V	- -	35	45	mA	Note1
Total Power Consumption	PC		- -	447	604	mW	Note1

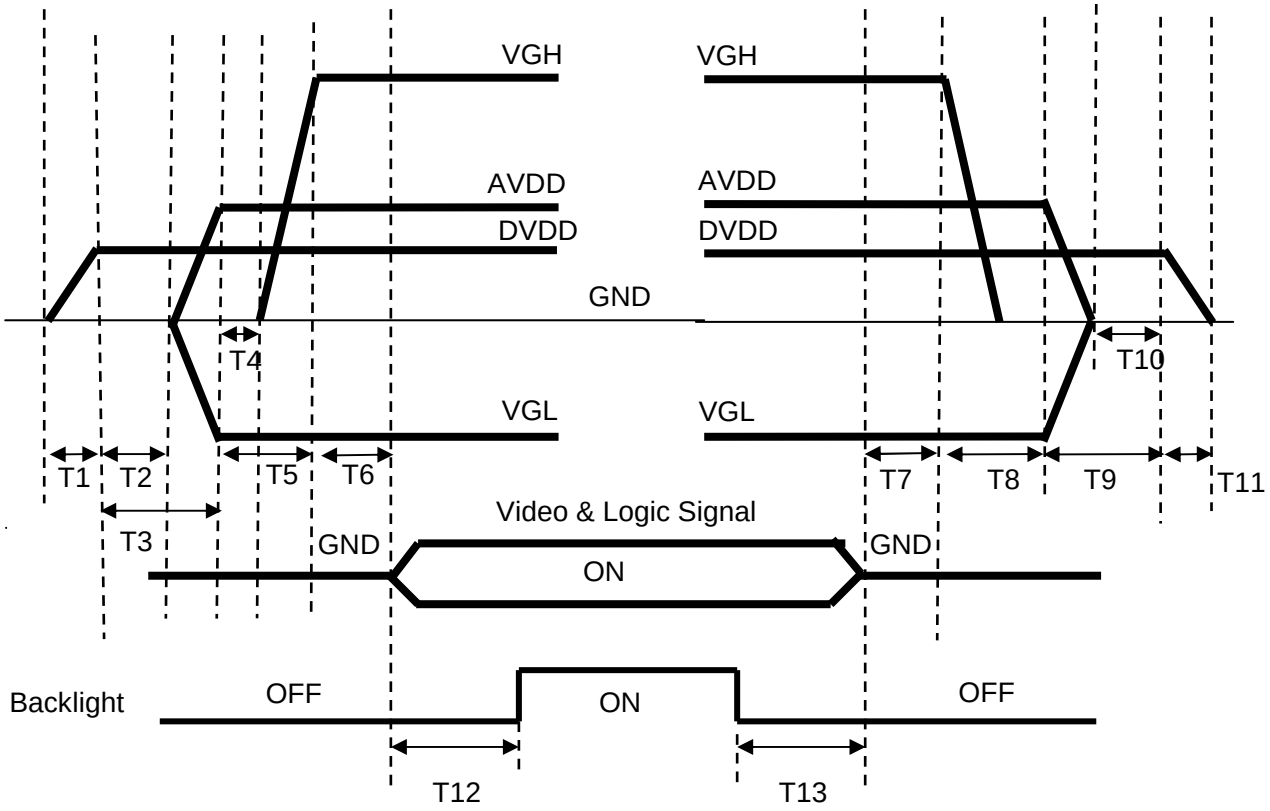
【Note1】Typ. specification：Gray-level test Pattern
Max. specification：Black test Pattern

256 gray pattern

Black Pattern

3.3 Power、Signal Sequence

Power On：DVDD→AVDD/VGL →VGH →Video &Logic Signal→Backlight
Power Off：Backlight→Video &Logic Signal→ VGH→AVDD/VGL→DVDD



0<T1≦10ms
T2>0ms
T3>20ms
T4>0ms
T5>10ms
0<T6≦10ms
T12≧200ms

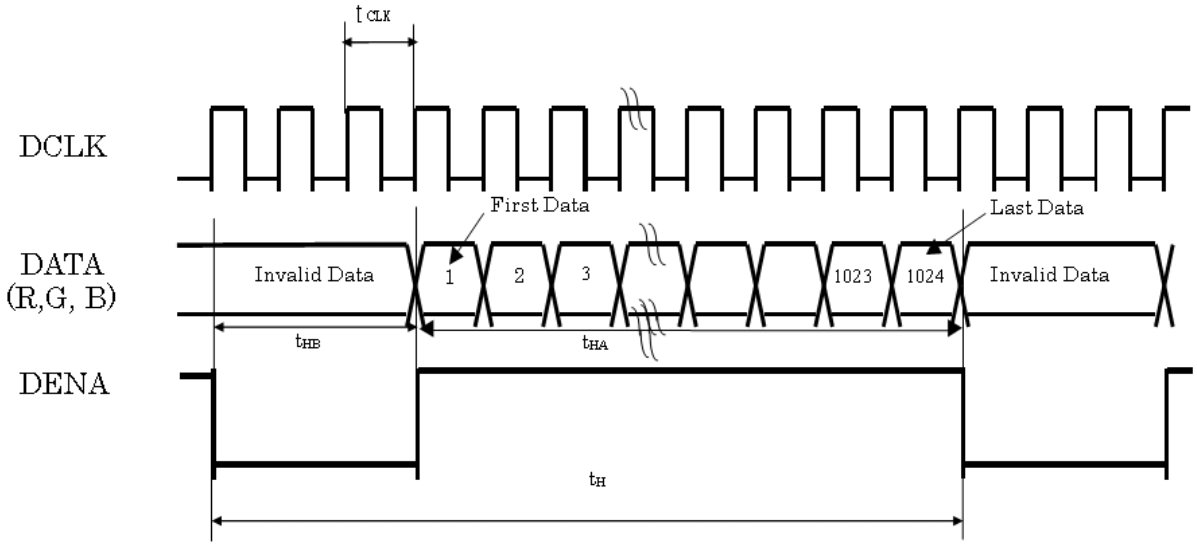
T7>0ms
T8>0ms
T9>0ms
T10>0ms
0<T11≦10ms
T13≧200ms

3.4 Timing Characteristics of Input Signals

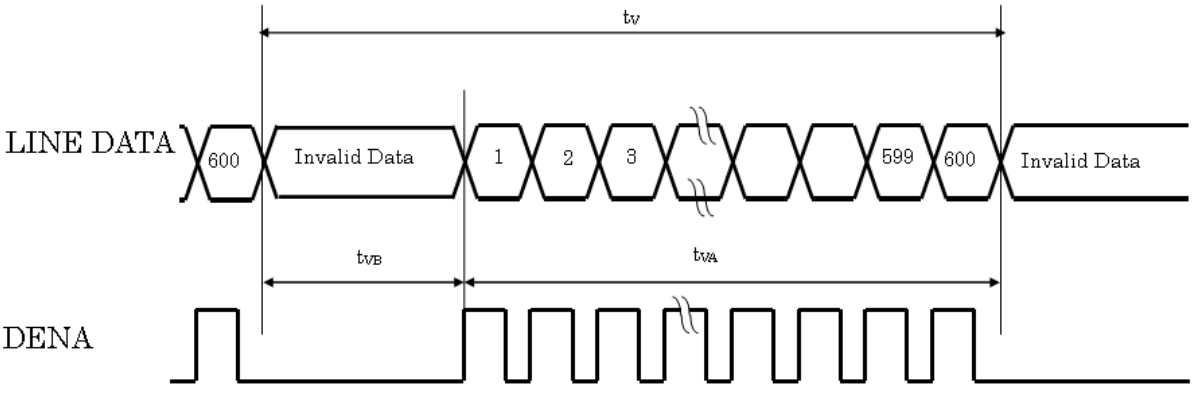
ITEM				SYMBOL	MIN	TYP	MAX	UNIT
LVDS input signal sequence	CLK Frequency			tclk	45	51.2	57	MHz
LCD input signal sequence （Input LVDS Transmitter）	DENA	Horizontal	Horizontal total Time	t_H	1324	1344	1364	tCLK
			Horizontal effective Time	t_{HA}	1024			tCLK
			Horizontal Blank Time	t_{HB}	300	320	340	tCLK
		Vertical	Vertical total Time	t_V	625	635	645	t_H
			Vertical effective Time	t_{VA}	600			t_H
			Vertical Blank Time	t_{VB}	25	35	45	t_H

3.5 Timing Sequence(Timing Chart)

3.5.1 Horizontal Timing Sequence

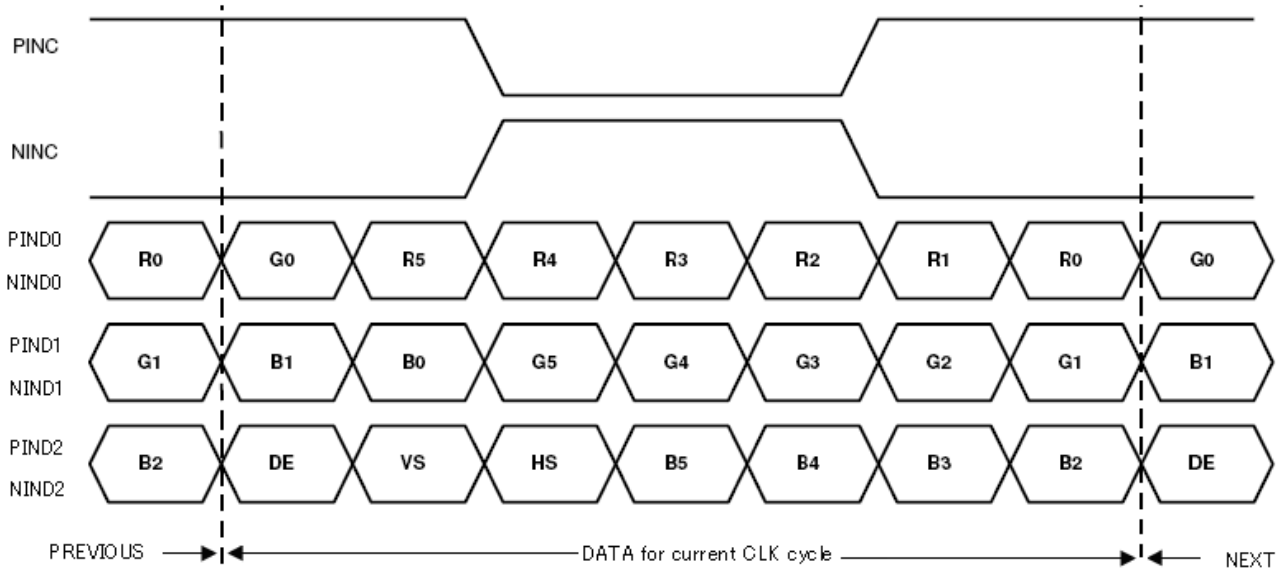


3.5.2 Vertical Timing Sequence

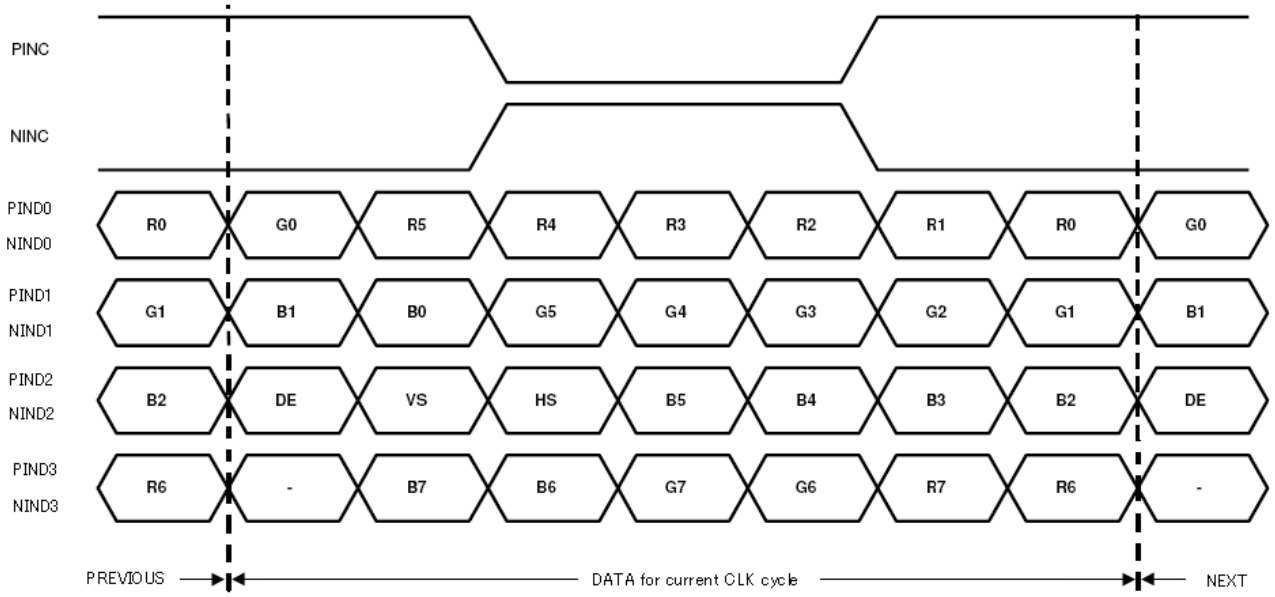


3.5.3 LVDS Input Data Mapping

6bits LVDS Input



8bits LVDS Input



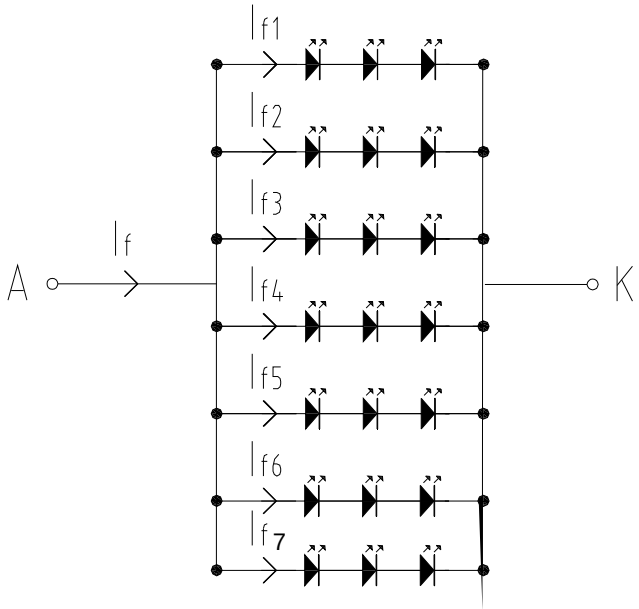
3.6 Backlight

Ta=25℃

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
LED Current	IL	540	630	700	mA
LED Voltage	VL	8.4	9.6	10.5	V
Power Consumption	WL	-	6.048	---	W
LED Lifetime	-	20000			Hr

【Note】

*1)LED Circuit Diagram：



*2) A：Anode(＋)，K：Cathode(－)

*3) LED control suggested fixed current.

*4) Definition of the LED life time：Luminance will decay less than 50%

4. INTERFACE CONNECTION

4.1 CN1 (Input Signal)

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	SYMBOL	FUNCTION	Remark
1	VCOM	Common Voltage	
2	DVDD	Digital Power	
3	DVDD	Digital Power	
4	NC	Not Connect	
5	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10KΩ，C=0.1μF)	
6	STBYB	Standby mode, normally pull high STBYB="1", normal operation STBYB="0",timing control, source driver will turn off, all output are high-Z	
7	GND	Ground	
8	NIND0	Negative LVDS differential data input	
9	PIND0	Positive LVDS differential data input	
10	GND	Ground	
11	NIND1	Negative LVDS differential data input	
12	PIND1	Positive LVDS differential data input	
13	GND	Ground	
14	NIND2	Negative LVDS differential data input	
15	PIND2	Positive LVDS differential data input	
16	GND	Ground	
17	NINC	Negative LVDS differential clock input	
18	PINC	Positive LVDS differential clock input	
19	GND	Ground	
20	NIND3	Negative LVDS differential data input	
21	PIND3	Positive LVDS differential data input	
22	GND	Ground	
23	NC	Not Connect	
24	NC	Not Connect	
25	GND	Ground	
26	NC	Not Connect	
27	NC	Not Connect	
28	SELB	6bit/8bit Mode Select	*1)
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	NC	Not Connect	
32	NC	Not Connect	
33	SHLR	Horizontal Inversion	*2)
34	UPDN	Vertical Inversion	*2)
35	VGL	Negative Power for TFT	
36	NC	Not Connect	
37	NC	Not Connect	
38	VGH	Positive Power for TFT	
39	NC	Not Connect	
40	NC	Not Connect	

Remarks：

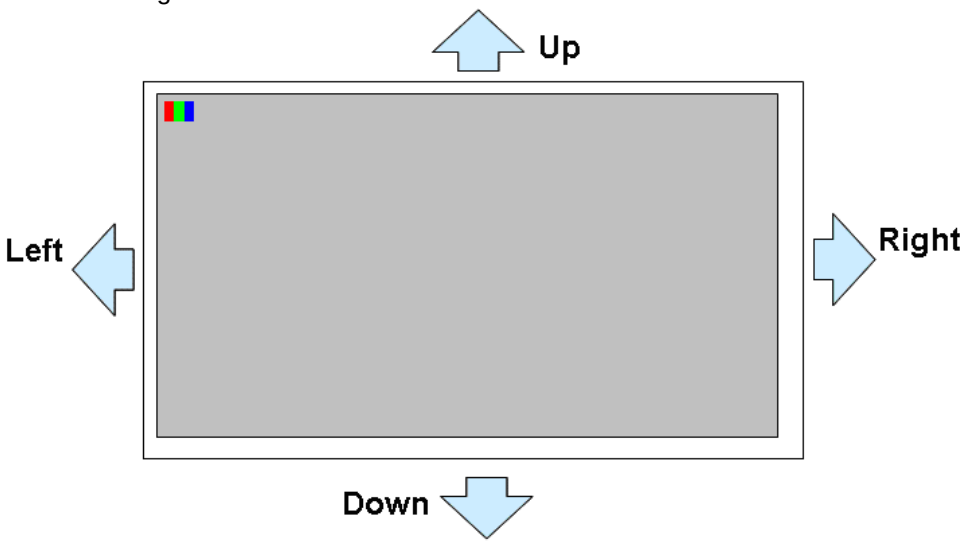
*1)if LVDS input data is 6bits,SELB must must be set to High
if LVDS input data is 8bit , SELB must be set to Low

*2)UPDN and SHLR control function

UPDN	SHLR	FUNCTION
0	1	Normal Display
0	0	Inverse Left and Right
1	1	Inverse Up and Down
1	0	Inverse Left and Right Inverse Up and Down

SHLR	UPDN	Data shifting
DVDD	GND	Left→Right，Up→Down(default)
GND	GND	Right→Left，Up→Down
DVDD	DVDD	Left→Right，Down→Up
GND	DVDD	Right→Left，Down→Up

Definition of scanning direction.

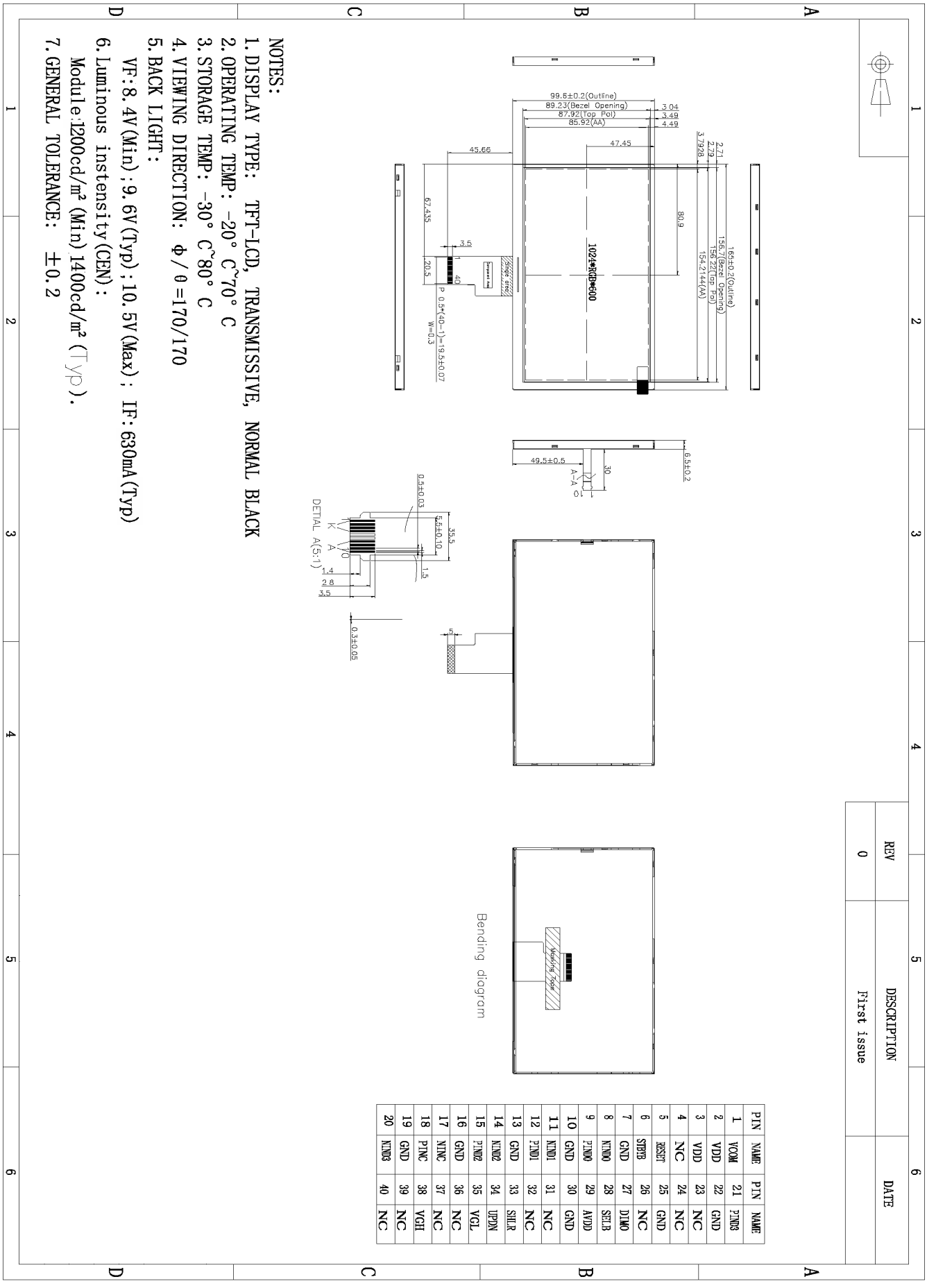


4.2Backlight Section

FPC is used for the backlight electronics interface. The FPC connector is suggested to Hirose FH28-10S-0.5SH

Pin No.	Symbol	I/O	Function	Remark
1	LED-	P	LED Cathode	
2	LED-	P	LED Cathode	
3	LED-	P	LED Cathode	
4	LED-	P	LED Cathode	
5	NC			
6	NC			
7	LED+	P	LED Anode	
8	LED+	P	LED Anode	
9	LED+	P	LED Anode	
10	LED+	P	LED Anode	

5. MECHANICAL DIMENSIONC



PIN	NAME	PIN	NAME
1	VCOM	21	PTND3
2	VDD	22	GND
3	VDD	23	NC
4	NC	24	NC
5	RESET	25	GND
6	STBYB	26	NC
7	GND	27	DIMO
8	NTND0	28	SELB
9	PTND0	29	AVDD
10	GND	30	GND
11	NTND1	31	NC
12	PTND1	32	NC
13	GND	33	SHLR
14	NTND2	34	UPDN
15	PTND2	35	VGL
16	GND	36	NC
17	NTNC	37	NC
18	PTNC	38	VGH
19	GND	39	NC
20	NTND3	40	NC

6. OPTICAL CHARACTERISTICS

(Use CPT LED backlight)

Ta=25℃

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Contrast Ratio		CR	Point-5	600	800	--	--	2
Response Time		Tr +Tf	Point-5	--	25	40	ms	3
Luminance(CEN)		L	Point-5	1200	1400	--	cd/m^2	
Luminance Uniformity		ΔL	*2)	70	80	--	%	
NTSC				45%	50%	--		
Viewing Angle	Left	ϕ_L	CR≧10	80	85			4
	Right	ϕ_R		80	85			4
	Upper	θ_T		80	85			4
	Lower	θ_B		80	85			4
MDL Chromacicity	White	x	$\theta=\phi=0^\circ$	0.27	0.290	0.31		
		y		0.311	0.331	0.351		

Note1: Measure condition：25℃±2℃，60±10%RH，under10 Lux in the dark room.BM-5A (TOPCON)，viewing angle2°，IL=540 mA（Backlight current），measurement after lighting on 10 mins.

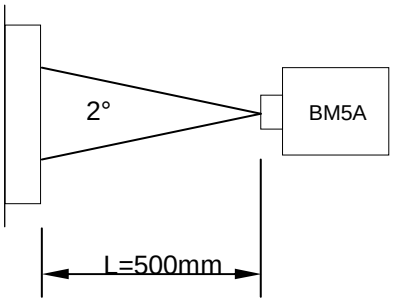


Fig. 6-1 Measuring point

Note2: Definition of contrast ratio：

Contrast Ratio (CR)= (White) Luminance of ON ÷ (Black) Luminance of OFF

Definition of luminance：Measure white luminance on the point 5 as figure.6-1

△L = [L(MIN)/L(MAX)]×100

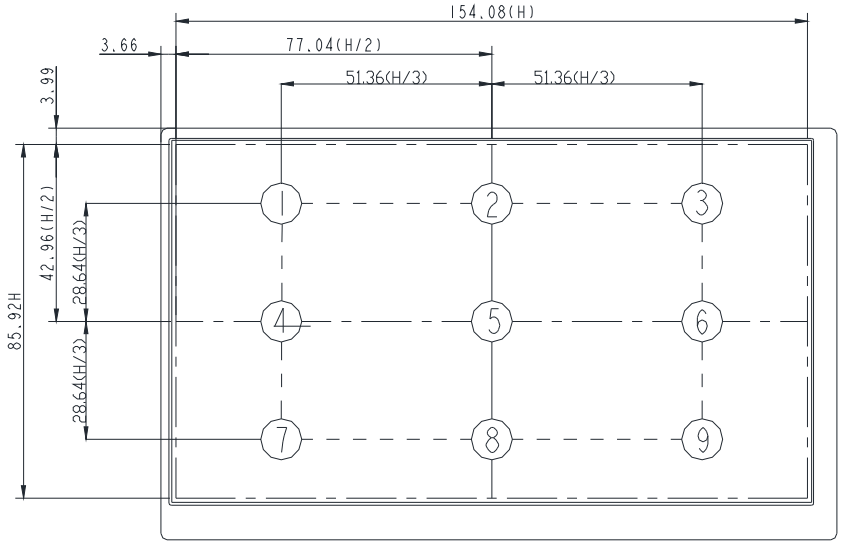


Fig. 6-1 Measuring point

Note 3: Definition of Response Time.(White-Black)

The response time is defined as the time interval between the 10% and 90% amplitudes.

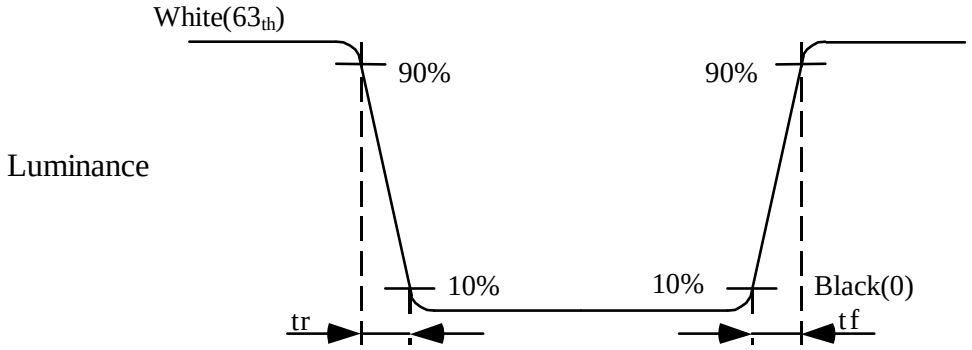


Fig. 6-2 Measuring point

Note 4: Definition of Viewing Angle(θ,ψ),refer to Fig.6 as below：

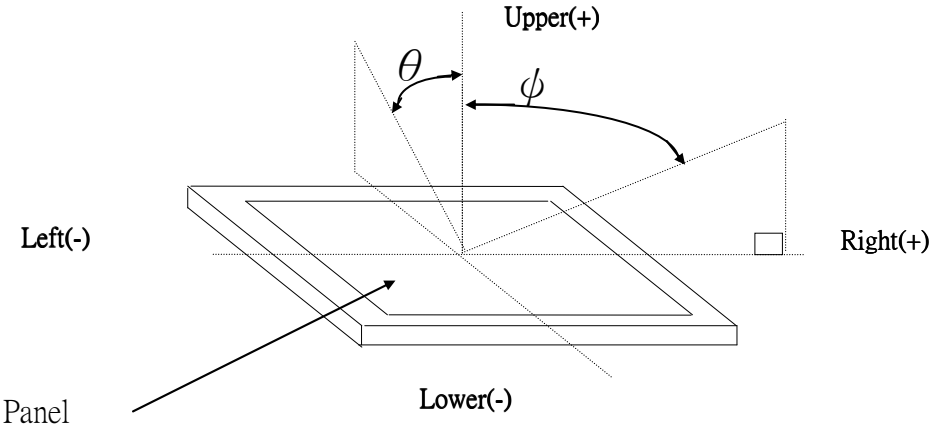


Fig.6-3 Definition of Viewing Angle

7. RELIABILITY TEST

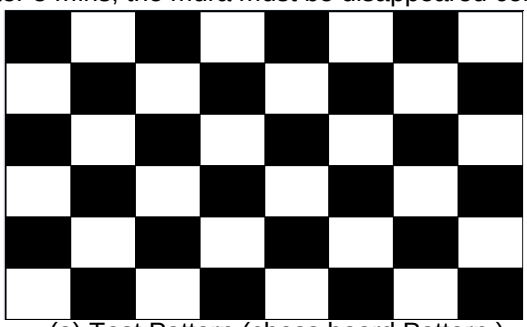
(These tests are conducted with CPT backlight.)

7.1 Temperature and Humidity

TEST ITEMS	CONDITIONS	NOTE
High Temperature Operation	70℃ ; 240hrs	
High Temperature Storage	80℃; 240hrs	
High Temperature High Humidity Operation	60℃ ; 90%RH ; 240hrs (No condensation)	
Low Temperature Operation	-20℃ ; 240hrs	
Low Temperature Storage	-30℃ ; 240hrs	
Thermal Shock	–20℃(0.5hr) ~ 70℃(0.5hr) ; 200 Cycles	Non-Operating
Image Sticking	25℃ ; 4hrs	1
MTBF	200,00hrs	

Note 1: Condition of Image Sticking test：25 ℃± 2 ℃

Operation with test pattern sustained for 4 hrs, then change to gray pattern immediately.
After 5 mins, the mura must be disappeared completely .



(a) Test Pattern (chess board Pattern)

(b) Gray Pattern

7.2 Shock and Vibration

ITEMS	CONDITIONS
Shock (Non-Operation)	● Shock level： 980m/s^2(equal to 100G). ● Waveform： 1/2 Sine wave,6msec ● ±X，±Y，±Z，each axis 1 times
Vibration (Non-Operation)	● Frequency range：8~33.3Hz ● Stoke：1.3 mm ● Vibration： sinusoidal wave, perpendicular axis (both x, z axis:2Hrs, y axis 4Hrs). ● Sweep： 2.9G, 33.3 Hz -400 Hz ● Cycle： 15 min

7.3 Electrostatic Discharge

TEST ITEM	CONDITIONS	NOTE
ESD	150pF，330Ω，±8kV&±15kV Air& Contact test	1
	200pF，0Ω，±200V Contact test	2

Note: Measure point :

1. LCD glass and metal bezel
2. IF connector pins

7.4 Judgment Standard

The Judgment of the above test should be made as follow:

Pass：Normal display image and no line defect.

Partial transformation of the module parts should be ignored.

Fail： No display image, function NG, or line defects.

8. PACKING

TBD

9. WARRANTY

9.1 The period is within 12 months since the date of shipping out under normal using and storage conditions.

9.2 The warranty will be avoided in case of defect induced by customer.