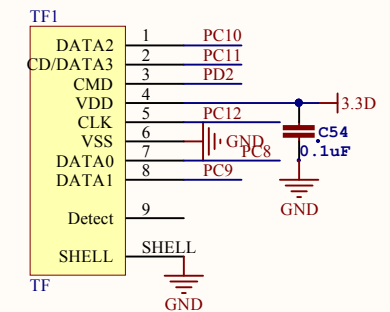
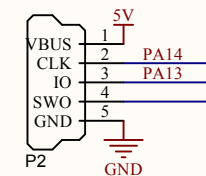
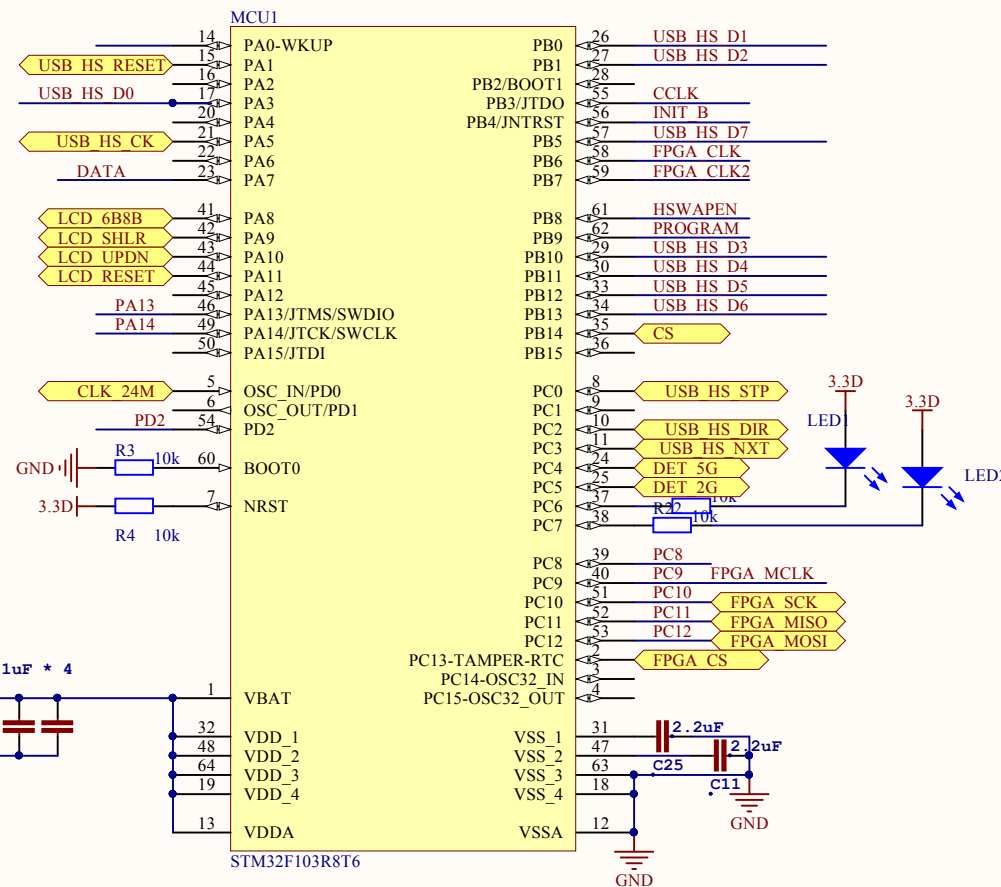
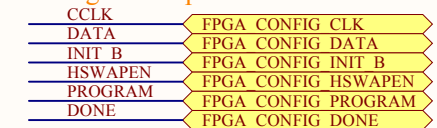




Date:	2019/7/21
File:	C:\Hardware\VNA\LCD.SchDoc



configuration ports



data ports



Title		
Size A4	Number	Revision
Date:	2019/4/21	Sheet of
File:	C:\Hardware\VNA\mcu.SchDoc	Drawn By:

1

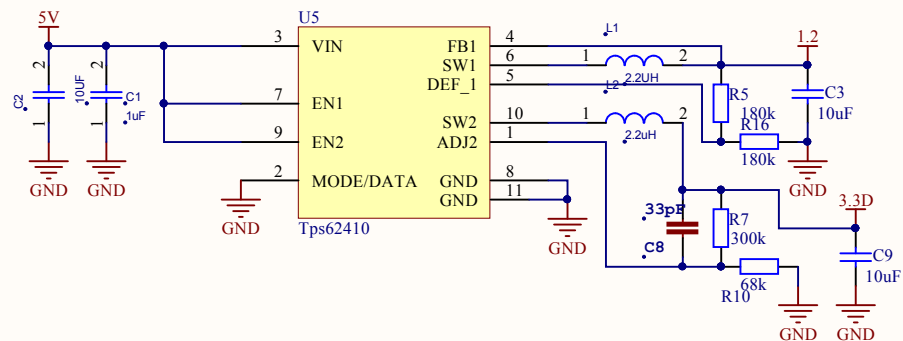
2

3

4

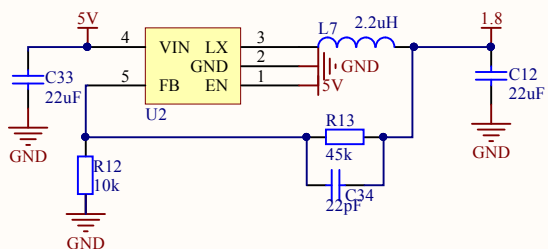
A

A



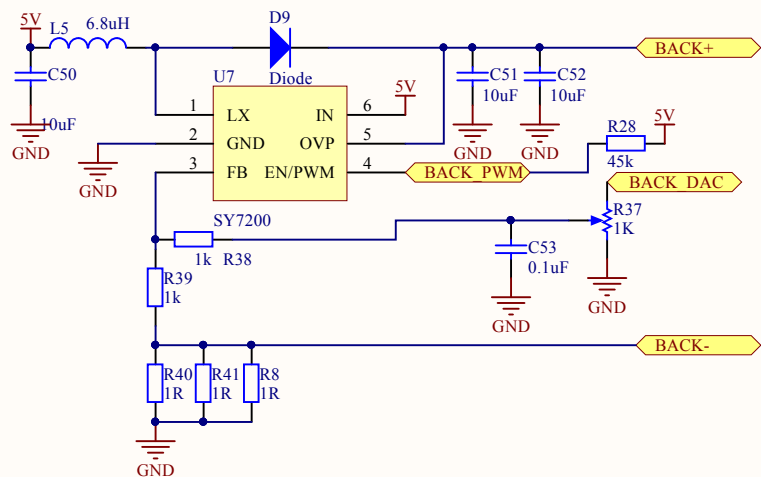
B

B



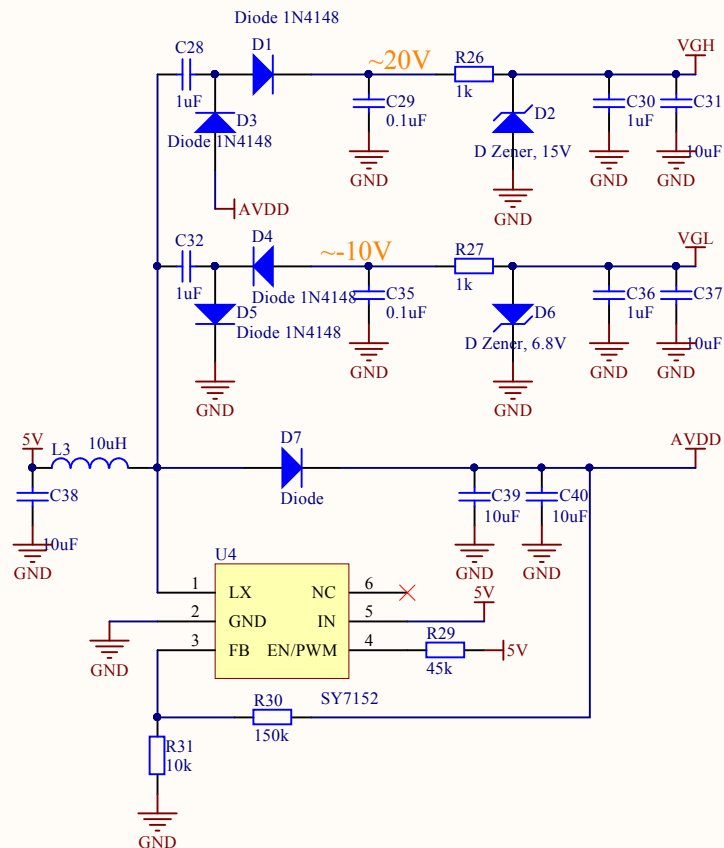
C

C



D

D



Title		
Size	Number	Revision
A4		
Date:	2019/4/21	Sheet of
File:	C:\Hardware\VNA\power.SchDoc	Drawn By:

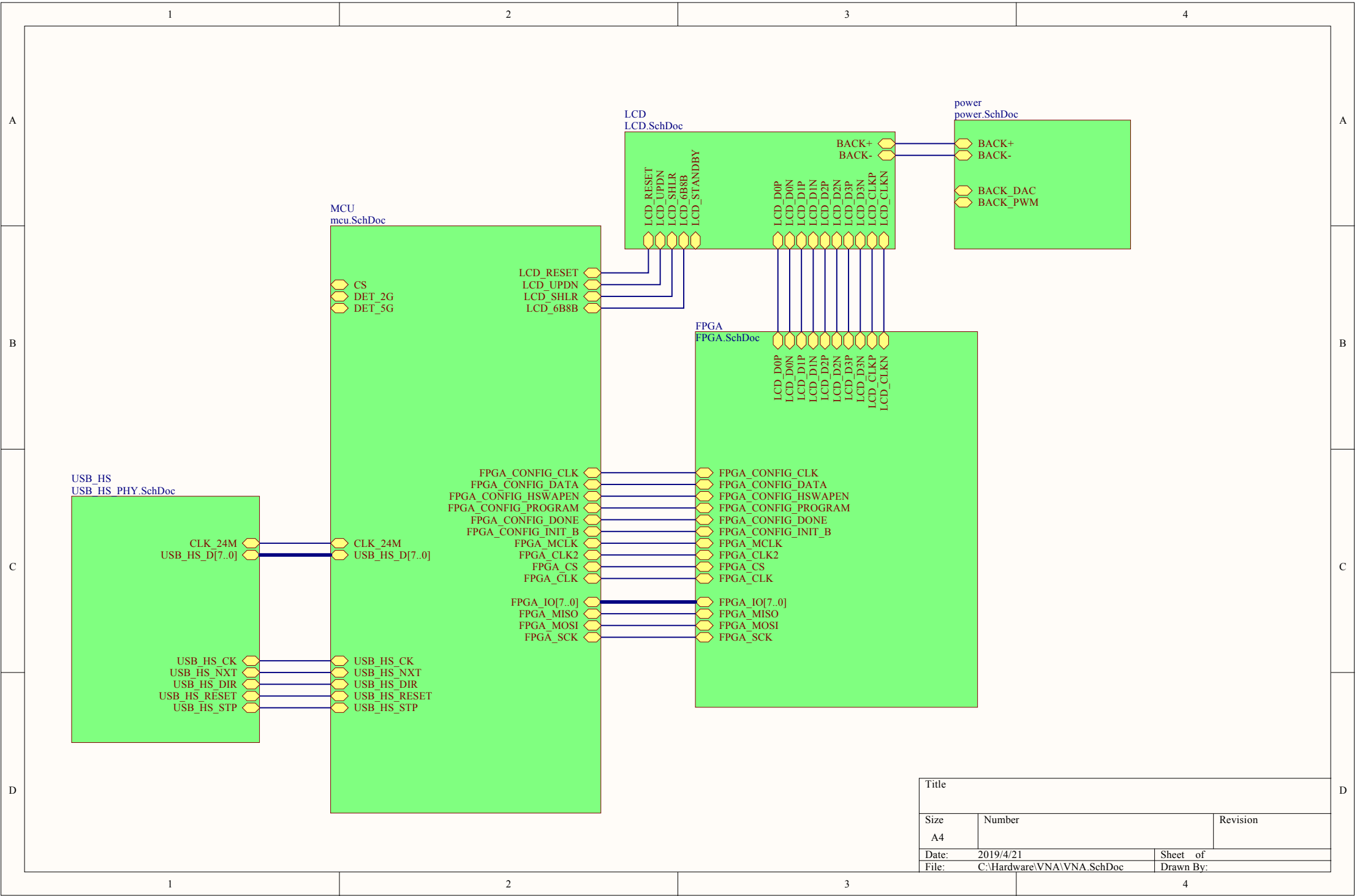
1

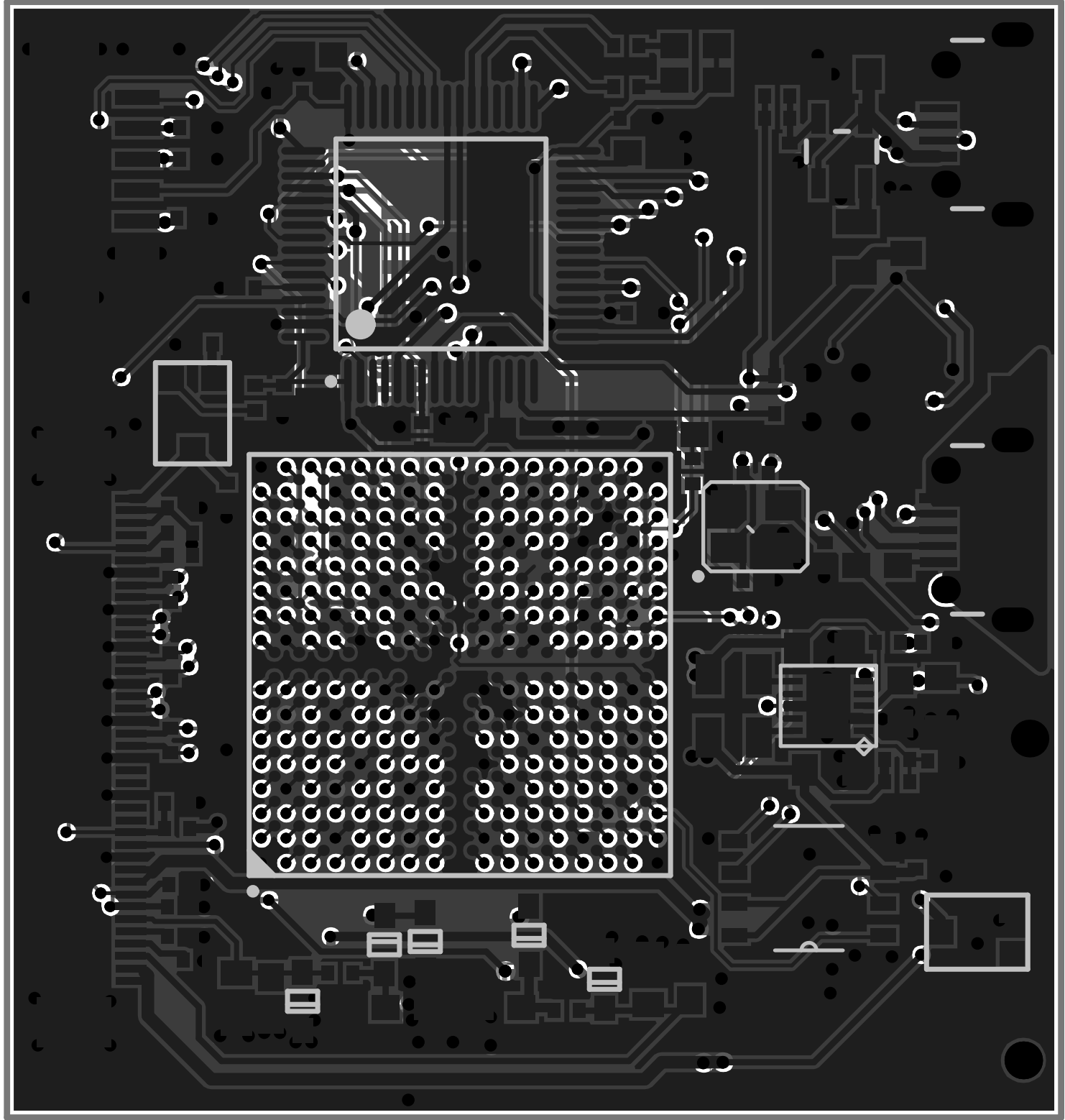
2

3

4







Board St